

Applications

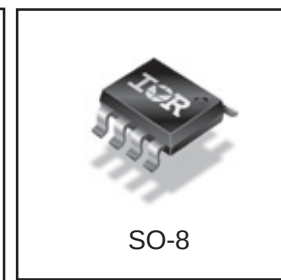
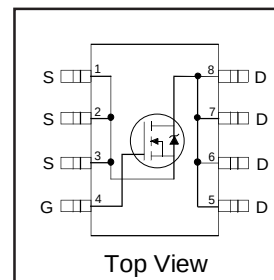
- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Telecom and Industrial Use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

Benefits

- Ultra-Low Gate Impedance
- Very Low $R_{DS(on)}$ at 4.5V V_{GS}
- Fully Characterized Avalanche Voltage and Current

HEXFET® Power MOSFET

V_{DSS}	$R_{DS(on)}$ max(m Ω)	I_D
40V	15.5@$V_{GS} = 10V$	9.4A



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-to-Source Voltage	± 12	V
I_D @ $T_A = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	9.4	A
I_D @ $T_A = 70^\circ C$	Continuous Drain Current, V_{GS} @ 10V	7.5	
I_{DM}	Pulsed Drain Current①	75	
P_D @ $T_A = 25^\circ C$	Maximum Power Dissipation	2.5	W
P_D @ $T_A = 70^\circ C$	Maximum Power Dissipation	1.6	W
	Linear Derating Factor	0.02	mW/ $^\circ C$
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	$^\circ C$

Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JL}$	Junction-to-Drain Lead	—	20	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient ④	—	50	

Notes ① through ⑤ are on page 8
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Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	40	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.025	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = 1mA$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	11.7	15.5	m Ω	$V_{GS} = 10V, I_D = 9.4A$ ④
		—	13.0	17.0		$V_{GS} = 4.5V, I_D = 7.5A$ ④
		—	18.0	35.0		$V_{GS} = 4.5V, I_D = 4.7A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	0.8	—	2.0	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	$V_{DS} = 32V, V_{GS} = 0V$
		—	—	100		$V_{DS} = 32V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	$V_{GS} = 12V$
	Gate-to-Source Reverse Leakage	—	—	-200		$V_{GS} = -12V$

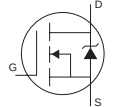
Dynamic @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g_{fs}	Forward Transconductance	27	—	—	S	$V_{DS} = 20V, I_D = 8.0A$
Q_g	Total Gate Charge	—	23	34	nC	$I_D = 8.0A$
Q_{gs}	Gate-to-Source Charge	—	6.4	9.6		$V_{DS} = 20V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	6.7	10		$V_{GS} = 4.5V$, ③
Q_{oss}	Output Gate Charge	—	17	26		$V_{GS} = 0V, V_{DS} = 16V$
$t_{d(on)}$	Turn-On Delay Time	—	7.6	—	ns	$V_{DD} = 20V$
t_r	Rise Time	—	2.3	—		$I_D = 8.0A$
$t_{d(off)}$	Turn-Off Delay Time	—	20	—		$R_G = 1.8\Omega$
t_f	Fall Time	—	3.8	—		$V_{GS} = 4.5V$ ③
C_{iss}	Input Capacitance	—	2460	—	pF	$V_{GS} = 0V$
C_{oss}	Output Capacitance	—	490	—		$V_{DS} = 20V$
C_{rss}	Reverse Transfer Capacitance	—	38	—		$f = 1.0MHz$

Avalanche Characteristics

Symbol	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy②	—	160	mJ
I_{AR}	Avalanche Current①	—	8.0	A

Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	2.3	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	74		
V_{SD}	Diode Forward Voltage	—	0.81	1.3	V	$T_J = 25^\circ\text{C}, I_S = 8.0A, V_{GS} = 0V$ ③
		—	0.65	—		$T_J = 125^\circ\text{C}, I_S = 8.0A, V_{GS} = 0V$ ③
t_{rr}	Reverse Recovery Time	—	45	68	ns	$T_J = 25^\circ\text{C}, I_F = 8.0A, V_R = 20V$
Q_{rr}	Reverse Recovery Charge	—	76	110	nC	$di/dt = 100A/\mu s$ ③
t_{rr}	Reverse Recovery Time	—	58	87	ns	$T_J = 125^\circ\text{C}, I_F = 8.0A, V_R = 20V$
Q_{rr}	Reverse Recovery Charge	—	110	160	nC	$di/dt = 100A/\mu s$ ③

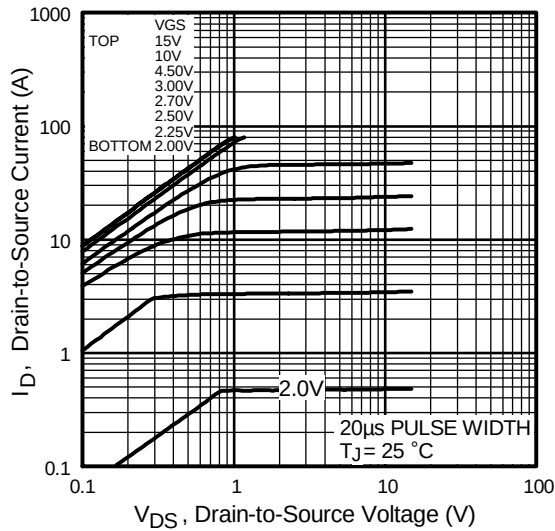


Fig 1. Typical Output Characteristics

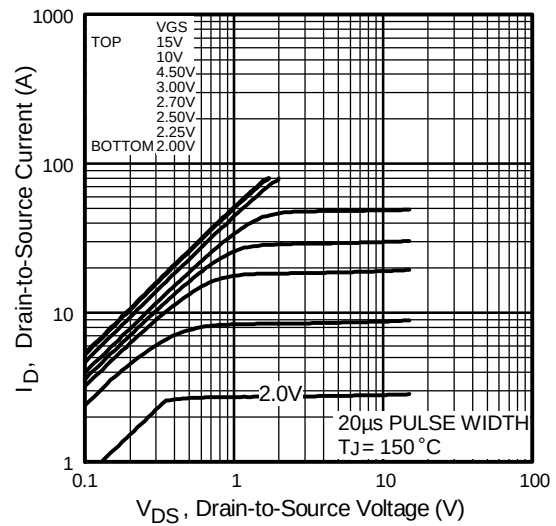


Fig 2. Typical Output Characteristics

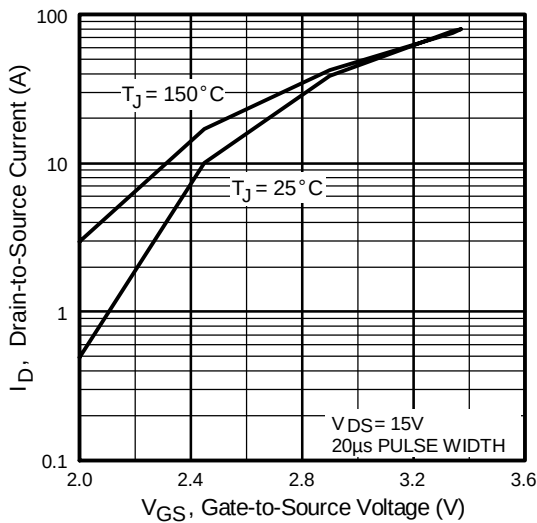


Fig 3. Typical Transfer Characteristics

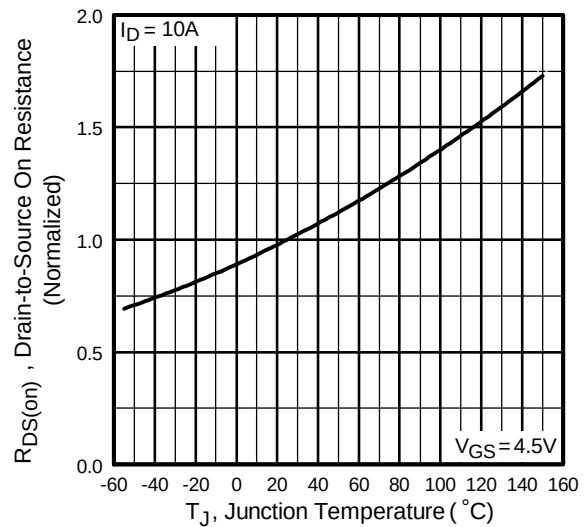


Fig 4. Normalized On-Resistance Vs. Temperature

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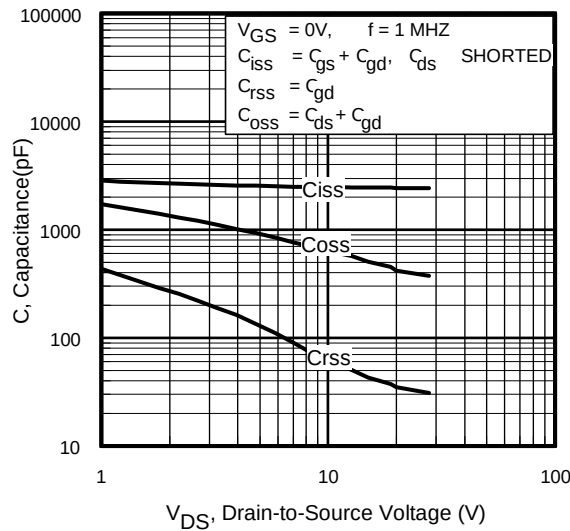


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

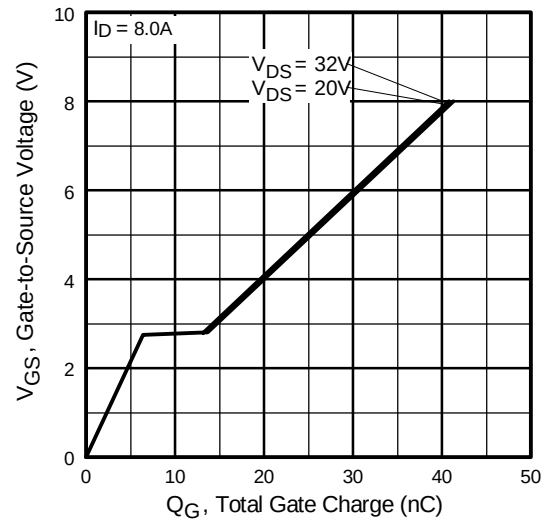


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

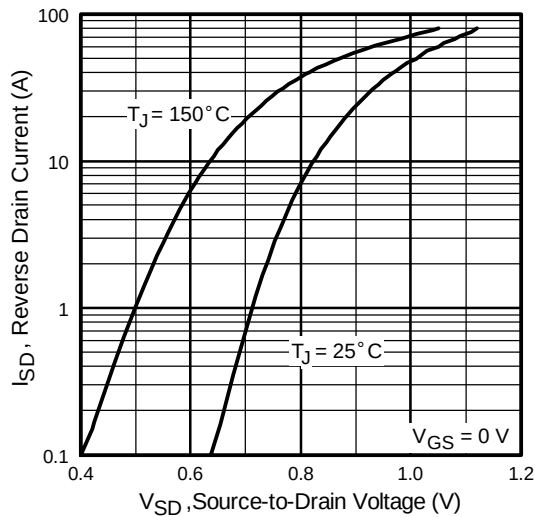


Fig 7. Typical Source-Drain Diode Forward Voltage

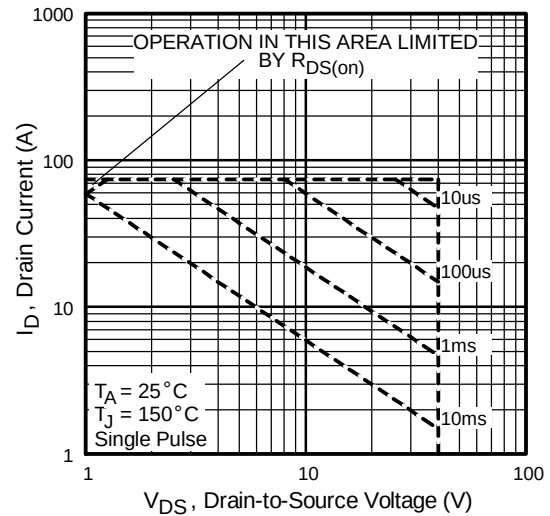


Fig 8. Maximum Safe Operating Area

Fig 6. On-Resistance Vs. Drain Current

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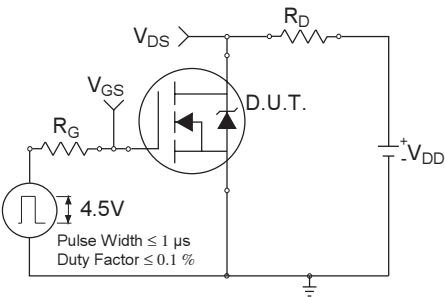
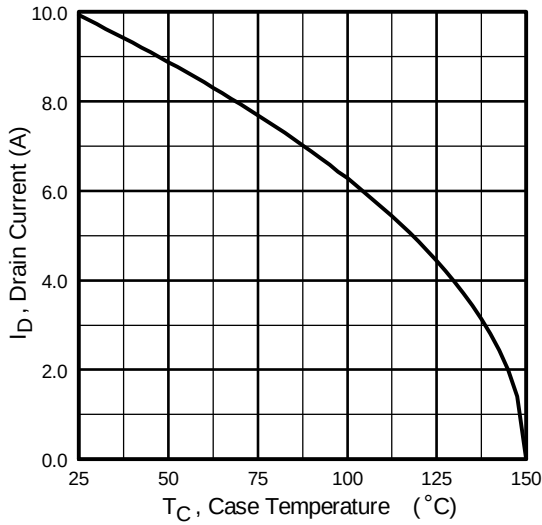


Fig 10a. Switching Time Test Circuit

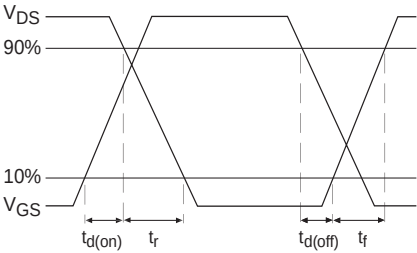


Fig 10b. Switching Time Waveforms

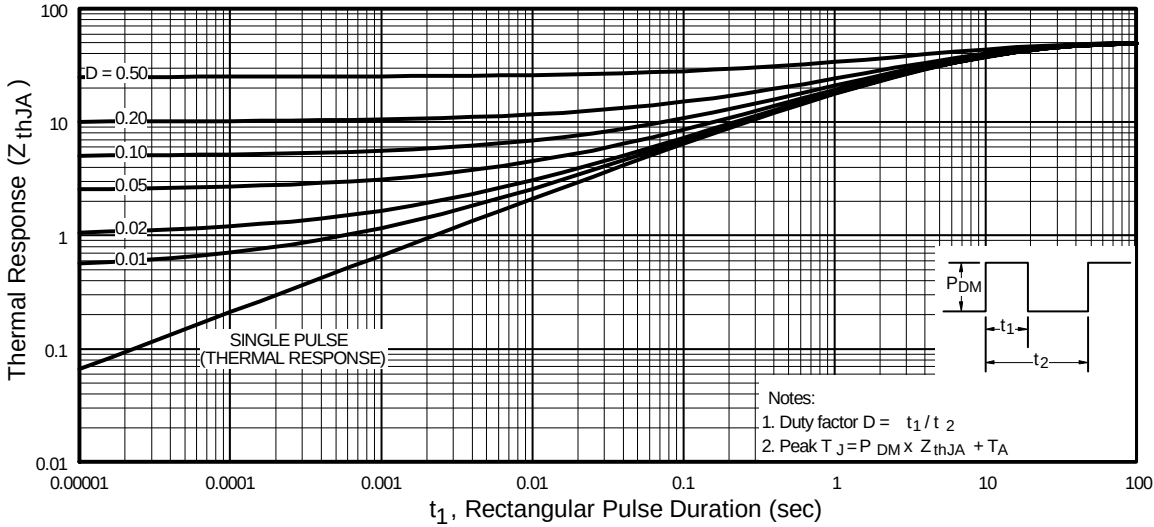


Fig 10. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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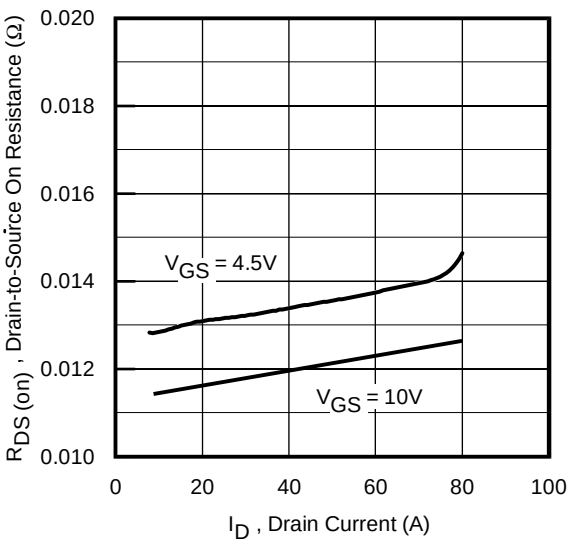


Fig 12. On-Resistance Vs. Drain Current

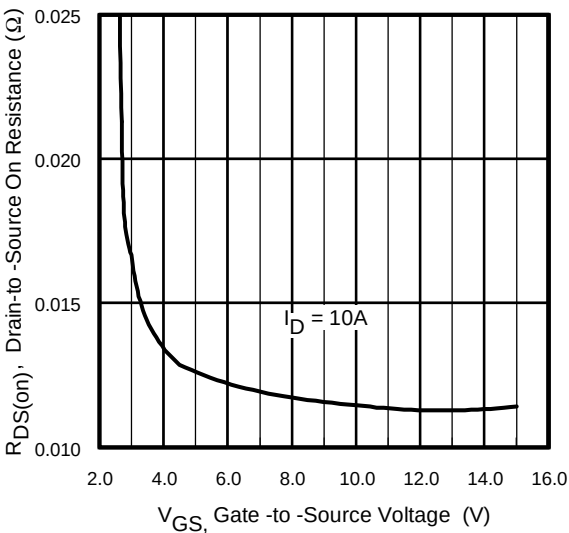


Fig 13. On-Resistance Vs. Gate Voltage

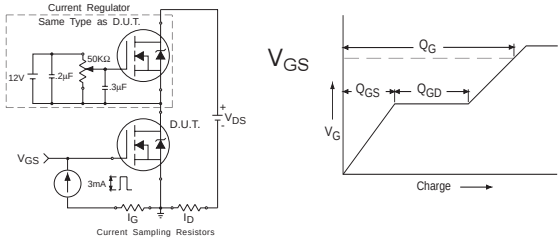


Fig 13a&b. Basic Gate Charge Test Circuit and Waveform

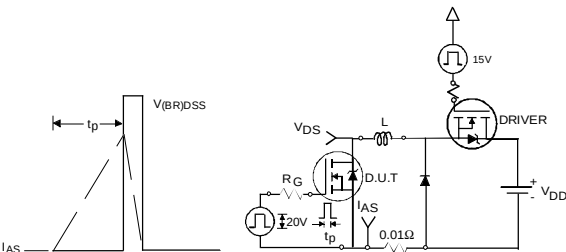


Fig 14a&b. Unclamped Inductive Test circuit and Waveforms

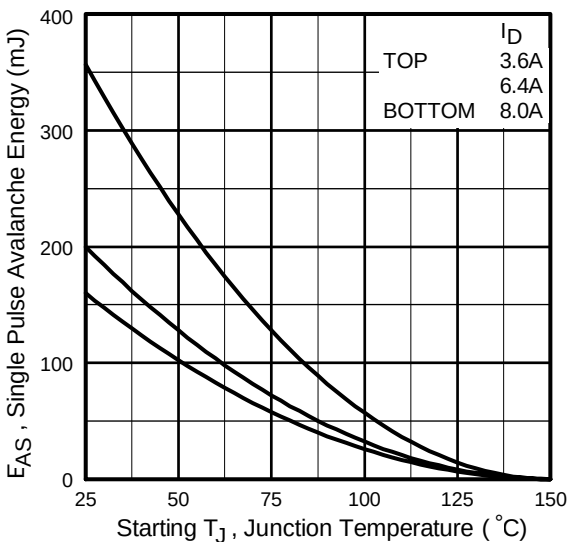
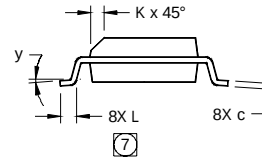
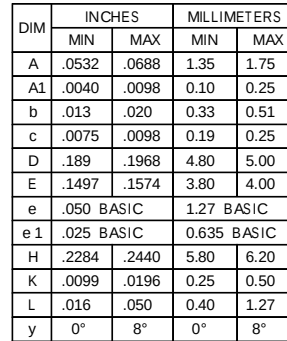


Fig 14c. Maximum Avalanche Energy Vs. Drain Current

Dimensions are shown in millimeters (inches)



1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.

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EXAMPLE: THIS IS AN IRF7101 (MOSFET)

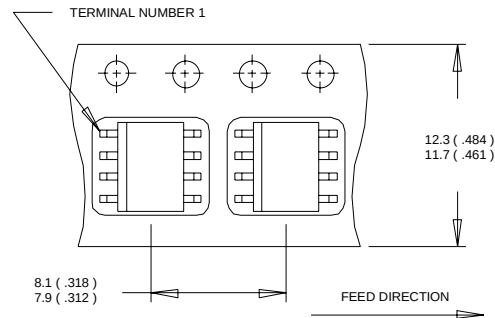


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SO-8 Tape and Reel

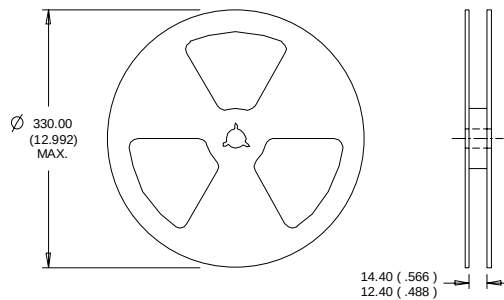
Dimensions are shown in millimeters (inches)

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NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 5.0\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 8.0\text{A}$.
- ③ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ When mounted on 1 inch square copper board, $t < 10\text{ sec}$

Data and specifications subject to change without notice.
This product has been designed and qualified for the Consumer market.
Qualifications Standards can be found on IR's Web site.

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