TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC3W02F, TC3W02FU

2-TO-3 LINE DECODER WITH ENABLE

The TC3W02 is a high speed C²MOS 2 to 3 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

When the enable input is held "H", all three outputs are fixed at a high logic level independent of the other inputs.

All inputs are equipped with protection circuits against static discharge or trasient excess voltage.

FEATURES

\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	•	High Speed		$t_{pd} = 16ns$ (Typ.) at
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• Low Power Dissipation
$$I_{CC} = 2\mu A$$
 (Max.) at $Ta = 25^{\circ}C$

• High Noise Immunity
$$V_{NIH} = V_{NIL} = 28\%$$
, V_{CC} (Min.)

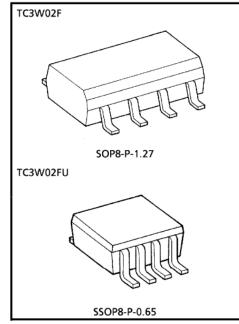
Balanced Propagation Delays t_{pLH}≒t_{pHL}

Wide Operating Voltage Range ... V_{CC} (opr) = 2∼6V

TRUTH TABLE

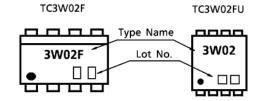
	INPUTS			OUTPUT:	SELECTED					
ENABLE	SELECT		<u> </u>	<u>72</u>	Y3	OUTPUT				
G	В	Α	11	12	13	OUTPUT				
Н	××		Н	Н	Н	NONE				
L	L	L	Н	Н	Н	NONE				
L	L	Н	L	Н	Н	<u>Y1</u>				
L	H L		Н	L	Н	Y2				
L	Н	н н		Н	L	\overline{Y3}				

x : Don't care

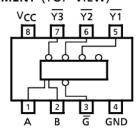


Weight SOP8-P-1.27 : 0.05g (Typ.) SSOP8-P-0.65 : 0.02g (Typ.)

MARKING



PIN ASSIGNMENT (TOP VIEW)



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	VIN	-0.5~V _{CC} +0.5	٧
DC Output Voltage	Vout	-0.5~V _{CC} +0.5	٧
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	lok	± 20	mΑ
DC Output Current	IOUT	± 25	mA
DC V _{CC} / Ground Current	Icc	± 25	mA
Power Dissipation	PD	300	mW
Storage Temperature	T _{stg}	- 65∼150	°C
Lead Temperature (10s)	TL	260	°C

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V _{CC}	٧
Output Voltage	Vout	0~V _{CC}	٧
Operating Temperature	T _{opr}	-40~85	°C
		$0 \sim 1000 \text{ (V}_{CC} = 2.0\text{V)}$	
Input Rise and Fall Time	t _r , t _f	0~ 500 (V _{CC} = 4.5V)	ns
		$0 \sim 400 \ (V_{CC} = 6.0V)$	

2

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST	CONDITION		Ta = 25°C		C	Ta = -4	UNIT	
CHARACTERISTIC	3 T IVIBOL	TEST CONDITION		Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	OIVII
High-Level Input Voltage	V _{IH}		_	2.0 4.5 6.0	1.5 3.15 4.2		_	1.5 3.15 4.2		<
Low-Level Input Voltage	VIL		_	2.0 4.5 6.0	_ _ _	_ _ _	0.5 1.35 1.8	_ _ _	0.5 1.35 1.8	V
High-Level Output Voltage	V _{ОН}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20\mu A$ $I_{OH} = -4mA$	2.0 4.5 6.0 4.5	1.9 4.4 5.9 4.18	2.0 4.5 6.0 4.31	_ _ _	1.9 4.4 5.9 4.13	_ 	>
Low-Level Output Voltage	V _{OL}	V _{IN} = V _{IH}	$I_{OH} = -5.2 \text{mA}$ $I_{OL} = 20 \mu \text{A}$	6.0 2.0 4.5 6.0	5.68 — — —	5.80 0.0 0.0 0.0	0.1 0.1 0.1	5.63 — — —	0.1 0.1 0.1	V
Input Leakage			I _{OL} = 4mA I _{OL} = 5.2mA	4.5 6.0 6.0	_	0.17 0.18	0.26		0.33	
Current Quiescent Supply Current	ICC	$V_{IN} = V_{CC}$		6.0	_	_	± 0.1	_	± 1.0	μΑ

AC ELECTRICAL CHARACTERISTICS ($C_L = 15pF$, $V_{CC} = 5V$, Ta = 25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time	t _{TLH} t _{THL}	_	_	4	8	
Propagation Delay Time (A, B- \overline{Y})	t _{pLH} t _{pHL}	_	_	12	22	ns
Propagation Delay Time (\overline{G} - \overline{Y})	t _{pLH} t _{pHL}	_	_	10	18	

AC ELECTRICAL CHARACTERISTICS ($C_L = 50pF$, Input $t_f = t_f = 6ns$)

PARAMETER	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
FARAIVIETER	STIVIBOL	TEST CONDITION	Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Output Transition	+		2.0	_	30	75	_	95	
Time	t _{TLH}	_	4.5	l —	8	15	l —	19	
Time	tTHL		6.0	—	7	13	—	16	
Propagation Delay	+		2.0	_	45	130	_	165	
Time (A, B- \overline{Y})	t _{pLH} t _{pHL}	_	4.5	l —	15	26	—	33	ns
Time (A, B-1)			6.0	_	13	22	_	28	
Propagation Delay	+		2.0	_	39	110	_	140	
Time $(\overline{G}-\overline{Y})$	t _{pLH}	_	4.5	l —	13	22	_	28	
Time (G-1)	t _{pHL}		6.0	 	11	19	—	24	
Input Capacitance	C _{IN}	_		_	5	10	_	10	
Power Dissipation Capacitance	C _{PD}	(Note 1)		_	46	_	_	_	pF

Note 1 : CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

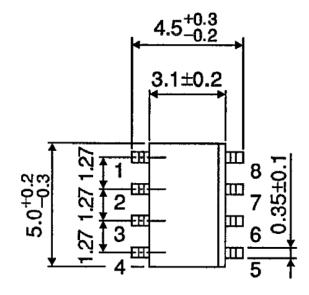
ICC (opr) = CpD·VCC·fIN+ICC

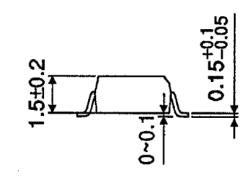
4 2008-06-03

PACKAGE DIMENSIONS

SOP8-P-1.27

Unit: mm





Weight: 0.05g (Typ.)

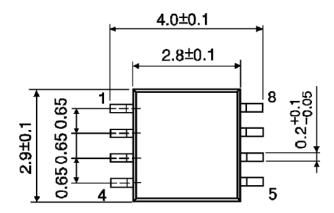
5

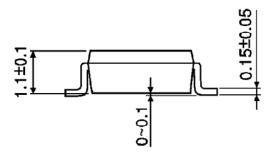
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PACKAGE DIMENSIONS

SSOP8-P-0.65

Unit: mm





6

Weight: 0.02g (Typ.)

2008-06-03

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20070701-EN GENERAL

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