

74AC139 • 74ACT139

Dual 1-of-4 Decoder/Demultiplexer

General Description

The AC/ACT139 is a high-speed, dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually-exclusive active-LOW outputs. Each decoder has an active-LOW Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the AC/ACT139 can be used as a function generator providing all four minterms of two variables.

Features

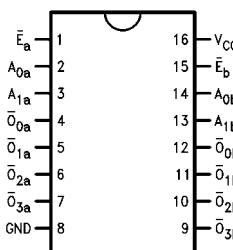
- I_{CC} reduced by 50%
- Multifunction capability
- Two completely independent 1-of-4 decoders
- Active LOW mutually exclusive outputs
- Outputs source/sink 24 mA
- ACT139 has TTL-compatible inputs

Ordering Code:

| Order Number | Package Number | Package Description |
|--------------|----------------|---|
| 74AC139SC | M16A | 16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Body |
| 74AC139SJ | M16D | 16-Lead Small Outline Package (SOIC), EIAJ Type II, 5.3mm Wide |
| 74AC139MTC | MTC16 | 16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide |
| 74AC139PC | N16E | 16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide |
| 74ACT139SC | M16A | 16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Body |
| 74ACT139SJ | M16D | 16-Lead Small Outline Package (SOIC), EIAJ Type II, 5.3mm Wide |
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| 74ACT139PC | N16E | 16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide |

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code.

Connection Diagram

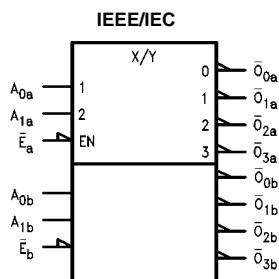
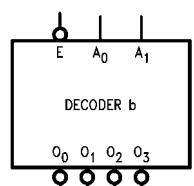
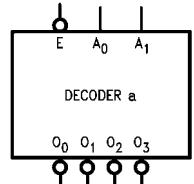


Pin Descriptions

| Pin Names | Description |
|---------------------------------|----------------|
| A ₀ , A ₁ | Address Inputs |
| Ē | Enable Inputs |
| O ₀ –O ₃ | Outputs |

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Logic Symbols



Truth Table

| Inputs | | | Outputs | | | |
|-----------|-------|-------|-------------|-------------|-------------|-------------|
| \bar{E} | A_0 | A_1 | \bar{O}_0 | \bar{O}_1 | \bar{O}_2 | \bar{O}_3 |
| H | X | X | H | H | H | H |
| L | L | L | L | H | H | H |
| L | H | L | H | L | H | H |
| L | L | H | H | H | L | H |
| L | H | H | H | H | H | L |

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immortal

Functional Description

The AC/ACT139 is a high-speed dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs (A_0 – A_1) and provides four mutually exclusive active-LOW outputs (\bar{O}_0 – \bar{O}_3). Each decoder has an active-LOW enable (\bar{E}). When \bar{E} is HIGH all outputs are forced HIGH. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the AC/ACT139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in Figure 1, and thereby reducing the number of packages required in a logic network.

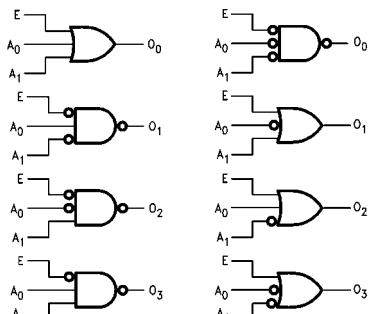
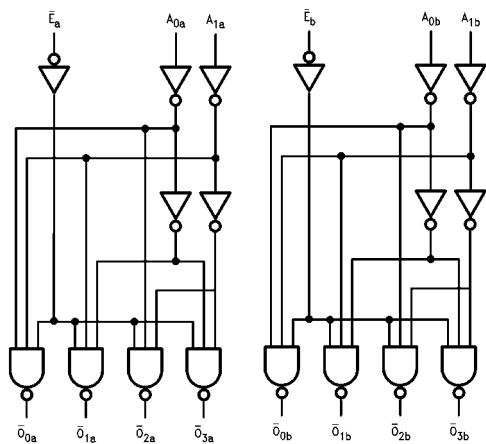


FIGURE 1. Gate Functions (Each Half)

Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings(Note 1)

| | | |
|--|--------------------------|---|
| Supply Voltage (V_{CC}) | -0.5V to +7.0V | |
| DC Input Diode Current (I_{IK}) | | |
| $V_I = -0.5V$ | -20 mA | AC |
| $V_I = V_{CC} + 0.5V$ | +20 mA | ACT |
| DC Input Voltage (V_I) | -0.5V to $V_{CC} + 0.5V$ | |
| DC Output Diode Current (I_{OK}) | | |
| $V_O = -0.5V$ | -20 mA | Input Voltage (V_I) |
| $V_O = V_{CC} + 0.5V$ | +20 mA | Output Voltage (V_O) |
| DC Output Voltage (V_O) | -0.5V to $V_{CC} + 0.5V$ | Operating Temperature (T_A) |
| DC Output Source or Sink Current (I_O) | ± 50 mA | Minimum Input Edge Rate ($\Delta V/\Delta t$) |
| DC V_{CC} or Ground Current per Output Pin (I_{CC} or I_{GND}) | ± 50 mA | AC Devices |
| Storage Temperature (T_{STG}) | -65°C to +150°C | V_{IN} from 30% to 70% of V_{CC} |
| Junction Temperature (T_J) | | $V_{CC} @ 3.3V, 4.5V, 5.5V$ |
| PDIP | 140°C | Operating Temperature (T_A) |

Recommended Operating Conditions

| | | |
|--|--------------------------|---|
| Supply Voltage (V_{CC}) | AC | 2.0V to 6.0V |
| DC Input Diode Current (I_{IK}) | ACT | 4.5V to 5.5V |
| $V_I = -0.5V$ | | |
| $V_I = V_{CC} + 0.5V$ | | |
| DC Input Voltage (V_I) | -0.5V to $V_{CC} + 0.5V$ | |
| DC Output Diode Current (I_{OK}) | | |
| $V_O = -0.5V$ | -20 mA | Input Voltage (V_I) |
| $V_O = V_{CC} + 0.5V$ | +20 mA | Output Voltage (V_O) |
| DC Output Voltage (V_O) | -0.5V to $V_{CC} + 0.5V$ | Operating Temperature (T_A) |
| DC Output Source or Sink Current (I_O) | ± 50 mA | Minimum Input Edge Rate ($\Delta V/\Delta t$) |
| DC V_{CC} or Ground Current per Output Pin (I_{CC} or I_{GND}) | ± 50 mA | AC Devices |
| Storage Temperature (T_{STG}) | -65°C to +150°C | V_{IN} from 0.8V to 2.0V |
| Junction Temperature (T_J) | | $V_{CC} @ 4.5V, 5.5V$ |
| PDIP | 140°C | Operating Temperature (T_A) |

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation of FACT™ circuits outside databook specifications.

DC Electrical Characteristics for AC

| Symbol | Parameter | V_{CC} (V) | $T_A = +25^\circ C$ | | Units | Conditions |
|-------------------|---|--------------|---------------------|-------------------|---------|--------------------------------------|
| | | | Typ | Guaranteed Limits | | |
| V_{IH} | Minimum HIGH Level Input Voltage | 3.0 | 1.5 | 2.1 | V | $V_{OUT} = 0.1V$ |
| | | 4.5 | 2.25 | 3.15 | | or $V_{CC} - 0.1V$ |
| | | 5.5 | 2.75 | 3.85 | | |
| | Maximum LOW Level Input Voltage | 3.0 | 1.5 | 0.9 | V | $V_{OUT} = 0.1V$ |
| | | 4.5 | 2.25 | 1.35 | | or $V_{CC} - 0.1V$ |
| | | 5.5 | 2.75 | 1.65 | | |
| V_{OH} | Minimum HIGH Level Output Voltage | 3.0 | 2.99 | 2.9 | V | $I_{OUT} = -50 \mu A$ |
| | | 4.5 | 4.49 | 4.4 | | |
| | | 5.5 | 5.49 | 5.4 | | |
| | | 3.0 | | 2.56 | V | $V_{IN} = V_{IL} \text{ or } V_{IH}$ |
| | | 4.5 | | 3.86 | | $I_{OH} = -12 mA$ |
| | | 5.5 | | 4.86 | | $I_{OH} = -24 mA$ |
| V_{OL} | Maximum LOW Level Output Voltage | 3.0 | 0.002 | 0.1 | V | $I_{OUT} = 50 \mu A$ |
| | | 4.5 | 0.001 | 0.1 | | |
| | | 5.5 | 0.001 | 0.1 | | |
| | | 3.0 | | 0.36 | V | $V_{IN} = V_{IL} \text{ or } V_{IH}$ |
| | | 4.5 | | 0.36 | | $I_{OL} = 12 mA$ |
| | | 5.5 | | 0.36 | | $I_{OL} = 24 mA$ |
| I_{IN} (Note 4) | Maximum Input Leakage Current | 5.5 | | ± 0.1 | μA | $V_I = V_{CC}, GND$ |
| | Minimum Dynamic Output Current (Note 3) | 5.5 | | | mA | $V_{OLD} = 1.65V$ Max |
| | | 5.5 | | | mA | $V_{OHD} = 3.85V$ Min |
| | Maximum Quiescent Supply Current | 5.5 | | 4.0 | 40.0 | μA |
| I_{CC} (Note 4) | | | | | | $V_{IN} = V_{CC} \text{ or } GND$ |

Note 2: All outputs loaded; thresholds on input associated with output under test.

Note 3: Maximum test duration 2.0 ms, one output loaded at a time.

Note 4: I_{IN} and I_{CC} @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V V_{CC} .

DC Electrical Characteristics for ACT

| Symbol | Parameter | V _{CC} (V) | T _A = +25°C | | T _A = -40°C to +85°C Guaranteed Limits | Units | Conditions |
|------------------|--|------------------------|------------------------|--------------|--|-------|---|
| | | | Typ | | | | |
| V _{IH} | Minimum HIGH Level Input Voltage | 4.5 5.5 | 1.5 1.5 | 2.0 2.0 | 2.0 2.0 | V | V _{OUT} = 0.1V or V _{CC} - 0.1V |
| V _{IL} | Maximum LOW Level Input Voltage | 4.5 5.5 | 1.5 1.5 | 0.8 0.8 | 0.8 0.8 | V | V _{OUT} = 0.1V or V _{CC} - 0.1V |
| V _{OH} | Minimum HIGH Level Output Voltage | 4.5 5.5 | 4.49 5.49 | 4.4 5.4 | 4.4 5.4 | V | I _{OUT} = -50 μA |
| | | 4.5 5.5 | | 3.86 4.86 | 3.76 4.76 | V | V _{IN} = V _{IL} or V _{IH} I _{OH} = -24 mA I _{OH} = -24 mA (Note 5) |
| | | 4.5 5.5 | 0.001 0.001 | 0.1 0.1 | 0.1 0.1 | V | I _{OUT} = 50 μA |
| V _{OL} | Maximum LOW Level Output Voltage | 4.5 5.5 | | 0.36 0.36 | 0.44 0.44 | V | V _{IN} = V _{IL} or V _{IH} I _{OL} = 24 mA I _{OL} = 24 mA (Note 5) |
| | | 4.5 5.5 | | 0.36 0.36 | 0.44 0.44 | V | |
| I _{IN} | Maximum Input Leakage Current | 5.5 | | ±0.1 | ±1.0 | μA | V _I = V _{CC} , GND |
| I _{CCT} | Maximum I _{CC} /Input | 5.5 | 0.6 | | 1.5 | mA | V _I = V _{CC} - 2.1V |
| I _{OLD} | Minimum Dynamic Output Current (Note 6) | 5.5 | | | 75 | mA | V _{OLD} = 1.65V Max |
| | | 5.5 | | | -75 | mA | V _{OLD} = 3.85V Min |
| I _{CC} | Maximum Quiescent Supply Current | 5.5 | | 4.0 | 40.0 | μA | V _{IN} = V _{CC} or GND |

Note 5: All outputs loaded; thresholds on input associated with output under test.

Note 6: Maximum test duration 2.0 ms, one output loaded at a time.

AC Electrical Characteristics for AC

| Symbol | Parameter | V _{CC} (V) (Note 7) | T _A = +25°C C _L = 50 pF | | | Units | |
|------------------|--|------------------------------------|---|------------|-------------|------------|--------------|
| | | | T _A = -40°C to +85°C C _L = 50 pF | | | | |
| | | | Min | Typ | Max | | |
| t _{PLH} | Propagation Delay A _n to \bar{O}_n | 3.3 5.0 | 4.0 3.0 | 8.0 6.5 | 11.5 8.5 | 3.5 2.5 | 13.0 9.5 |
| t _{PHL} | Propagation Delay A _n to \bar{O}_n | 3.3 5.0 | 3.0 2.5 | 7.0 5.5 | 10.0 7.5 | 2.5 2.0 | 11.0 8.5 |
| t _{PLH} | Propagation Delay \bar{E}_n to \bar{O}_n | 3.3 5.0 | 4.5 3.5 | 9.5 7.0 | 12.0 8.5 | 3.5 3.0 | 13.0 10.0 |
| t _{PHL} | Propagation Delay \bar{E}_n to \bar{O}_n | 3.3 5.0 | 4.0 2.5 | 8.0 6.0 | 10.0 7.5 | 3.0 2.5 | 11.0 8.5 |

Note 7: Voltage Range 3.3 is 3.3V ± 0.3V.

Voltage Range 5.0 is 5.0V ± 0.5V

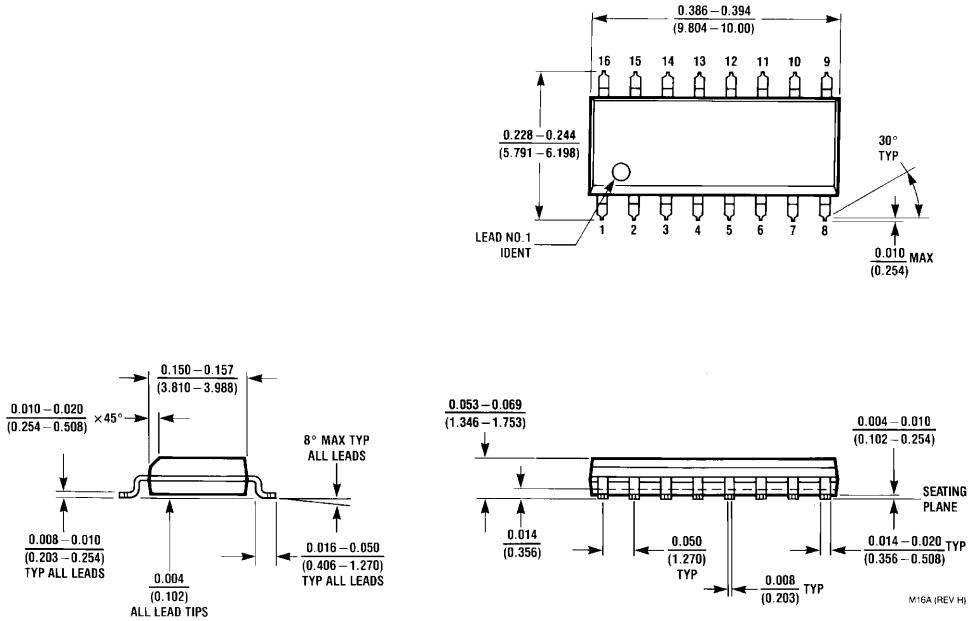
AC Electrical Characteristics for ACT

| Symbol | Parameter | V _{CC} (V) (Note 8) | T _A = +25°C C _L = 50 pF | | | T _A = -40°C to +85°C C _L = 50 pF | | Units |
|------------------|--|------------------------------------|--|-----|------|---|------|-------|
| | | | Min | Typ | Max | Min | Max | |
| t _{PLH} | Propagation Delay A _n to \bar{O}_n | 5.0 | 1.5 | 6.0 | 8.5 | 1.5 | 9.5 | ns |
| t _{PHL} | Propagation Delay A _n to \bar{O}_n | 5.0 | 1.5 | 6.0 | 9.5 | 1.5 | 10.5 | ns |
| t _{PLH} | Propagation Delay \bar{E}_n to \bar{O}_n | 5.0 | 2.5 | 7.0 | 10.0 | 2.0 | 11.0 | ns |
| t _{PHL} | Propagation Delay \bar{E}_n to \bar{O}_n | 5.0 | 2.0 | 7.0 | 9.5 | 1.5 | 10.5 | ns |

Note 8: Voltage Range 5.0 is 5.0V \pm 0.5V

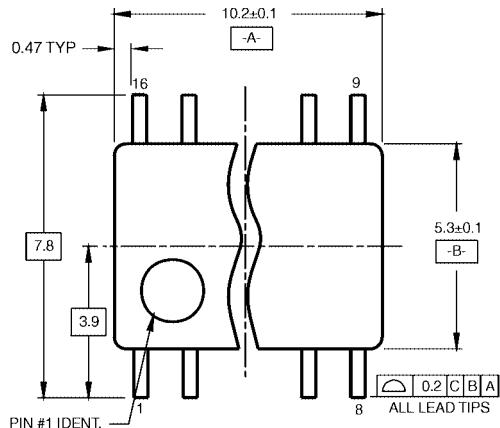
Capacitance

| Symbol | Parameter | Typ | Units | Conditions |
|-----------------|-------------------------------|------|-------|------------------------|
| C _{IN} | Input Capacitance | 4.5 | pF | V _{CC} = OPEN |
| C _{PD} | Power Dissipation Capacitance | 40.0 | pF | V _{CC} = 5.0V |

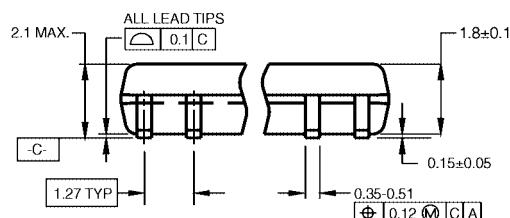
Physical Dimensions inches (millimeters) unless otherwise noted

16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Body
Package Number M16A

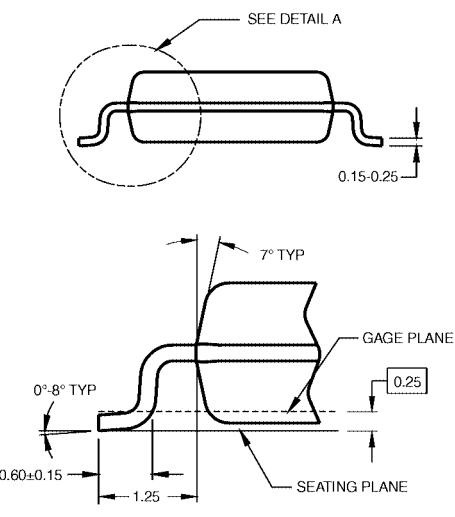
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



LAND PATTERN RECOMMENDATION



DIMENSIONS ARE IN MILLIMETERS



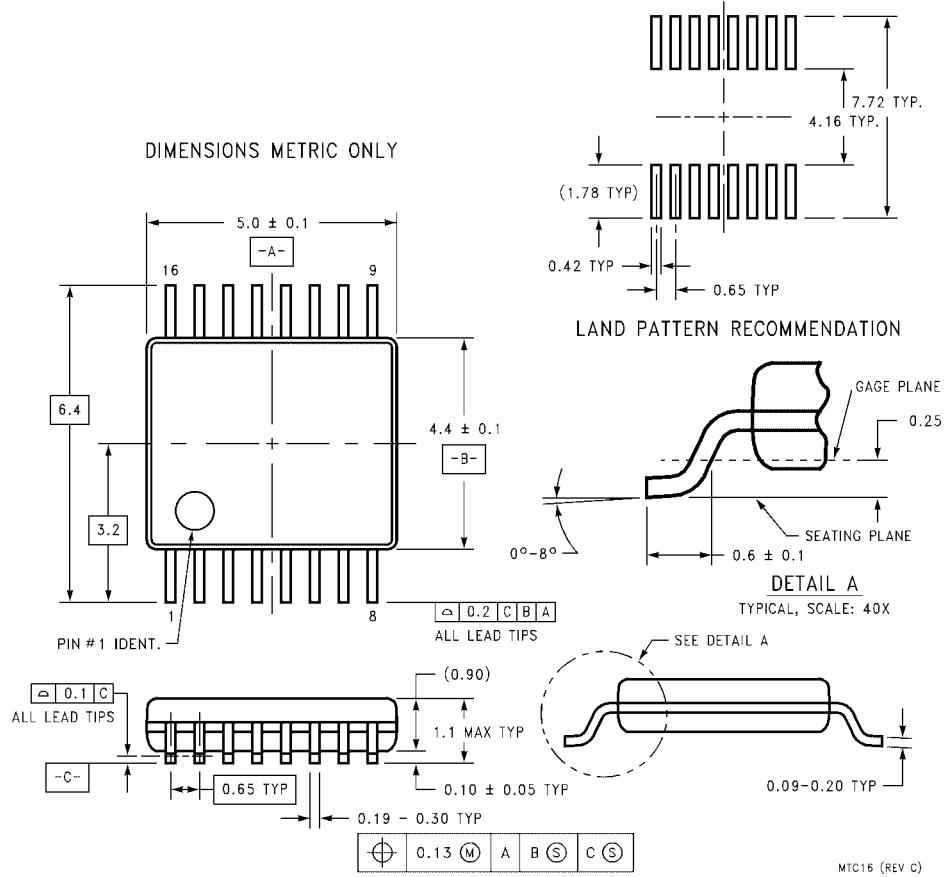
DETAIL A

NOTES:

- A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

M16DRevB1

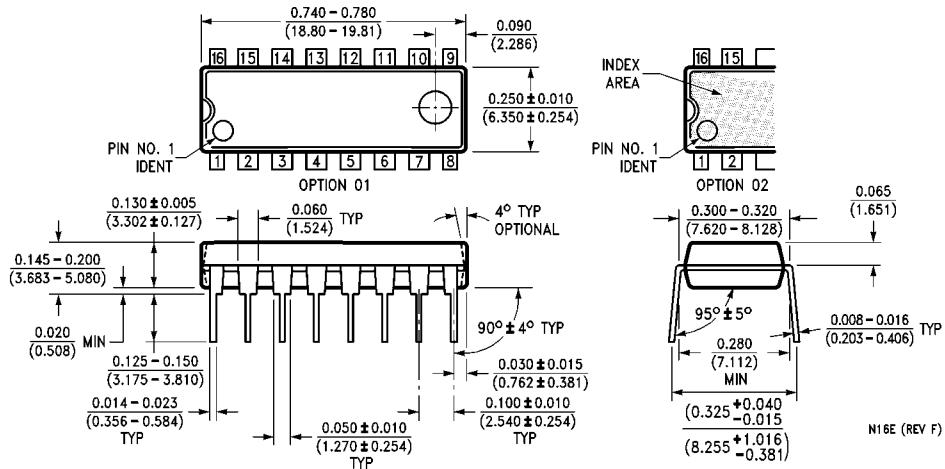
**16-Lead Small Outline Package (SOP), EIAJ Type II, 5.3mm Wide
Package Number M16D**

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
Package Number MTC16

Physical Dimensions

inches (millimeters) unless otherwise noted (Continued)



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
Package Number N16E

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