

NTE7115 **Integrated Circuit** **Color TV Horizontal Combination Circuit**

Description:

The NTE7115 is a monolithic integrated circuit in a 18-Lead DIP type package designed for use in color television receivers.

Features:

- Positive Video Input: Capacitively Coupled (Source Impedance < 200Ω)
- Adaptive Sync Separator: Slicing Level at 50% of Sync Amplitude
- Internal Vertical Pulse Separator ^w/Double Slope Integrator
- Output Stage for Vertical Sync Pulse or Composite Sync Depending on the Load; Both are Switched OFF at Muting
- φ_1 Phase Control Between Horizontal Sync and Oscillator
- Coincidence Detector φ_3 for Automatic Time Constant Switching; Overruled by the VCR Switch
- Time Constant Switch Between Two External Time Constants for Loop Gain; Both Controlled by the Coincidence Detector φ_3
- φ_1 Gating Pulse Controlled by Coincidence Detector φ_3
- Mute Circuit Depending on TV Transmitter Identification
- φ_2 Phase Control Between Line Flyback and Oscillator; the Slicing Levels for φ_2 Control and Horizontal Blanking can be set Separately
- Burst Keying and Horizontal Blanking Pulse Generation, in Combination with Clamping of the Vertical Blanking Pulse (Three-Level Sandcastle)
- Horizontal Drive Output with Constant Duty Cycle Inhibited by the Protection Circuit or the Supply Voltage Sensor
- Detector for Too Low Supply Voltage
- Protection Circuit for Switching Off the Horizontal Drive Output Continuously if the Input Voltage is Below 4V or Higher than 8V
- Line Flyback Control Causing the Horizontal Blanking Level at the Sandcastle Output Continuously in Case of a Missing Flyback Pulse
- Spot Suppressor Controlled by the Line Flyback Control

Applications:

- Television Receivers
- Video Receivers

Absolute Maximum Ratings:Supply Voltage (Pin19), $V_{15-5} = V_{CC}$ 13.2V

Voltages at:

(Pin1, Pin4, and Pin7), V_{1-5} , V_{4-5} , V_{7-5} 18V(Pin8, Pin13, and Pin18), V_{8-5} , V_{13-5} , V_{18-5} V_{CC} (Pin11 (Range)), V_{11-5} -0.5 to +6.0V

Currents at:

Pin1, I_1 10mAPin2 (Peak Value), $\pm I_{2M}$ 10mAPin4, I_4 100mAPin6 (Peak Value), $\pm I_{6M}$ 6mAPin7, I_7 10mAPin8 (Range), I_8 -5 to +1mAPin9 (Range), I_9 -10 to +3mAPin18, $\pm I_{18}$ 10mATotal Power Dissipation, P_{TOT} 800mWOperating Ambient Temperature Range, T_A -0° to +70°CStorage Temperature Range, T_{stg} -65° to +150°C**DC and AC Electrical Characteristics:** ($V_{CC} = 12V$, $T_A = +25^\circ C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Composite Video Input and Sync Separator, Pin11 (Internal Black Level Determination)						
Input Signal (Positive Video; Standard Signal; Peak-to-Peak Value)	V _{11-5(P-P)}		0.2	1.0	3.0	V
Sync Pulse Amplitude (Independent of Video Content)	V _{11-5(P-P)}		50	–	–	mV
Generator Resistance	R _G		–	–	200	Ω
Input Current During Video	I ₁₁		–	5	–	μA
Sync Pulse	–I ₁₁		–	40	–	μA
Black Level	–I ₁₁		–	25	–	μA
Composite Sync Generation, Pin10 (Horizontal Slicing Level at 50% of the Sync Pulse Amplitude)						
Capacitor Current During Video	I ₁₀		–	16	–	μA
Sync Pulse	–I ₁₀		–	170	–	μA
Vertical Sync Pulse Generation, Pin9 (Slicing Level at 30% (60% Between Black Level and Horizontal Slicing Level))						
Output Voltage	V ₉₋₅		10	–	–	V
Pulse Duration	t _p		–	190	–	μs
Delay With Respect to the Vertical Sync Pulse (LeadinG Edge)	t _D		–	45	–	μs
Pulse-Mode Control Output Current for Vertical Sync Pulse (Dual Integrated)			No Current Applied at Pin9			
Output Current for Horizontal and Vertical Sync Pulse (Non-Integrated Separated Signal)			Current Applied Via a 15kΩ from V _{CC} to Pin9			
Horizontal Oscillator, Pin14 and Pin16						
Free-Running Frequency	f _{OSC}		–	15.625	–	kHz
Reference Voltage for f _{OSC}	V ₁₄₋₅		–	6	–	V

DC and AC Electrical Characteristics (Cont'd): ($V_{CC} = 12V$, $T_A = +25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Horizontal Oscillator, Pin14 and Pin16 (Cont'd)						
Frequency Control Sensitivity	$\Delta f_{OSC}/\Delta I_{14}$		—	31	—	Hz/ μA
Adjustment Range of Circuit	Δf_{OSC}		—	± 10	—	%
Spread of Frequency	Δf_{OSC}		—	—	5	%
Frequency Dependency (Excluding Tolerance of External Components) w/Supply Voltage	$\frac{\Delta f_{OSC}/f_{OSC}}{\Delta V_{15-5}/V_{15-5}}$	$V_{CC} = 12V$	—	± 0.05	—	%
w/Supply Voltage Drop of 5V	Δf_{OSC}		—	—	10	%
w/Temperature	TC		—	—	$\pm 10^{-4}$	$^{\circ}C^{-1}$
Capacitor Current During: Charging	$-I_{16}$		—	1024	—	μA
Discharging	I_{16}		—	313	—	μA
Sawtooth Voltage Timing (Pin14) Rise Time	t_R		—	49	—	μs
Fall Time	t_F		—	15	—	μs
Horizontal Output Pulse, Pin4						
Output Voltage, Low	V_{4-5}	$I_4 = 30mA$	—	—	0.5	V
Pulse Duration, High	t_P		—	29 ± 1.5	—	μs
Supply Voltage for Switching Off the Output Pulse (Pin15)	V_{CC}		—	4	—	V
Hysteresis for Switching On the Output Pulse	ΔV_P		—	250	—	mV
Phase Compensation, ϕ_1, Pin17						
Control Voltage Range	V_{17-5}		3.55	—	8.3	V
Leakage Current	I_{17}	$V_{17-5} = 3.55$ to $8.3V$	—	—	1	μA
Control Current: for External Time Constant Switch	$\pm I_{17}$		1.8	2.0	2.2	mA
at $V_{18-5} = V_{15-5}$ and $V_{13-5} < 2V$ or $V_{13-5} > 9.5V$			—	8	—	mA
at $V_{18-5} = V_{15-5}$ and $V_{13-5} = 2$ to $9.5V$			1.8	2.0	2.2	mA
Horizontal Oscillator Control Control Sensitivity	S_{ϕ}		6	—	—	kHz/ μs
Catching and Holding Range	Δf_{OSC}		—	± 680	—	Hz
Spread of Catching and Holding Range			—	± 10	—	%
Internal Keying Pulse	t_P	$V_{13-5} = 2.9$ to $9.5V$	—	7.5	—	μs
Time Constant Switch Slow Time Constant	V_{13-5}		9.5	—	2.0	V
Fast Time Constant			2.0	—	9.5	V
Impedance Converter Offset Voltage (Slow Time Constant)	$\pm V_{17-18}$		—	—	3	mV
Output Resistance Slow Time Constant	R_{18-5}		—	—	10	Ω
Fast Time Constant		High Impedance	—	—	—	Ω

DC and AC Electrical Characteristics (Cont'd): ($V_{CC} = 12V$, $T_A = +25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Phase Compensation, ϕ_1, Pin17 (Cont'd)						
Leakage Current	I_{18}		–	–	1	μA
Coincidence Detector, ϕ_3, Pin13						
Output Voltage $\frac{w/o \text{ Coincidence } w/ \text{ Composite Video Signal}}$	V_{13-5}		–	–	1	V
$\frac{w/o \text{ Coincidence } w/o \text{ Composite Video Signal (Noise)}}$			–	–	2	V
$\frac{w/ \text{ Coincidence } w/ \text{ Composite Video Signal}}$			–	6	–	V
Output Current $\frac{w/o \text{ Coincidence } w/ \text{ Composite Video Signal}}$	I_{13}		–	50	–	μA
$\frac{w/ \text{ Coincidence } w/ \text{ Composite Video Signal}}$	$-I_{13}$		–	300	–	μA
Switching Current	I_{13}	$V_{13-5} = V_{CC} - 0.5V$	–	–	100	μA
	$I_{13(av)}$	$V_{13-5} = 0.5V$ (Average)	–	–	100	μA
Phase Comparison, ϕ_2, Pin2 and Pin3 (Note 1)						
Phase Relation Between Middle of the Horizontal Sync Pulse and the Middle of the Line Flyback Pulse	Δt	$t_{FP} = 12\mu s$, Note 2	–	2.6 ± 0.7	–	μs
If Additional Adjustment is Required, it can be Arranged by Applying a Current at Pin3, such that for Applied Current	$\Delta I / \Delta t$		–	30	–	$\mu A / \mu s$
Input for Line Flyback Pulse, Pin2						
Switching Level for ϕ_2 Comparison	V_{2-5}		–	3	–	V
Switching Level for Horizontal Blanking and Flyback Control	V_{2-5}		–	0.3	–	V
Input Voltage Limiting	V_{2-5}		–	-0.7 $+4.5$	–	V
Switching Current at Horizontal Flyback	I_2		0.01	1.0	–	mA
at Horizontal Scan			–	–	2.0	μA
Maximum Negative Input Current	$-I_2$		–	–	500	μA
Phase Detector Output, Pin3						
Control Current for ϕ_2	$\pm I_3$		–	1	–	mA
Control Range	$\Delta t_{\phi 2}$		–	19	–	μs
Static Control Error	$\Delta t / \Delta t_d$		–	–	0.2	%
Leakage Current	I_3		–	–	5	μA

Note 1. Phase comparison between horizontal oscillator and the line flyback pulse. Generation of a phase-modulated (ϕ_2) horizontal output pulse with constant duration.

Note 2. t_{FP} is the line flyback pulse duration.

DC and AC Electrical Characteristics (Cont'd): ($V_{CC} = 12V$, $T_A = +25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Burst Gating Pulse, Pin6 (Note 3)						
Output Voltage	V ₆₋₅		10	11	—	V
Pulse Duration	t _p		3.7	4.0	4.3	μs
Phase Relation Between Middle of Sync Pulse at the Input and the Leading Edge of the Burst Gating Pulse	t _{φ6}	V ₆₋₅ = 7V	2.15	2.65	3.15	μs
Output Trailing Edge Current	I ₆		—	2	—	mA
Horizontal Blanking Pulse, Pin6 (Note 3)						
Output Voltage	V ₆₋₅		4.2	4.5	4.9	V
Output Trailing Edge Current	I ₆		—	2	—	mA
Saturation Voltage at Horizontal Scan	V _{6-5sat}		—	—	0.5	V
Clamping Circuit for Vertical Blanking Pulse, Pin6 (Note 3)						
Output Voltage	V ₆₋₅	I ₆ = 2.8mA	2.15	2.5	3.0	V
Minimum Output Current	I _{6min}	V ₆₋₅ > 2.15V	—	2.3	—	mA
Maximum Output Current	I _{6max}	V ₆₋₅ < 3V	—	3.3	—	mA
TV Transmitter Identification, Pin12						
Output Voltage No TV Transmitter	V ₁₂₋₅		—	—	1	V
TV Transmitter Identified			7	—	—	V
Mute Output, Pin7						
Output Voltage, No TV Transmitter	V ₇₋₅	I ₇ = 3mA	—	—	0.5	V
Output Resistance, No TV Transmitter	R ₇₋₅	I ₇ = 3mA	—	—	100	Ω
Output Leakage Current, TV Transmitter Identified	I ₇	V ₁₂₋₅ > 3V	—	—	5	μA
Protection Circuit, Pin8 (Beam Current/EHT Voltage Protection)						
No-Load Voltage (Operative Condition)	V ₈₋₅	I ₈ = 0	—	6	—	V
Threshold Positive-Going Voltage	V ₈₋₅		—	8 ±0.8	—	V
Negative-Going Voltage			—	4 ±0.4	—	V
Current Limiting	±I ₈	V ₈₋₅ = 1 to 8.5V	—	60	—	μA
Input Resistance	R ₈₋₅	V ₈₋₅ > 8.5V	—	3	—	kΩ
Response Delay of Threshold Switch	t _d		—	10	—	μs
Control Output of Line Flyback Pulse Condition, Pin1						
Saturation Voltage at Standard Operation	V _{1-5sat}	I ₁ = 3mA	—	—	0.5	V
Output Leakage Current in Case of Break in Transmission	I ₁		—	—	5	μA

Note 3. Three-level sandcastle pulse.

Pin Connection Diagram

Control Out Line FB Pulse Control	1	18	Volt Fol Output
FB Pulse Input	2	17	Phase Com
Phase Det	3	16	Horiz OSC
Horiz Output Pulse	4	15	V _{CC}
GND	5	14	Horiz OSC
Burst Gating Pulse	6	13	Coin Det
Mute Output	7	12	TV Trans Ident
EHT Volt Protection	8	11	Comp Video In/Sync Sep
Vert Sync Pulse Gen	9	10	Comp Sync Gen

