

ELECTRICAL SPECIFICATIONS:

1.0	TURNs RATIO: $\{(P_6-P_8-P_3)\} : \{(J_6-J_3)\}$: 1CT : 1 $+/-3\%$
	$\{(P_2-P_7-P_1)\} : \{(J_2-J_1)\}$: 1CT : 2.5 $+/-3\%$
2.0	INDUCTANCE: (P_6-P_3)	: 200uH MIN. @ 0.01V, 10KHz
	(P_2-P_1)	: 35uH MIN. @ 0.01V, 10KHz
3.0	LEAKAGE INDUCTANCE: P_6-P_3 (WITH J_6 AND J_3 SHORT)	: 0.3uH MAX. @ 1MHz
	P_2-P_1 (WITH J_2 AND J_1 SHORT)	: 0.3uH MAX. @ 1MHz
4.0	INTERWINDING CAPACITANCE: $\{(P_6,P_3)\}$ TO (J_6,J_3)	: 10pf MAX @ 1MHz
	$\{(P_2,P_1)\}$ TO (J_2,J_1)	: 10pf MAX. @ 1MHz
5.0	DC RESISTANCE: $(J_6-J_3)=(J_2-J_1)$: 1.0 ohms Max.
	$(P_6-P_8)=(P_8-P_3)$: 0.3 ohms Max.
	$(P_2-P_7)=(P_7-P_1)$: 0.3 ohms Max.
6.0	DIELECTRIC WITHSTAND: $\{(P_6,P_3)\}$ TO (J_6,J_3)	: 1500VAC
	$\{(P_2,P_1)\}$ TO (J_2,J_1)	: 1500VAC

NOTES

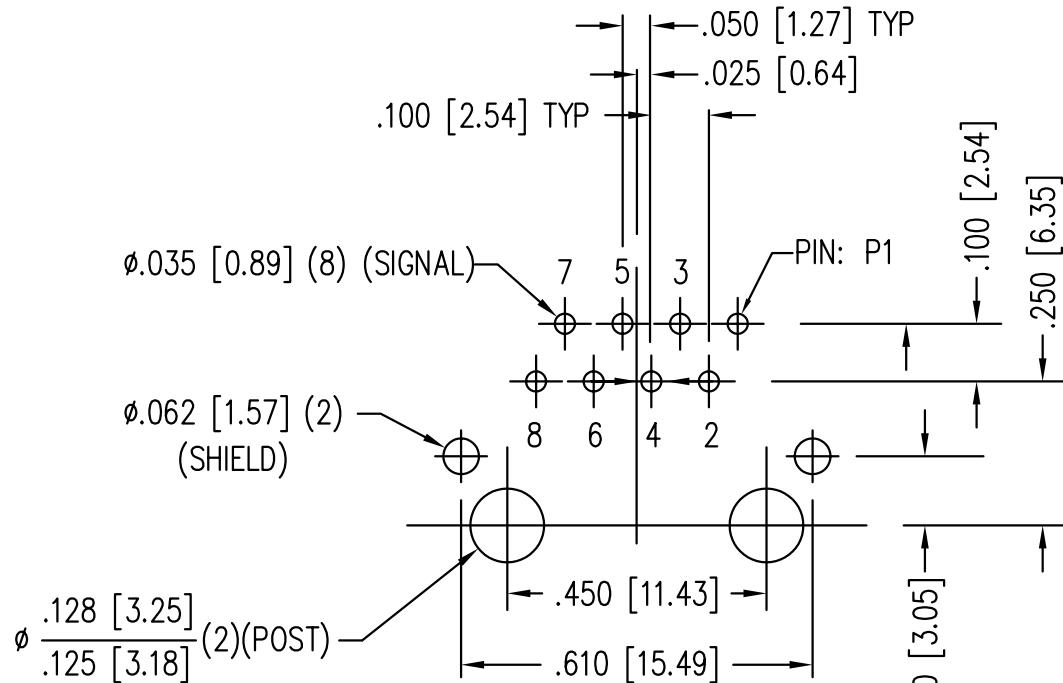
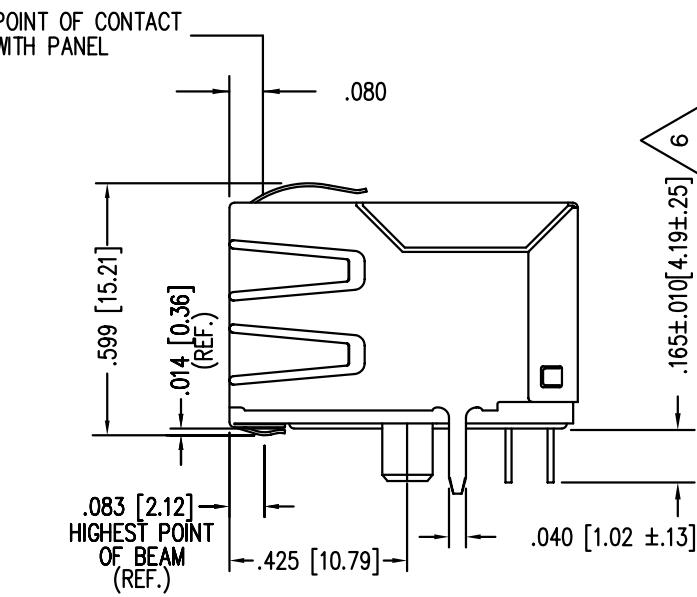
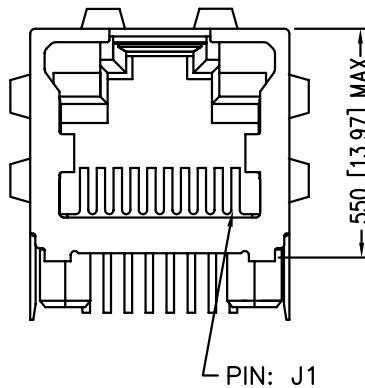
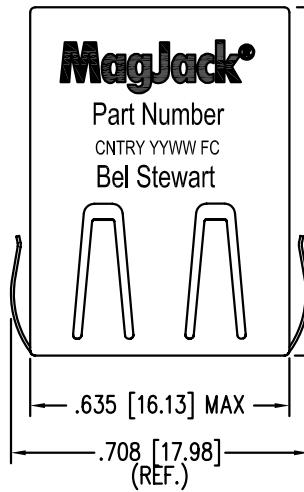
1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.



Bel Stewart Connector
11118 Susquehanna Trail, South
Glen Rock, Pa 17327-9199
717.234.7512

MagJack®
<http://www.stewartconnector.com>

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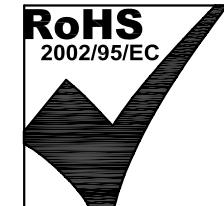


P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE
TOLERANCE $\pm .003 [0.08]$ UNLESS OTHERWISE SPECIFIED

NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005 [0.13]$
5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR
10 SECONDS MAX.

NOTE SPECIAL LEAD LENGTH

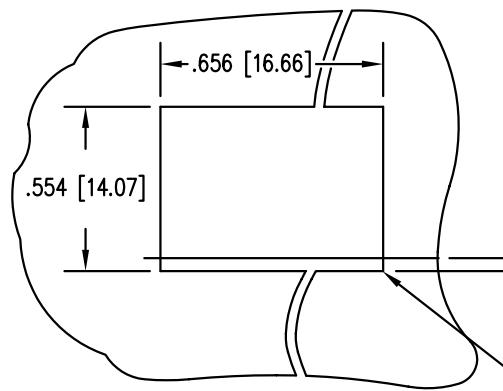


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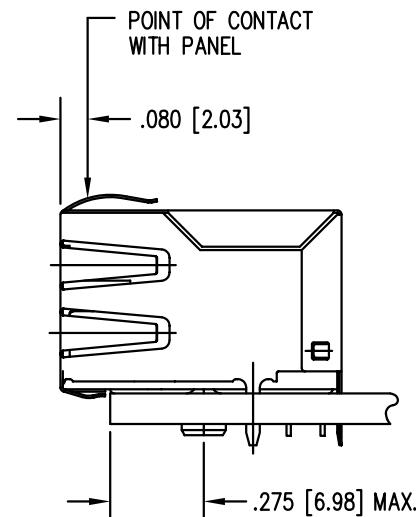
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.000 [0.00] (TOP OF PCB TO BOTTOM OF OPENING)
.010 [0.25] MAX. RADIUS(4)



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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SHEET 3 OF 3

DRAWING NO.

SI-50113-F REV. 05