SN54AS881A, SN74AS881A ARITHMETIC LOGIC UNITS/FUNCTION GENERATORS

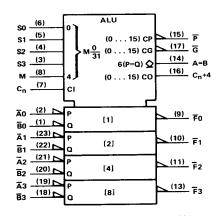
 Package Options Include Plastic "Small Outline" Packages, Both Plastic and Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

- Full Look-Ahead for High-Speed Operations on Long Words
- Arithmetic Operating Modes:

Addition
Subtraction
Shift Operand A One Position
Magnitude Comparison
Plus Twelve Other Arithmetic Operations

- Logic Function Modes
 Exclusive-OR
 Comparator
 AND, NAND, OR, NOR
 'AS881A Provides Status Register Checks
 Plus Ten Other Logic Operations
- Dependable Texas Instruments Quality and Reliability

logic symbol †



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

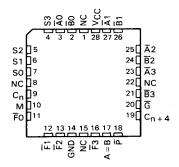
Pin numbers are for DW, JT, and NT packages.

SN54AS181A . . . J OR JT PACKAGE SN54AS881A . . . JT PACKAGE SN74AS181A . . . N OR NT PACKAGE SN74AS881A . . . DW OR NT PACKAGE (TOP VIEW)

B0 1 ∪24 V_{CC}

Ã0 🛮 2 23 🔲 🛱 1 S3 ∏3 22 T B1 S2 🗖 4 21 A2 20 B2 S1 ∏5 so ☐6 18[☐ 🛱 3 Cn □7 17 🗖 👨 м Пв F0 **□**9 16 Cn + 4 F1 ∐10 15 P F2 ∏11 14 A = B 13 🗍 F3 GND 12

SN54AS181A, SN54AS881A . . . FK PACKAGE SN74AS181A, SN74AS881A . . . FN PACKAGE (TOP VIEW)



NC - No internal connection

For complete information on the SN54AS881A and the SN74AS881A, see page 2-187.

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