- **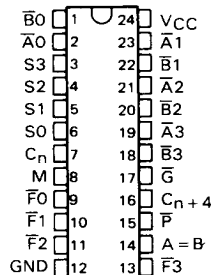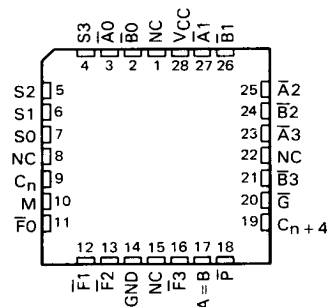Package Options Include Plastic "Small Outline" Packages, Both Plastic and Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs**

- **Full Look-Ahead for High-Speed Operations on Long Words**

- **Arithmetic Operating Modes:**
  Addition
  Subtraction
  Shift Operand A One Position
  Magnitude Comparison
  Plus Twelve Other Arithmetic Operations

- **Logic Function Modes**
  Exclusive-OR
  Comparator
  AND, NAND, OR, NOR
  'AS881A Provides Status Register Checks
  Plus Ten Other Logic Operations

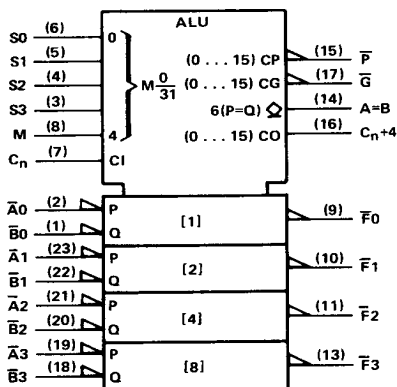- **Dependable Texas Instruments Quality and Reliability**

SN54AS181A . . . J OR JT PACKAGE
SN54AS881A . . . JT PACKAGE
SN74AS181A . . . N OR NT PACKAGE
SN74AS881A . . . DW OR NT PACKAGE
(TOP VIEW)

$\overline{B0}$	1	24	V_{CC}
$\overline{A0}$	2	23	$\overline{A1}$
S3	3	22	$\overline{B1}$
S2	4	21	$\overline{A2}$
S1	5	20	$\overline{B2}$
S0	6	19	$\overline{A3}$
C_n	7	18	$\overline{B3}$
M	8	17	$\overline{G}$
$\overline{F0}$	9	16	C_{n+4}
$\overline{F1}$	10	15	$\overline{P}$
$\overline{F2}$	11	14	$A = B$
GND	12	13	$\overline{F3}$

SN54AS181A, SN54AS881A . . . FK PACKAGE
SN74AS181A, SN74AS881A . . . FN PACKAGE
(TOP VIEW)

NC – No internal connection

logic symbol†

† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers are for DW, JT, and NT packages.

For complete information on the SN54AS881A and the SN74AS881A, see page 2-187.

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ALS and AS Circuits