

# 74LVC2G04

## Dual inverter

Rev. 13 — 15 August 2023

Product data sheet

## 1. General description

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The 74LVC2G04 is a dual inverter. Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of these devices as translators in mixed 3.3 V and 5 V environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

## 2. Features and benefits

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- Wide supply voltage range from 1.65 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- $\pm 24$  mA output drive ( $V_{CC} = 3.0$  V)
- CMOS low power dissipation
- Direct interface with TTL levels
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
  - JESD36 (4.6 V to 5.5 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from  $-40$  °C to  $+85$  °C and  $-40$  °C to  $+125$  °C.

### 3. Ordering information

Table 1. Ordering information

| Type number                 | Package           |              |                                                                                                                |                           |
|-----------------------------|-------------------|--------------|----------------------------------------------------------------------------------------------------------------|---------------------------|
|                             | Temperature range | Name         | Description                                                                                                    | Version                   |
| <a href="#">74LVC2G04GW</a> | -40 °C to +125 °C | TSSOP6       | plastic thin shrink small outline package; 6 leads; body width 1.25 mm                                         | <a href="#">SOT363-2</a>  |
| <a href="#">74LVC2G04GV</a> | -40 °C to +125 °C | SC-74; TSOP6 | plastic surface-mounted package; 6 leads                                                                       | <a href="#">SOT457</a>    |
| <a href="#">74LVC2G04GM</a> | -40 °C to +125 °C | XSON6        | plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm                    | <a href="#">SOT886</a>    |
| <a href="#">74LVC2G04GN</a> | -40 °C to +125 °C | XSON6        | extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm                          | <a href="#">SOT1115</a>   |
| <a href="#">74LVC2G04GS</a> | -40 °C to +125 °C | XSON6        | extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm                          | <a href="#">SOT1202</a>   |
| <a href="#">74LVC2G04GX</a> | -40 °C to +125 °C | X2SON6       | plastic thermal enhanced extremely thin small outline package; no leads; 6 terminals; body 1.0 × 0.8 × 0.32 mm | <a href="#">SOT1255-2</a> |

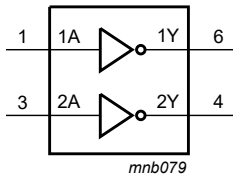
### 4. Marking

Table 2. Marking

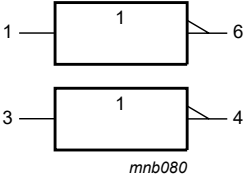
| Type number | Marking code [1] |
|-------------|------------------|
| 74LVC2G04GW | V4               |
| 74LVC2G04GV | V04              |
| 74LVC2G04GM | V4               |
| 74LVC2G04GN | V4               |
| 74LVC2G04GS | V4               |
| 74LVC2G04GX | V4               |

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

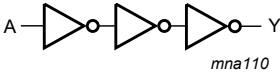
### 5. Functional diagram



**Fig. 1. Logic symbol**



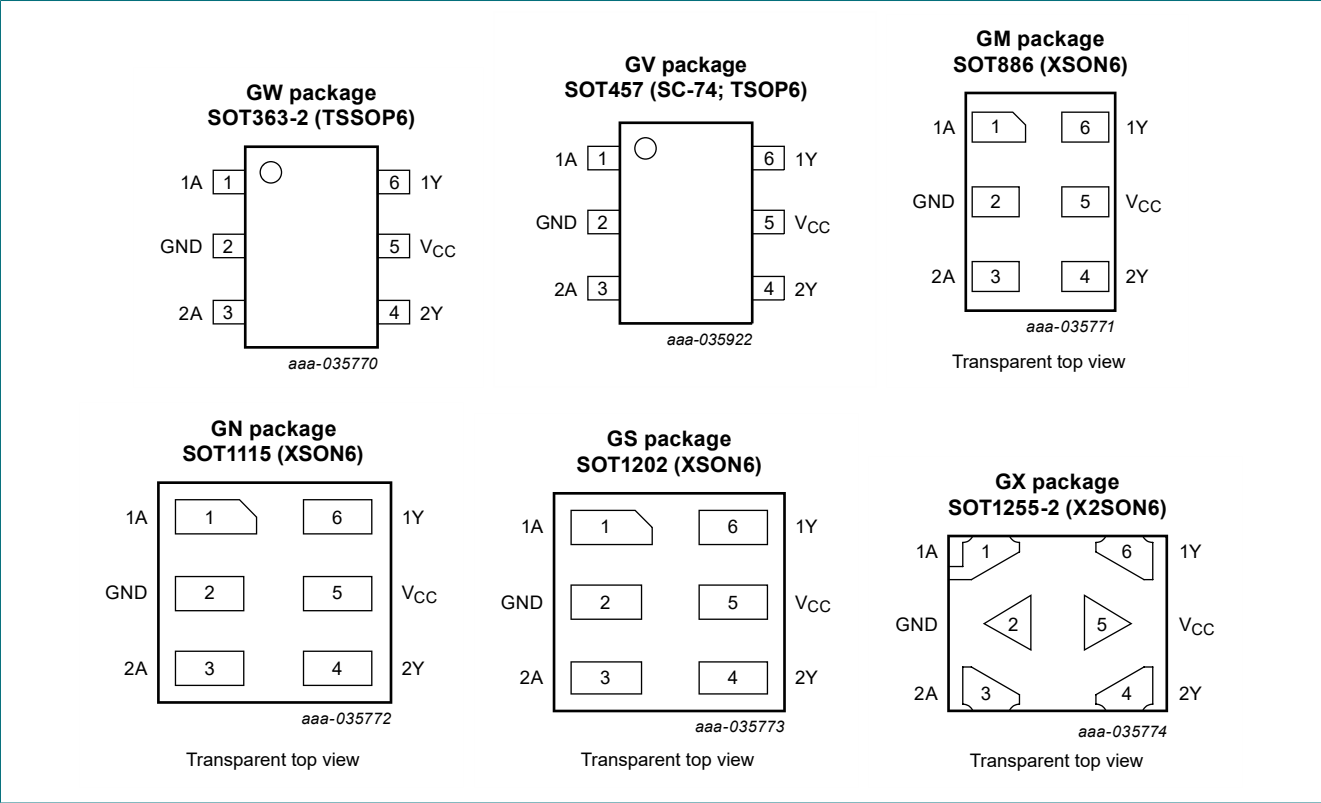
**Fig. 2. IEC logic symbol**



**Fig. 3. Logic diagram (one gate)**

6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

| Symbol          | Pin | Description    |
|-----------------|-----|----------------|
| 1A              | 1   | data input     |
| GND             | 2   | ground (0 V)   |
| 2A              | 3   | data input     |
| 2Y              | 4   | data output    |
| V <sub>CC</sub> | 5   | supply voltage |
| 1Y              | 6   | data output    |

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

| Input | Output |
|-------|--------|
| nA    | nY     |
| L     | H      |
| H     | L      |

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol    | Parameter               | Conditions                      | Min  | Max            | Unit |
|-----------|-------------------------|---------------------------------|------|----------------|------|
| $V_{CC}$  | supply voltage          |                                 | -0.5 | +6.5           | V    |
| $I_{IK}$  | input clamping current  | $V_I < 0$ V                     | -50  | -              | mA   |
| $V_I$     | input voltage           |                                 | -0.5 | +6.5           | V    |
| $I_{OK}$  | output clamping current | $V_O > V_{CC}$ or $V_O < 0$ V   | -    | $\pm 50$       | mA   |
| $V_O$     | output voltage          | Active mode                     | -0.5 | $V_{CC} + 0.5$ | V    |
|           |                         | Power-down mode; $V_{CC} = 0$ V | -0.5 | +6.5           | V    |
| $I_O$     | output current          | $V_O = 0$ V to $V_{CC}$         | -    | $\pm 50$       | mA   |
| $I_{CC}$  | supply current          |                                 | -    | 100            | mA   |
| $I_{GND}$ | ground current          |                                 | -100 | -              | mA   |
| $P_{tot}$ | total power dissipation | $T_{amb} = -40$ °C to $+125$ °C | -    | 250            | mW   |
| $T_{stg}$ | storage temperature     |                                 | -65  | +150           | °C   |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT363-2 (TSSOP6) package:  $P_{tot}$  derates linearly with 3.7 mW/K above 83 °C.  
 For SOT457 (SC-74; TSOP6) package:  $P_{tot}$  derates linearly with 4.1 mW/K above 89 °C.  
 For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.  
 For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C.  
 For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.  
 For SOT1255-2 (X2SON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 75 °C.

## 9. Recommended operating conditions

**Table 6. Recommended operating conditions**

| Symbol              | Parameter                           | Conditions                      | Min  | Typ | Max      | Unit |
|---------------------|-------------------------------------|---------------------------------|------|-----|----------|------|
| $V_{CC}$            | supply voltage                      |                                 | 1.65 | -   | 5.5      | V    |
| $V_I$               | input voltage                       |                                 | 0    | -   | 5.5      | V    |
| $V_O$               | output voltage                      | Active mode                     | 0    | -   | $V_{CC}$ | V    |
|                     |                                     | Power-down mode; $V_{CC} = 0$ V | 0    | -   | 5.5      | V    |
| $T_{amb}$           | ambient temperature                 |                                 | -40  | -   | +125     | °C   |
| $\Delta t/\Delta V$ | input transition rise and fall rate | $V_{CC} = 1.65$ V to $2.7$ V    | -    | -   | 20       | ns/V |
|                     |                                     | $V_{CC} = 2.7$ V to $5.5$ V     | -    | -   | 10       | ns/V |

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                 | Conditions                                                                                                | -40 °C to +85 °C      |         |                     | -40 °C to +125 °C     |                     | Unit |
|------------------|---------------------------|-----------------------------------------------------------------------------------------------------------|-----------------------|---------|---------------------|-----------------------|---------------------|------|
|                  |                           |                                                                                                           | Min                   | Typ [1] | Max                 | Min                   | Max                 |      |
| V <sub>IH</sub>  | HIGH-level input voltage  | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                        | 0.65V <sub>CC</sub>   | -       | -                   | 0.65V <sub>CC</sub>   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                          | 1.7                   | -       | -                   | 1.7                   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                          | 2.0                   | -       | -                   | 2.0                   | -                   | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                          | 0.7V <sub>CC</sub>    | -       | -                   | 0.7V <sub>CC</sub>    | -                   | V    |
| V <sub>IL</sub>  | LOW-level input voltage   | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                        | -                     | -       | 0.35V <sub>CC</sub> | -                     | 0.35V <sub>CC</sub> | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                          | -                     | -       | 0.7                 | -                     | 0.7                 | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                          | -                     | -       | 0.8                 | -                     | 0.8                 | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                          | -                     | -       | 0.3V <sub>CC</sub>  | -                     | 0.3V <sub>CC</sub>  | V    |
| V <sub>OH</sub>  | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                       |                       |         |                     |                       |                     |      |
|                  |                           | I <sub>O</sub> = -100 µA; V <sub>CC</sub> = 1.65 V to 5.5 V                                               | V <sub>CC</sub> - 0.1 | -       | -                   | V <sub>CC</sub> - 0.1 | -                   | V    |
|                  |                           | I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V                                                          | 1.2                   | -       | -                   | 0.95                  | -                   | V    |
|                  |                           | I <sub>O</sub> = -8 mA; V <sub>CC</sub> = 2.3 V                                                           | 1.9                   | -       | -                   | 1.7                   | -                   | V    |
|                  |                           | I <sub>O</sub> = -12 mA; V <sub>CC</sub> = 2.7 V                                                          | 2.2                   | -       | -                   | 1.9                   | -                   | V    |
|                  |                           | I <sub>O</sub> = -24 mA; V <sub>CC</sub> = 3.0 V                                                          | 2.3                   | -       | -                   | 2.0                   | -                   | V    |
|                  |                           | I <sub>O</sub> = -32 mA; V <sub>CC</sub> = 4.5 V                                                          | 3.8                   | -       | -                   | 3.4                   | -                   | V    |
| V <sub>OL</sub>  | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                       |                       |         |                     |                       |                     |      |
|                  |                           | I <sub>O</sub> = 100 µA; V <sub>CC</sub> = 1.65 V to 5.5 V                                                | -                     | -       | 0.10                | -                     | 0.10                | V    |
|                  |                           | I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V                                                           | -                     | -       | 0.45                | -                     | 0.70                | V    |
|                  |                           | I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V                                                            | -                     | -       | 0.30                | -                     | 0.45                | V    |
|                  |                           | I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V                                                           | -                     | -       | 0.40                | -                     | 0.60                | V    |
|                  |                           | I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V                                                           | -                     | -       | 0.55                | -                     | 0.80                | V    |
|                  |                           | I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V                                                           | -                     | -       | 0.55                | -                     | 0.80                | V    |
| I <sub>I</sub>   | input leakage current     | V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V                                             | -                     | ±0.1    | ±1                  | -                     | ±1                  | µA   |
| I <sub>OFF</sub> | power-off leakage current | V <sub>CC</sub> = 0 V; V <sub>I</sub> or V <sub>O</sub> = 5.5 V                                           | -                     | ±0.1    | ±2                  | -                     | ±2                  | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V                    | -                     | 0.1     | 4                   | -                     | 4                   | µA   |
| ΔI <sub>CC</sub> | additional supply current | per pin; V <sub>CC</sub> = 2.3 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A | -                     | 5       | 500                 | -                     | 500                 | µA   |
| C <sub>I</sub>   | input capacitance         | V <sub>CC</sub> = 3.3 V; V <sub>I</sub> = GND to V <sub>CC</sub>                                          | -                     | 2.5     | -                   | -                     | -                   | pF   |

[1] All typical values are measured at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.

## 11. Dynamic characteristics

**Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

| Symbol   | Parameter                     | Conditions                                                | -40 °C to +85 °C |         |     | -40 °C to +125 °C |     | Unit |
|----------|-------------------------------|-----------------------------------------------------------|------------------|---------|-----|-------------------|-----|------|
|          |                               |                                                           | Min              | Typ [1] | Max | Min               | Max |      |
| $t_{pd}$ | propagation delay             | nA to nY; see Fig. 4 [2]                                  |                  |         |     |                   |     |      |
|          |                               | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$              | 1.0              | 3.5     | 8.0 | 1.0               | 9.5 | ns   |
|          |                               | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$                | 1.0              | 2.2     | 4.4 | 1.0               | 5.4 | ns   |
|          |                               | $V_{CC} = 2.7 \text{ V}$                                  | 1.0              | 2.7     | 5.2 | 1.0               | 7.0 | ns   |
|          |                               | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$                | 0.5              | 2.7     | 4.1 | 0.5               | 5.5 | ns   |
|          |                               | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$                | 1.0              | 1.9     | 3.2 | 1.0               | 3.8 | ns   |
| $C_{PD}$ | power dissipation capacitance | $V_I = \text{GND to } V_{CC}; V_{CC} = 3.3 \text{ V}$ [3] | -                | 13.5    | -   | -                 | -   | pF   |

[1] Typical values are measured at  $T_{amb} = 25 \text{ °C}$  and  $V_{CC} = 1.8 \text{ V}, 2.5 \text{ V}, 2.7 \text{ V}, 3.3 \text{ V}$  and  $5.0 \text{ V}$  respectively.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu\text{W}$ ).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

$f_i$  = input frequency in MHz;

$f_o$  = output frequency in MHz;

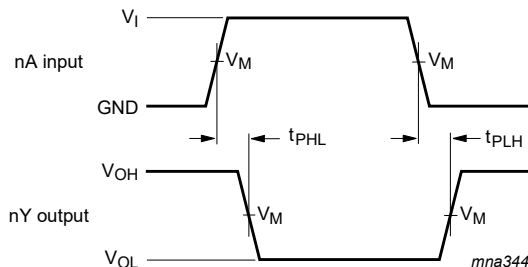
$C_L$  = output load capacitance in pF;

$V_{CC}$  = supply voltage in V;

$N$  = number of inputs switching;

$\sum (C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

### 11.1. Waveforms and test circuit



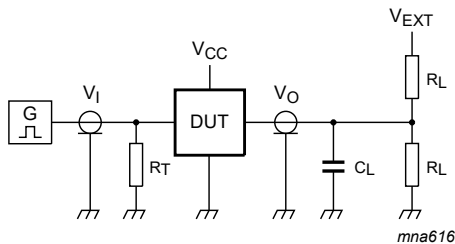
Measurement points are given in Table 9.

$V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig. 4. The input nA to output nY propagation delays**

**Table 9. Measurement points**

| Supply voltage   | Input       | Output      |
|------------------|-------------|-------------|
| $V_{CC}$         | $V_M$       | $V_M$       |
| 1.65 V to 1.95 V | $0.5V_{CC}$ | $0.5V_{CC}$ |
| 2.3 V to 2.7 V   | $0.5V_{CC}$ | $0.5V_{CC}$ |
| 2.7 V            | 1.5 V       | 1.5 V       |
| 3.0 V to 3.6 V   | 1.5 V       | 1.5 V       |
| 4.5 V to 5.5 V   | $0.5V_{CC}$ | $0.5V_{CC}$ |



Test data is given in [Table 10](#).  
Definitions for test circuit:  
 $R_L$  = Load resistance;  
 $C_L$  = Load capacitance including jig and probe capacitance;  
 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator;  
 $V_{EXT}$  = External voltage for measuring switching times.

Fig. 5. Test circuit for measuring switching times

Table 10. Test data

| Supply voltage   | Input    |                       | Load  |              | $V_{EXT}$          |
|------------------|----------|-----------------------|-------|--------------|--------------------|
| $V_{CC}$         | $V_I$    | $t_r = t_f$           | $C_L$ | $R_L$        | $t_{PLH}, t_{PHL}$ |
| 1.65 V to 1.95 V | $V_{CC}$ | $\leq 2.0 \text{ ns}$ | 30 pF | 1 k $\Omega$ | open               |
| 2.3 V to 2.7 V   | $V_{CC}$ | $\leq 2.0 \text{ ns}$ | 30 pF | 500 $\Omega$ | open               |
| 2.7 V            | 2.7 V    | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |
| 3.0 V to 3.6 V   | 2.7 V    | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |
| 4.5 V to 5.5 V   | $V_{CC}$ | $\leq 2.5 \text{ ns}$ | 50 pF | 500 $\Omega$ | open               |

12. Package outline

TSSOP6: plastic thin shrink small outline package; 6 leads; body width 1.25 mm

SOT363-2

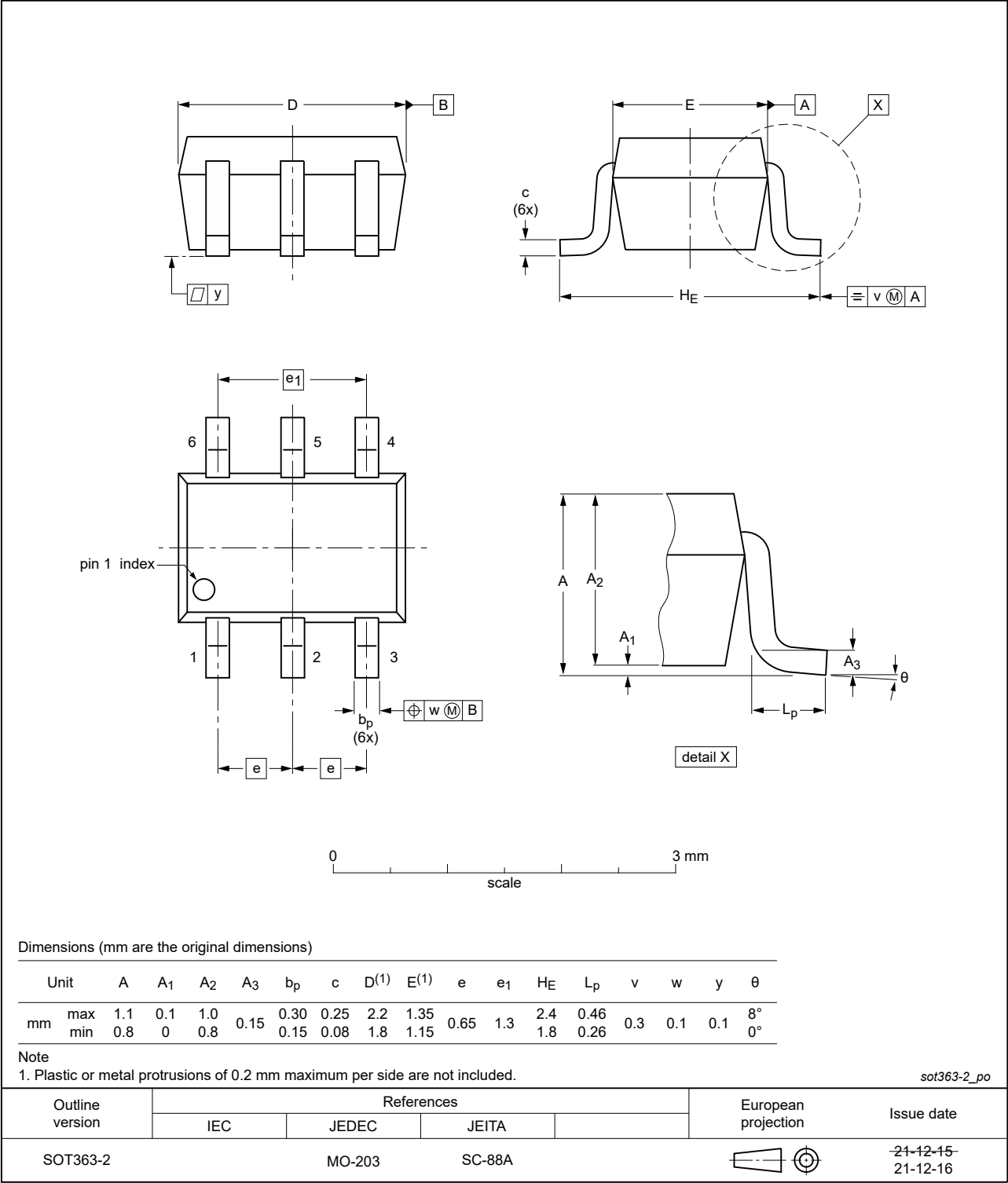


Fig. 6. Package outline SOT363-2 (TSSOP6)



Plastic, surface-mounted package (SC-74; TSOP6); 6 leadsSOT457

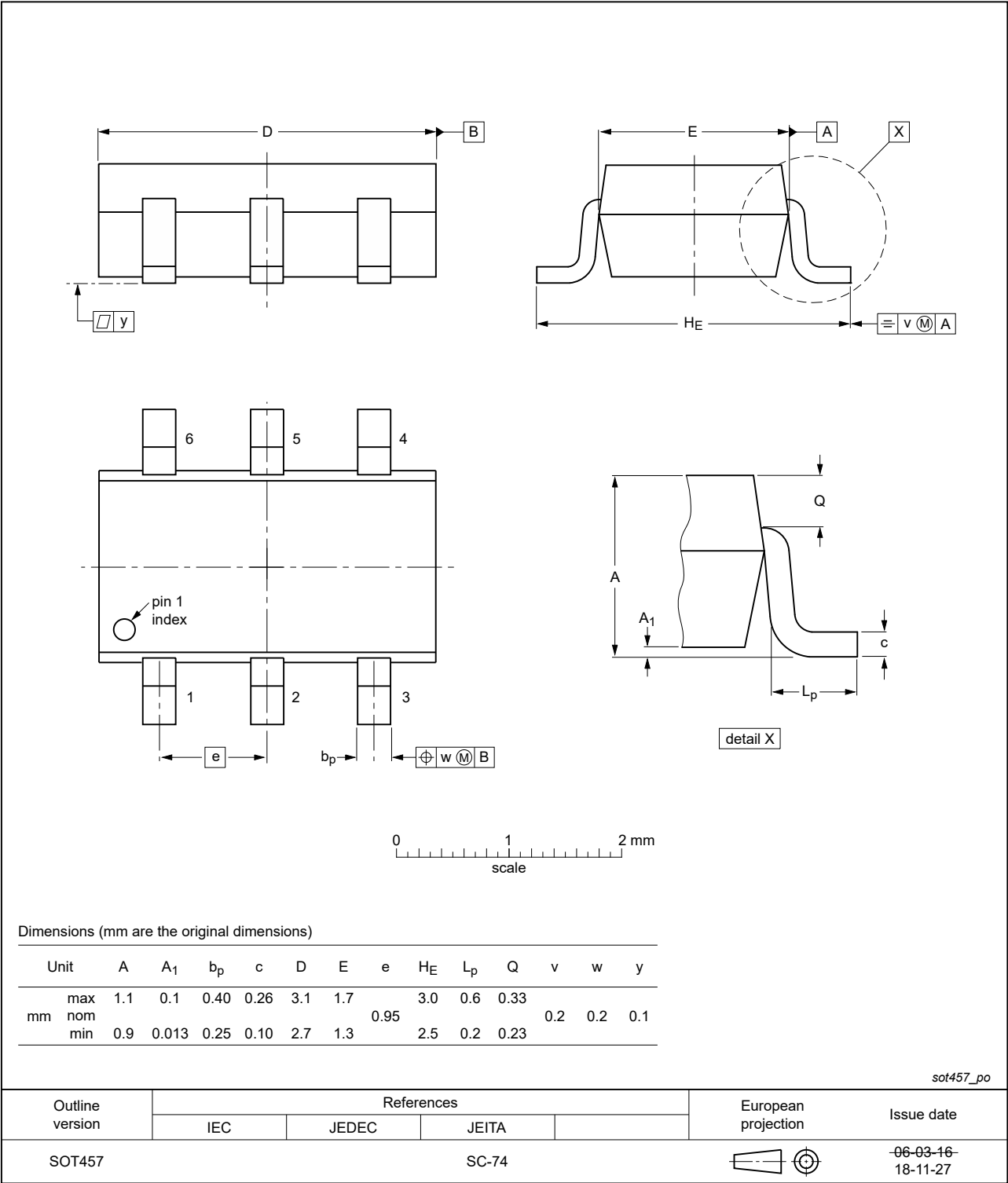


Fig. 7. Package outline SOT457 (SC-74; TSOP6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886

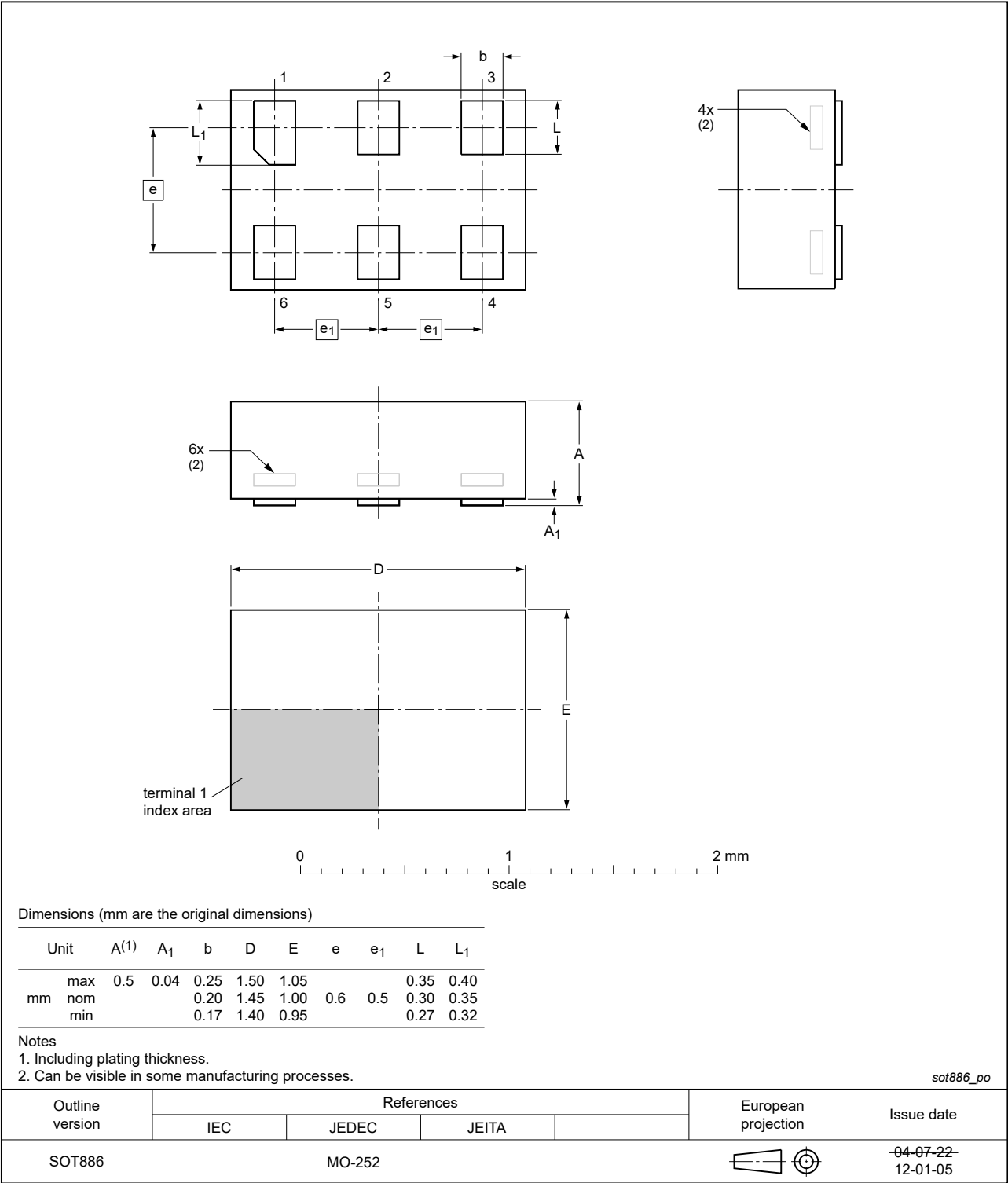


Fig. 8. Package outline SOT886 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115

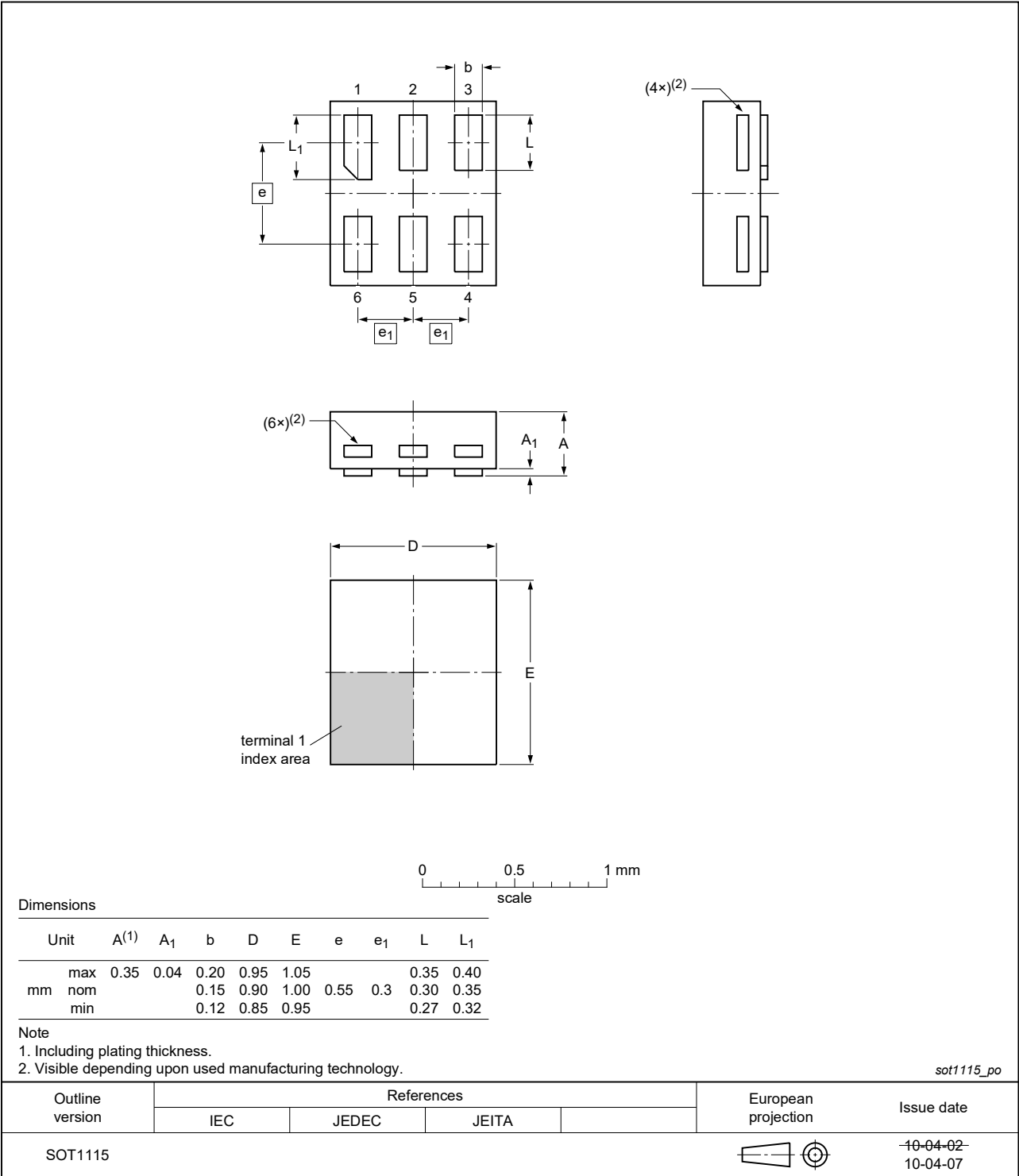
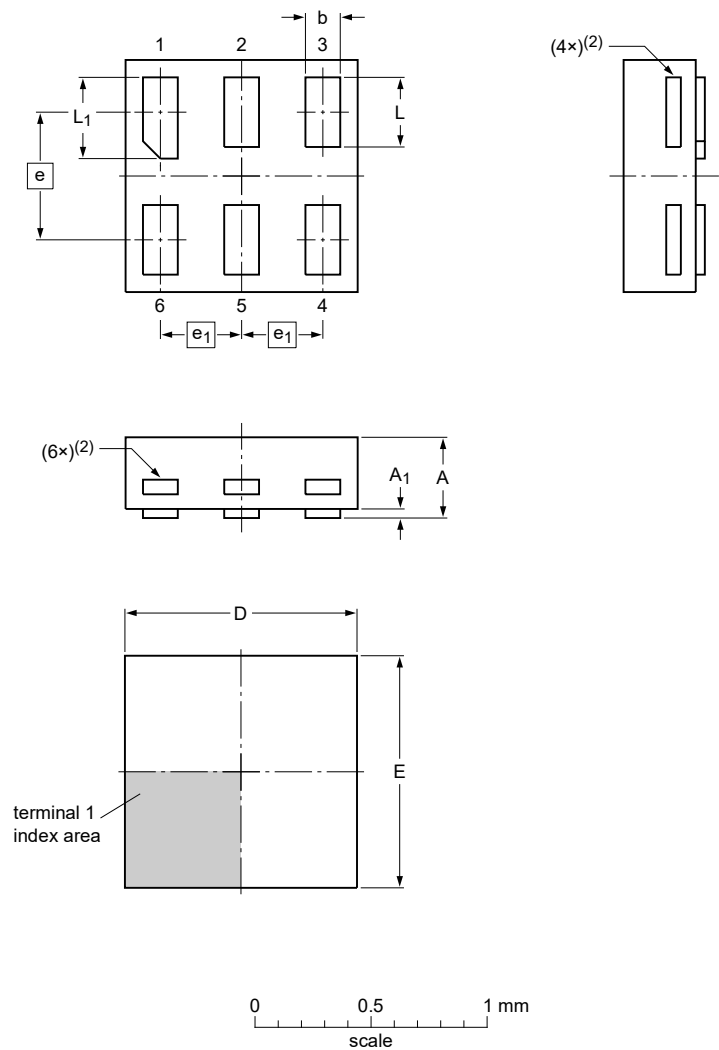


Fig. 9. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Dimensions

| Unit | A <sup>(1)</sup> | A <sub>1</sub> | b    | D    | E    | e    | e <sub>1</sub> | L    | L <sub>1</sub> |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm   | max              | 0.35           | 0.04 | 0.20 | 1.05 | 1.05 |                | 0.35 | 0.40           |
|      | nom              |                |      | 0.15 | 1.00 | 1.00 | 0.55           | 0.30 | 0.35           |
|      | min              |                |      | 0.12 | 0.95 | 0.95 |                | 0.27 | 0.32           |

Note

- 1. Including plating thickness.
- 2. Visible depending upon used manufacturing technology.

sot1202\_po

| Outline version | References |       |       |  | European projection                                                                   | Issue date                      |
|-----------------|------------|-------|-------|--|---------------------------------------------------------------------------------------|---------------------------------|
|                 | IEC        | JEDEC | JEITA |  |                                                                                       |                                 |
| SOT1202         |            |       |       |  |  | <del>10-04-02</del><br>10-04-06 |

Fig. 10. Package outline SOT1202 (XSON6)

X2SON6: plastic thermal enhanced extremely thin small outline package; no leads;  
6 terminals; body 1.0 x 0.8 x 0.32 mm

SOT1255-2

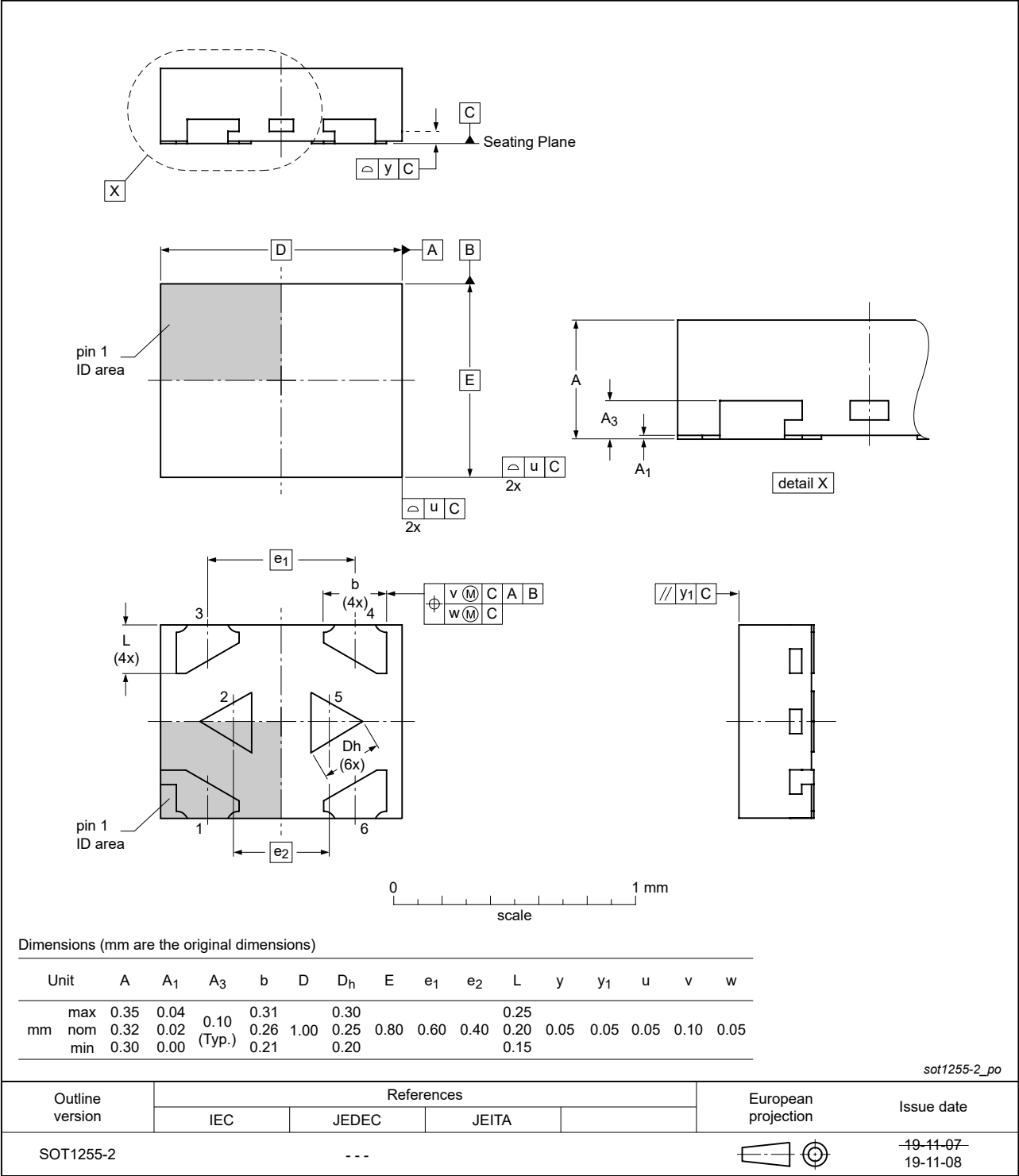


Fig. 11. Package outline SOT1255-2 (X2SON6)

## 13. Abbreviations

Table 11. Abbreviations

| Acronym | Description                             |
|---------|-----------------------------------------|
| CDM     | Charged Device Model                    |
| CMOS    | Complementary Metal-Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| HBM     | Human Body Model                        |
| TTL     | Transistor-Transistor Logic             |

## 14. Revision history

Table 12. Revision history

| Document ID    | Release date                                                                                                                                                                                                                                                                                                                                                                                                                        | Data sheet status     | Change notice | Supersedes     |
|----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|---------------|----------------|
| 74LVC2G04 v.13 | 20230815                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.12 |
| Modifications: | <ul style="list-style-type: none"> <li><a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li> </ul>                                                                                                                                                                                                                                                                                      |                       |               |                |
| 74LVC2G04 v.12 | 20220126                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.11 |
| Modifications: | <ul style="list-style-type: none"> <li>Package SOT363 (SC-88) changed to SOT363-2 (TSSOP6).</li> </ul>                                                                                                                                                                                                                                                                                                                              |                       |               |                |
| 74LVC2G04 v.11 | 20210929                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.10 |
| Modifications: | <ul style="list-style-type: none"> <li>Type number 74LVC2G04GF (SOT891) removed.</li> <li>SOT1255 (X2SON6) package changed to SOT1255-2 (X2SON6) package.</li> <li><a href="#">Table 5</a>: Derating values for <math>P_{tot}</math> total power dissipation updated.</li> <li><a href="#">Fig. 7</a>: Package outline drawing SOT457 updated.</li> <li><a href="#">Section 1</a> and <a href="#">Section 2</a> updated.</li> </ul> |                       |               |                |
| 74LVC2G04 v.10 | 20170522                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.9  |
| Modifications: | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Pin 6 description updated. <a href="#">Section 6.2</a></li> </ul>                                                                                                                           |                       |               |                |
| 74LVC2G04 v.9  | 20161212                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.8  |
| Modifications: | <ul style="list-style-type: none"> <li><a href="#">Table 7</a>: The maximum limits for leakage current and supply current have changed.</li> </ul>                                                                                                                                                                                                                                                                                  |                       |               |                |
| 74LVC2G04 v.8  | 20150917                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.7  |
| Modifications: | <ul style="list-style-type: none"> <li>Added type number 74LVC2G04GX (SOT1255/X2SON6).</li> </ul>                                                                                                                                                                                                                                                                                                                                   |                       |               |                |
| 74LVC2G04 v.7  | 20140910                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.6  |
| Modifications: | <ul style="list-style-type: none"> <li>Package outline drawing of SOT886 (<a href="#">Fig. 8</a>) modified.</li> </ul>                                                                                                                                                                                                                                                                                                              |                       |               |                |
| 74LVC2G04 v.6  | 20111206                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.5  |
| 74LVC2G04 v.5  | 20100805                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.4  |
| 74LVC2G04 v.4  | 20070725                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.3  |
| 74LVC2G04 v.3  | 20070216                                                                                                                                                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74LVC2G04 v.2  |
| 74LVC2G04 v.2  | 20040915                                                                                                                                                                                                                                                                                                                                                                                                                            | Product specification | -             | 74LVC2G04 v.1  |
| 74LVC2G04 v.1  | 20030722                                                                                                                                                                                                                                                                                                                                                                                                                            | Product specification | -             | -              |

## 15. Legal information

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| Document status [1][2]         | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

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