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# **HAT2137H**

# Silicon N Channel Power MOS FET Power Switching

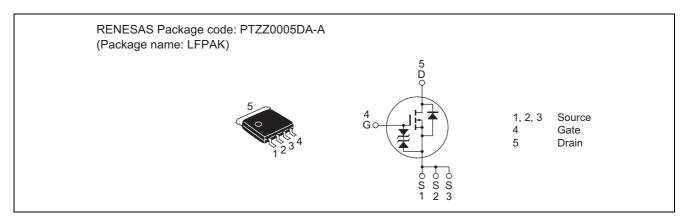
REJ03G1191-0400 (Previous: ADE-208-1579B)

> Rev.4.00 Sep 07, 2005

#### **Features**

- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS \; (on)} = 3.8 \; m\Omega \; typ. \; (at \; V_{GS} = 10 \; V) \label{eq:DS}$

### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	40	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	45	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	180	A
Body-drain diode reverse drain current	I <sub>DR</sub>	45	A
Avalanche current	I <sub>AP</sub> Note 3	30	A
Avalanche energy	E <sub>AR</sub> Note 3	72	mJ
Channel dissipation	Pch Note 2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Tc = 25 °C

3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

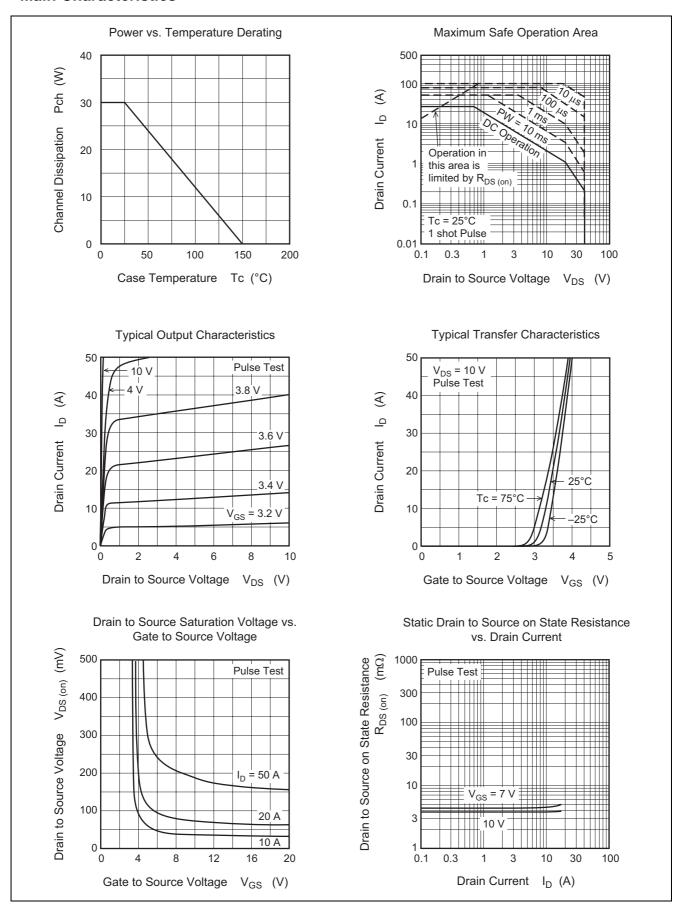
## **Electrical Characteristics**

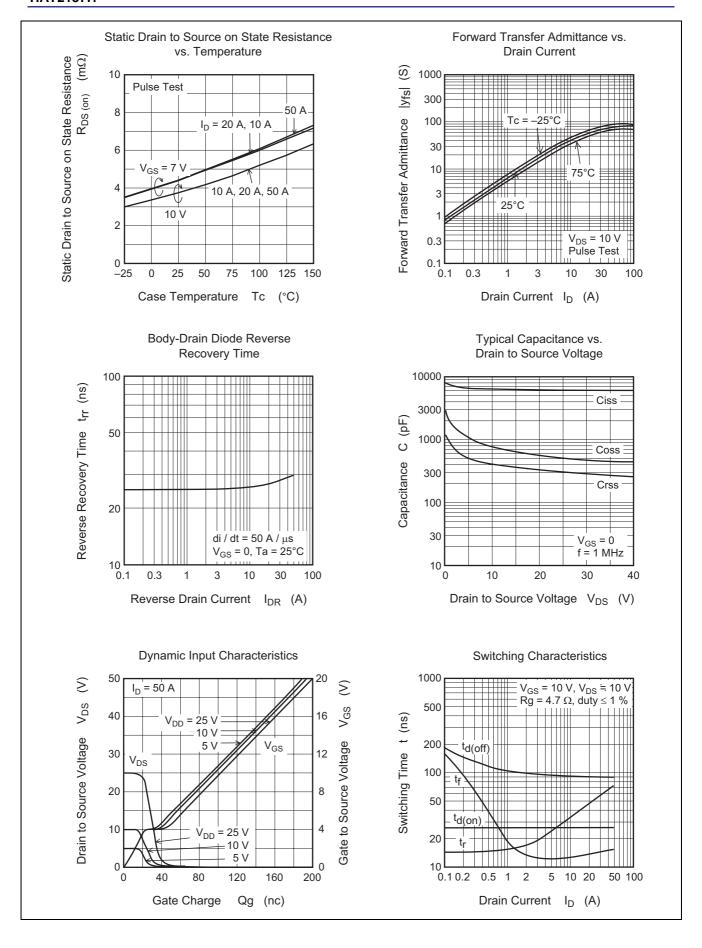
 $(Ta = 25^{\circ}C)$ 

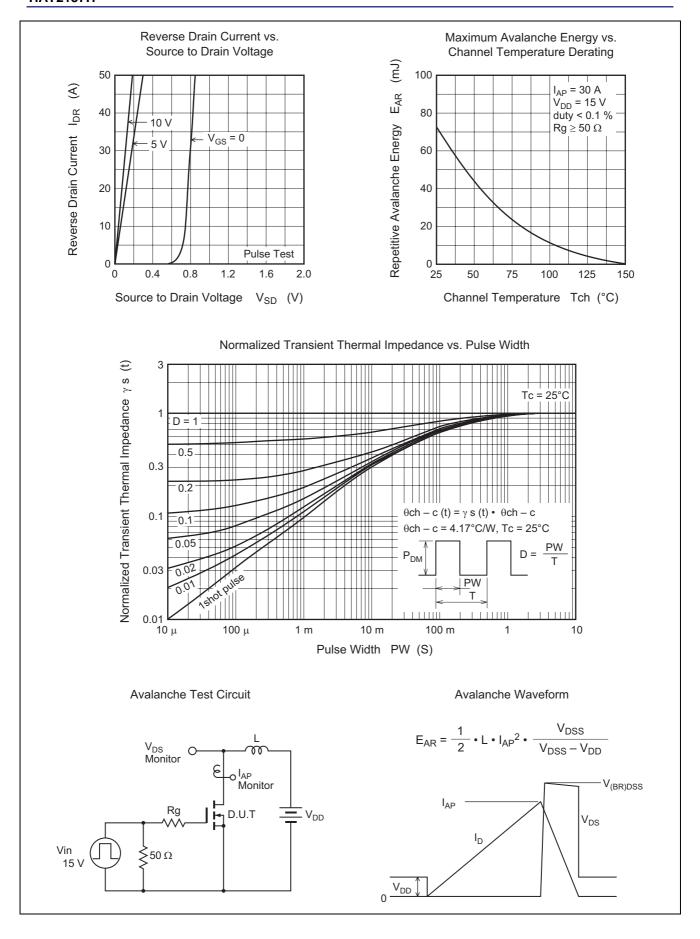
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	2.0	_	3.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	3.8	4.8	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
	R <sub>DS (on)</sub>	_	4.4	6.0	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 7 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y <sub>fs</sub>	38	64	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	6200	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	780	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	410	_	pF	f = 1 MHz
Total gate charge	Qg	_	95	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	24	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	14	_	nC	I <sub>D</sub> = 45 A
Turn-on delay time	t <sub>d (on)</sub>	_	27	_	ns	$V_{GS} = 10 \text{ V}, I_D = 22.5 \text{ A}$
Rise time	t <sub>r</sub>	_	50	_	ns	V <sub>DD</sub> ≅ 10 V
Turn-off delay time	t <sub>d (off)</sub>	_	90	_	ns	$R_L = 0.44 \Omega$
Fall time	t <sub>f</sub>	_	14	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.10	V	$I_F = 45 \text{ A}, V_{GS} = 0^{\text{Note 4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	40	_	ns	I <sub>F</sub> = 45 A, V <sub>GS</sub> = 0
						di <sub>F</sub> /dt = 100 A/μs

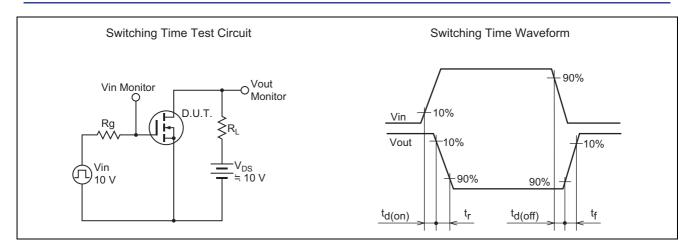
Note: 4. Pulse test

#### **Main Characteristics**

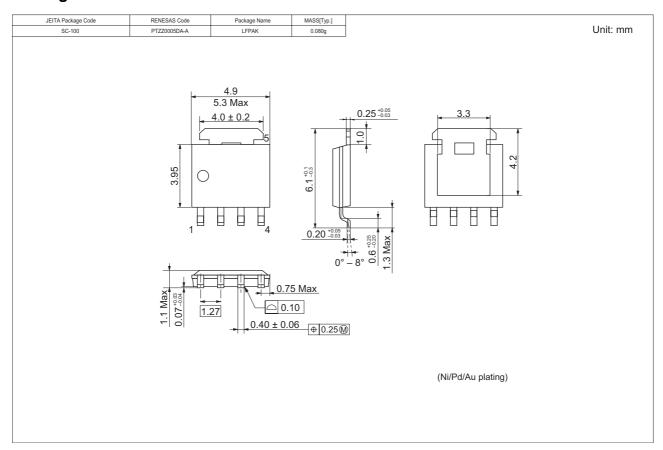








## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2137H-EL-E	2500 pcs	Taping

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