

## Product Summary

BV <sub>DSS</sub>	R <sub>DSON</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
-30V	10mΩ @ V <sub>GS</sub> = -10V	-40A
	18mΩ @ V <sub>GS</sub> = -4.5V	-25A

## Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DSON</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

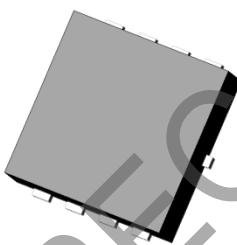
- Backlighting
- Power Management Functions
- DC-DC Converters

## Features and Benefits

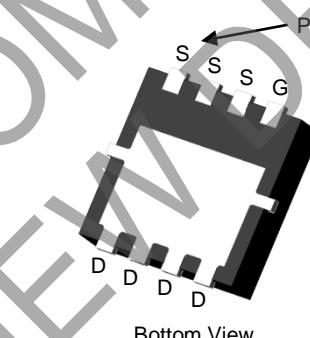
- Low R<sub>DSON</sub> – ensures on-state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free, "Green" Device (Note 3)**

## Mechanical Data

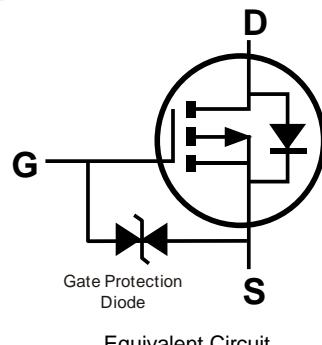
- Case: PowerDI 3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⑧3
- Weight: 0.072 grams (Approximate)



Top View



Bottom View



Equivalent Circuit

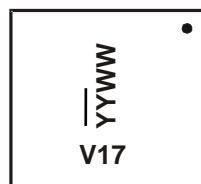
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3017SFV-7	PowerDI3333-8 (Type UX)	2,000/Tape & Reel
DMP3017SFV-13	PowerDI3333-8 (Type UX)	3,000/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



V17 = Product Type Marking Code  
YYWW = Date Code Marking  
YY = Last Two Digits of Year (ex: 17 = 2017)  
WW = Week Code (01 to 53)

## Maximum Ratings (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			$V_{GSS}$	$\pm 25$	V
Continuous Drain Current (Note 6) $V_{GS} = -10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	$I_D$	-11.5 -9.4	A
Continuous Drain Current (Note 7) $V_{GS} = -10\text{V}$	Steady State	$T_C = +25^\circ\text{C}$ $T_C = +70^\circ\text{C}$	$I_D$	-40 -30	A
Maximum Continuous Body Diode Forward Current (Note 7)			$I_S$	-30	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)			$I_{DM}$	-80	A
Pulsed Body Diode Forward Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)			$I_{SM}$	-80	A
Avalanche Current (Note 8) $L = 1\text{mH}$			$I_{AS}$	-14	A
Avalanche Energy (Note 8) $L = 1\text{mH}$			$E_{AS}$	104	mJ

## Thermal Characteristics (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	0.94	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	134	°C/W
Total Power Dissipation (Note 6)	$P_D$	1.94	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	65	°C/W
Total Power Dissipation (Note 7)	$P_D$	31	W
Thermal Resistance, Junction to Case (Note 7)	$R_{\theta JC}$	4.0	°C/W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C

## Electrical Characteristics (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-30	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	-1	$\mu\text{A}$	$V_{DS} = -24\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 25\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.0	—	-3.0	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	8.5	10	$\text{m}\Omega$	$V_{GS} = -10\text{V}, I_D = -11.5\text{A}$
		—	15	18		$V_{GS} = -4.5\text{V}, I_D = -8.5\text{A}$
Diode Forward Voltage	$V_{SD}$	—	-0.7	-1.2	V	$V_{GS} = 0\text{V}, I_S = -1\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	$C_{iss}$	—	2,246	—	pF	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	—	352	—	pF	
Reverse Transfer Capacitance	$C_{rss}$	—	294	—	pF	$V_{DS} = -15\text{V}, I_D = -11.5\text{A}$
Gate Resistance	$R_g$	—	5.1	—	$\Omega$	
Total Gate Charge ( $V_{GS} = -5\text{V}$ )	$Q_g$	—	20.5	—	nC	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, R_G = 6\Omega, I_D = -11.5\text{A}$
Total Gate Charge ( $V_{GS} = -10\text{V}$ )	$Q_g$	—	41	—	nC	
Gate-Source Charge	$Q_{gs}$	—	7.6	—	nC	$I_S = -11.5\text{A}, dI/dt = 100\text{A}/\mu\text{s}$
Gate-Drain Charge	$Q_{gd}$	—	8.0	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	7.5	—	ns	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, R_G = 6\Omega, I_D = -11.5\text{A}$
Turn-On Rise Time	$t_R$	—	15.4	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	45.6	—	ns	
Turn-Off Fall Time	$t_F$	—	36.8	—	ns	
Reverse Recovery Time	$t_{RR}$	—	20	—	ns	$I_S = -11.5\text{A}, dI/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	$Q_{RR}$	—	9.5	—	nC	

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
  7. Thermal resistance from junction to soldering point (on the exposed drain pad).
  8.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^\circ\text{C}$ .
  9. Short duration pulse test used to minimize self-heating effect.
  10. Guaranteed by design. Not subject to product testing.

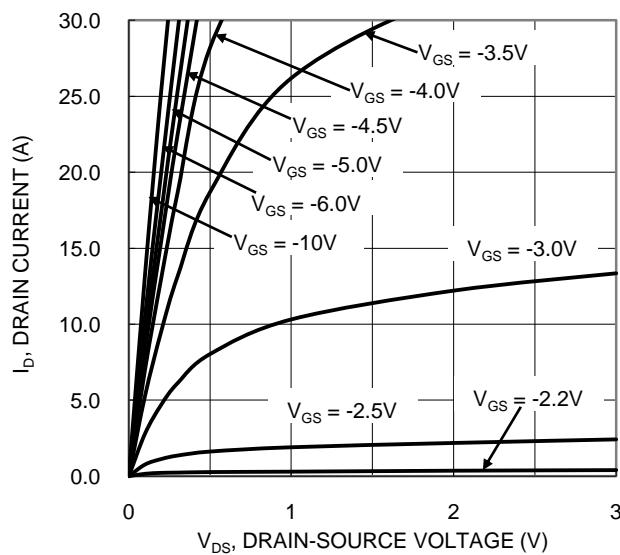


Figure 1. Typical Output Characteristic

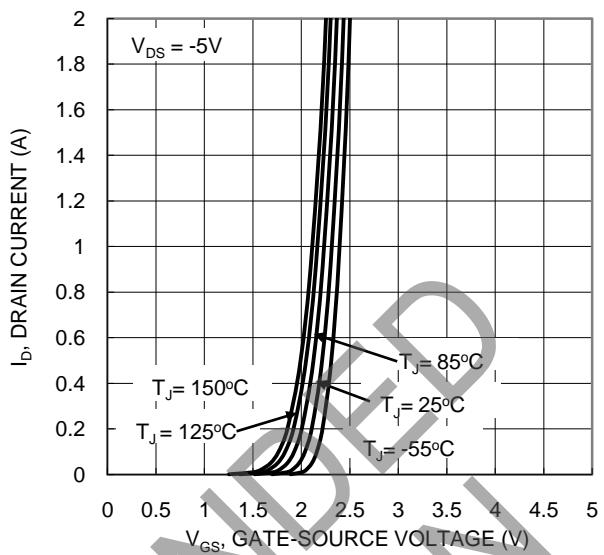


Figure 2. Typical Transfer Characteristic

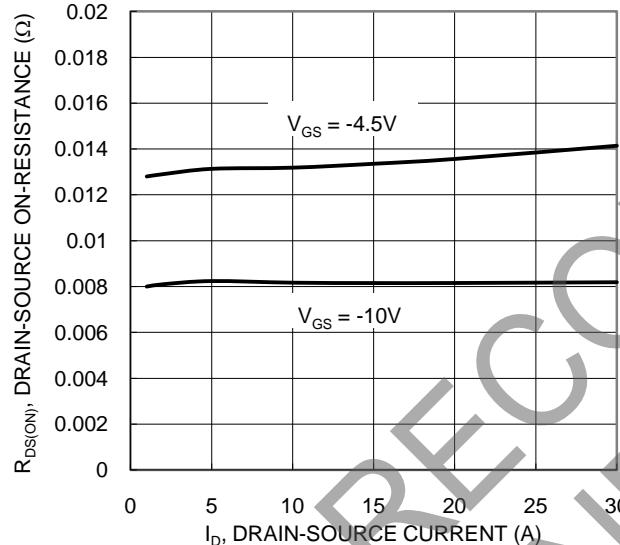


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

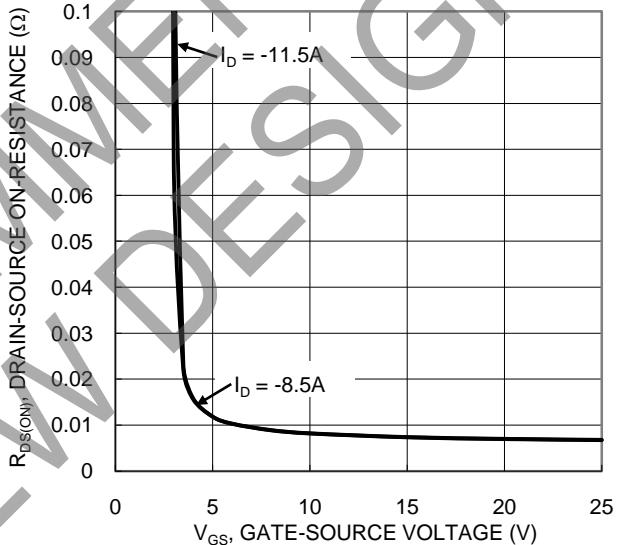


Figure 4. Typical Transfer Characteristic

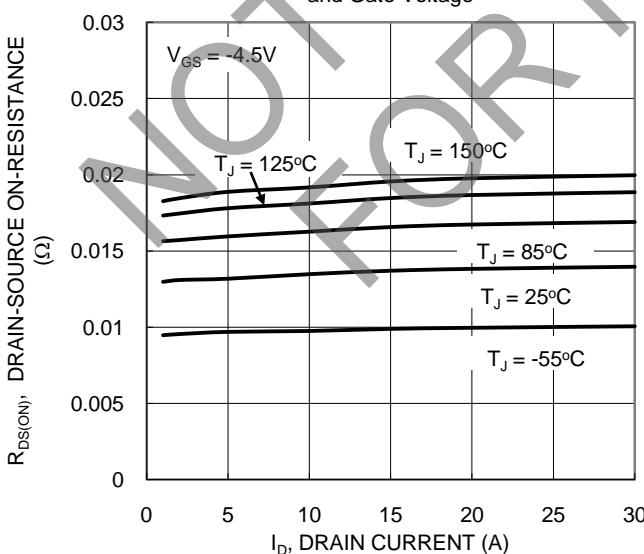


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

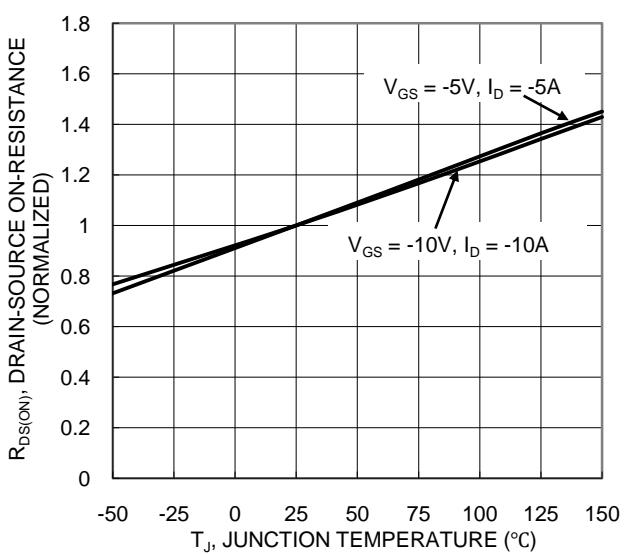


Figure 6. On-Resistance Variation with Temperature

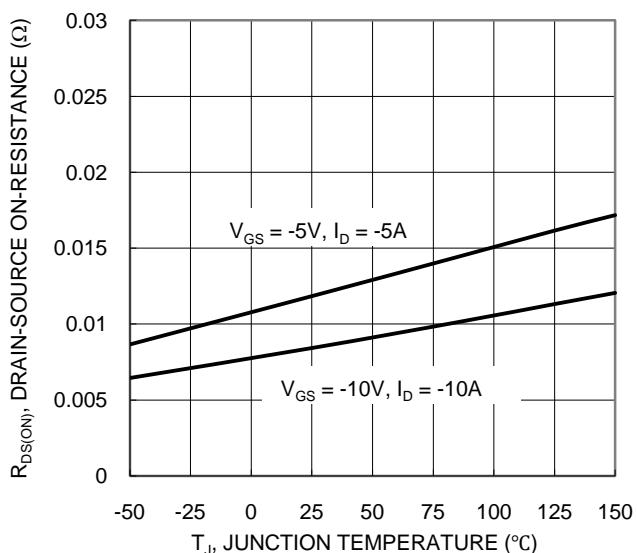


Figure 7. On-Resistance Variation with Temperature

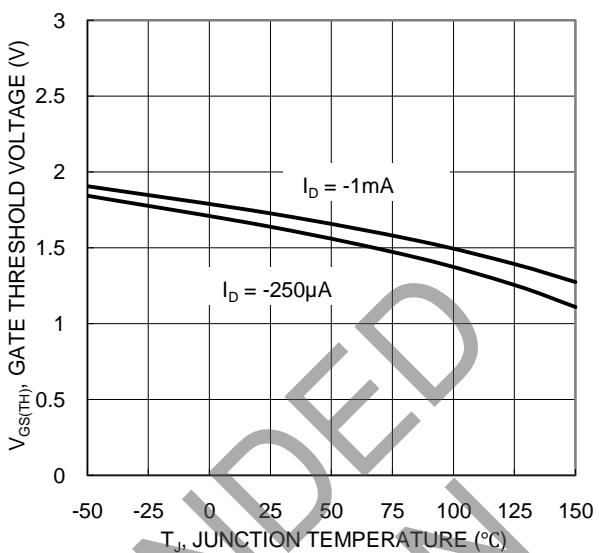


Figure 8. Gate Threshold Variation vs. Junction Temperature

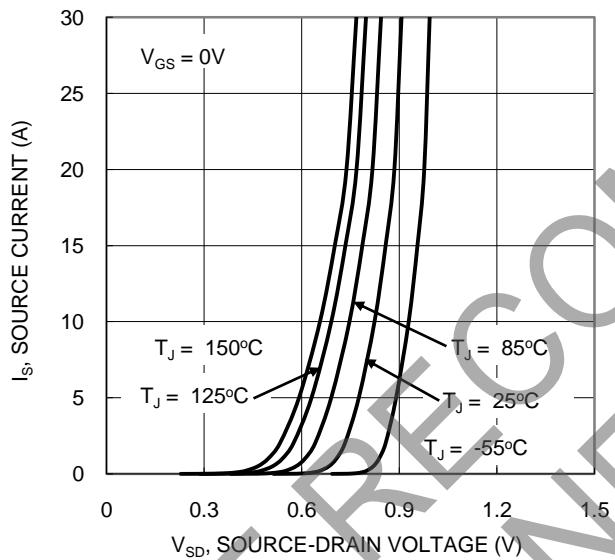


Figure 9. Diode Forward Voltage vs. Current

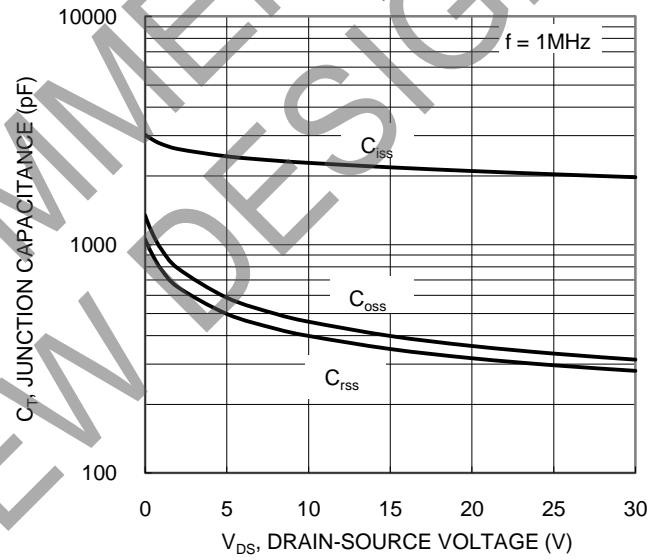


Figure 10. Typical Junction Capacitance

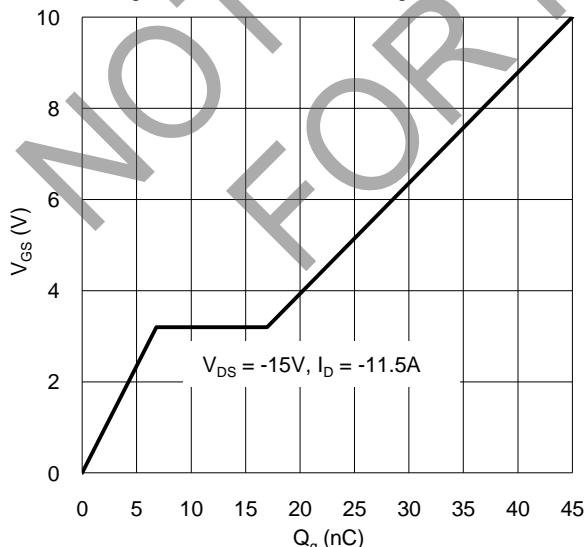


Figure 11. Gate Charge

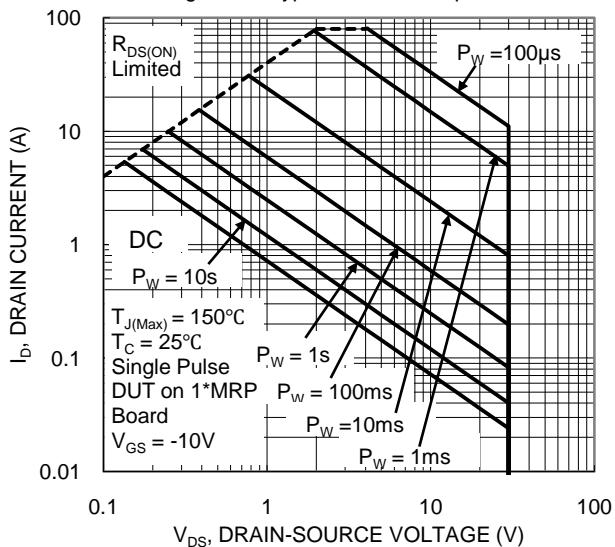
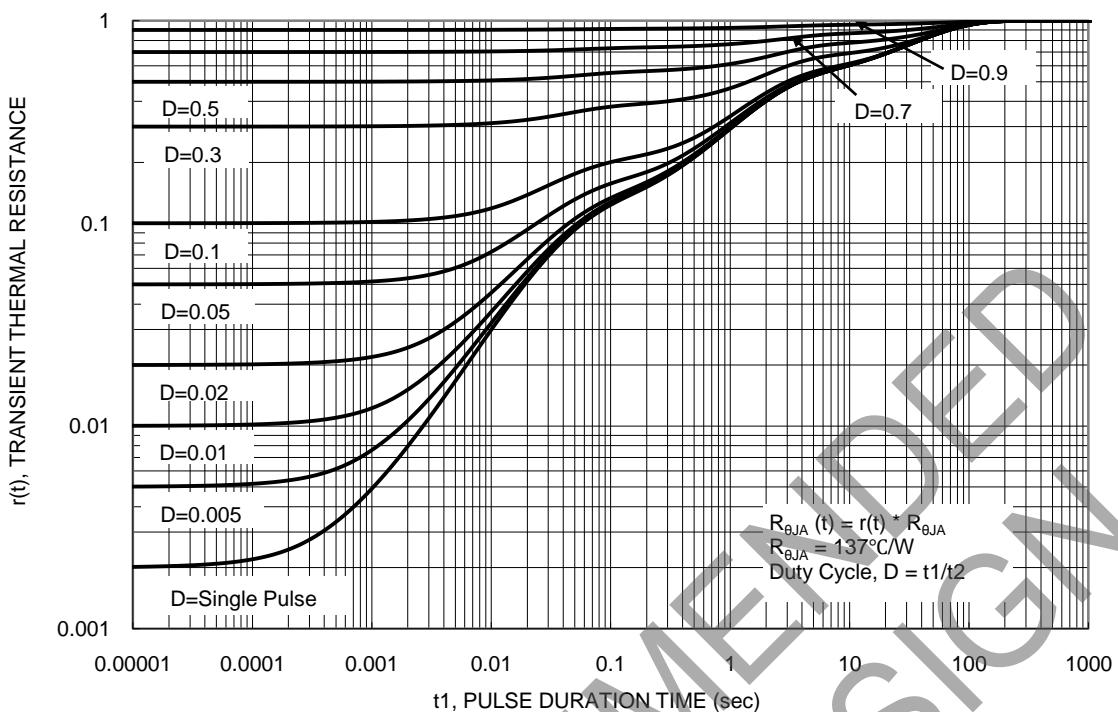


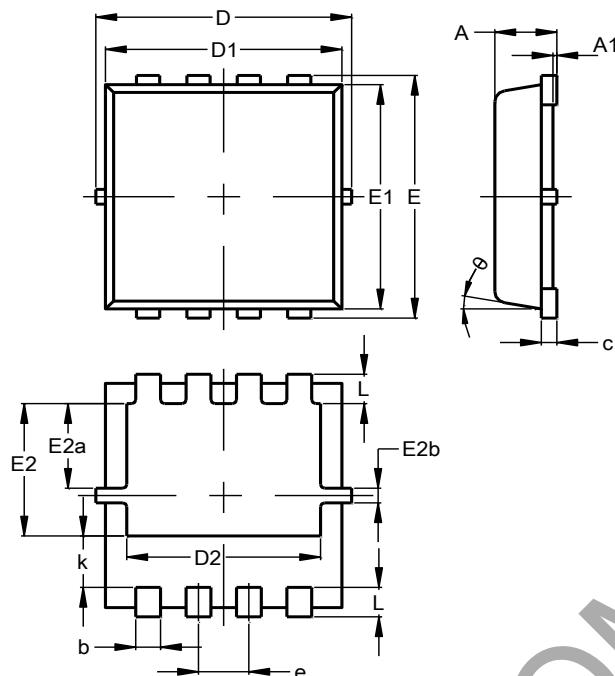
Figure 12. SOA, Safe Operation Area



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)

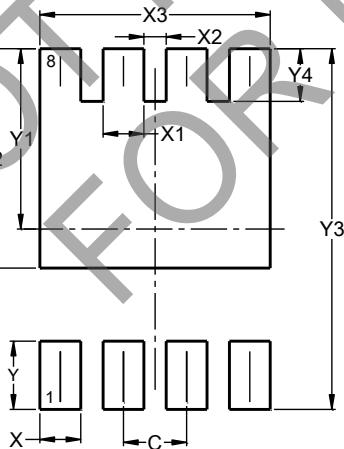


PowerDI3333-8 (Type UX)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	2.30	2.70	2.50
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E2a	0.95	1.35	1.15
E2b	0.10	0.30	0.20
e	0.65 BSC		
k	0.50	0.90	0.70
L	0.30	0.50	0.40
$\theta$	0°	12°	10°
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540

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