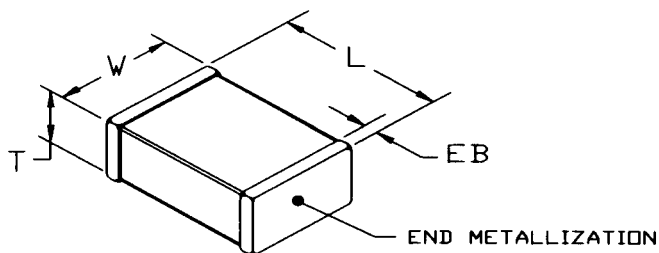


ROHS COMPLIANT



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	NPO
RATED VOLTAGE	100 VDC
CAPACITANCE RANGE (EIA)	10 pFd (100)
CAPACITANCE TOLERANCE	J ($\pm 5\%$)
TEST PARAMETERS	1 MHz $\pm$ 50 Hz, Values $\leq$ 100pF @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	0 $\pm$ 30ppm / °C (-55°C TO +125°C)
DISSIPATION FACTOR	0.1% Max, 25°C
INSULATION RESISTANCE	> 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	> 2.5 x WVDC, 50 mA max

MECHANICAL CHARACTERISTICS

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.040 $\pm$.010	.030 $\pm$.010	.030 MAX	.010 $\pm$.005

TERMINATION DESCRIPTION:

TERMINATION: P - PALLADIUM SILVER

PACKAGING DESCRIPTION:

PACKAGE: T - 7" PAPER TAPE/REEL

MARKING DESCRIPTION:

4 - UNMARKED

				JOHANSON DIELECTRICS 		15191 BLEDSOE ST SYLMAR, CA 91342		
				TITLE				
REV	DESCRIPTION	APPROVED	DATE	MULTILAYER CHIP CAPACITOR				
ENG. MGR.		DATE	Q.A. MGR.	DATE	PART NO. 101R09N100JP4T		DWG SCALE NONE	
DRN BY C. ENGLAND		DATE 11/25/09	CHKD BY	DATE	DWG. NO. 101R09N100JP4T		SHEET 1 OF 1	RE V