

LM118/218/318

High-Speed Operational Amplifier

Distinctive Characteristics

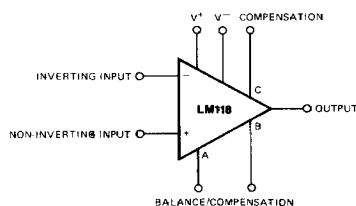
- The LM118/218/318 are functionally, electrically, and pin-for-pin equivalent to the National LM118/218/318
- Slew rate: 70V/ μ s
- Small signal bandwidth: 15MHz
- Internal frequency compensation
- Supply voltage range: ± 5 V to ± 20 V

- Electrically tested and optically inspected dice for hybrid manufacturers.
- Available in metal can, hermetic dual-in-line, hermetic flat package or plastic minidip.

FUNCTIONAL DESCRIPTION

The LM118/218/318 are internally compensated high-speed operational amplifiers featuring minimum slew rate of 50V/ μ s, low input bias currents, large input voltage range and excellent performance over a wide range of supply voltages and temperature. They have provision for increased speeds when operating in the inverting mode.

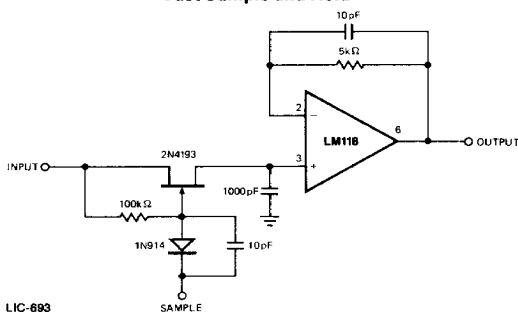
FUNCTIONAL DIAGRAM



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TYPICAL APPLICATIONS

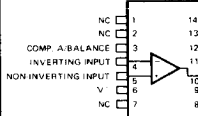
Fast Sample and Hold



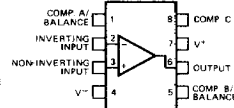
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CONNECTION DIAGRAMS – Top Views

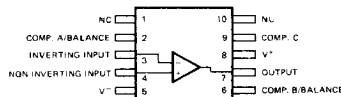
Hermetic Dual In-Line D-14-1



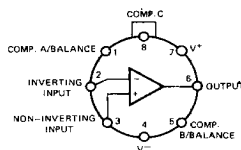
Molded Dual In-Line P-8-1



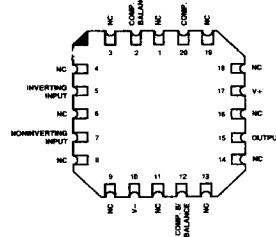
Flat Package F-10-1



Metal Can H-8-1



Leadless Chip-Pak L-20-1



Note: 1. On Metal Can, pin 4 is connected to case.

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ORDERING INFORMATION*

Part Number	Package Type	Temperature Range	Order Number
LM318	Metal Can	0 to +70°C	LM318H
	Hermetic DIP	0 to +70°C	LM318D
	Flat Package	0 to +70°C	LM318F
	Molded DIP	0 to +70°C	LM318N
	Dice	0 to +70°C	LD318
LM218	Leadless	0 to +70°C	LM318L
	Metal Can	-25 to +85°C	LM218H
	Hermetic DIP	-25 to +85°C	LM218D
	Flat Pak	-25 to +85°C	LM218F
LM118	Leadless	-25 to +85°C	LM218L
	Metal Can	-55 to +125°C	LM118H
	Hermetic DIP	-55 to +125°C	LM118D
	Flat Package	-55 to +125°C	LM118F
	Dice	-55 to +125°C	LD118
	Leadless	-55 to +125°C	LM118L

*Also available with burn-in processing. To order add suffix B to part number.

LM118/218/318 MAXIMUM RATINGS

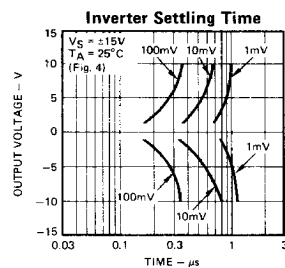
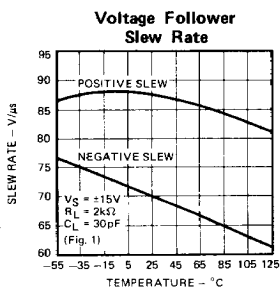
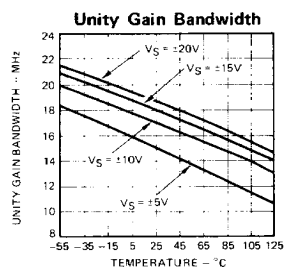
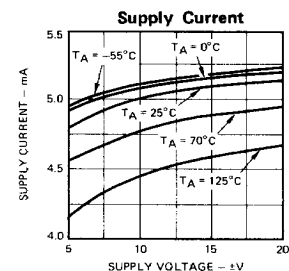
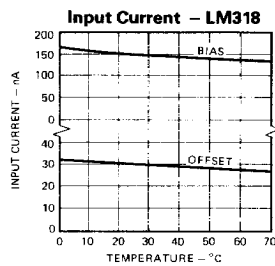
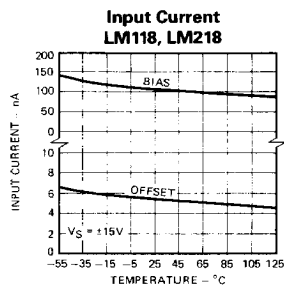
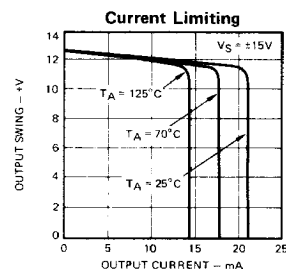
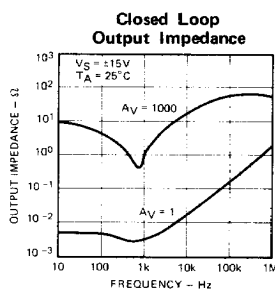
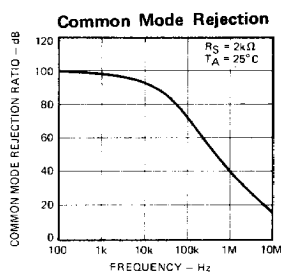
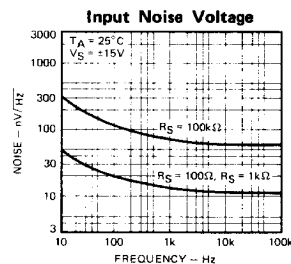
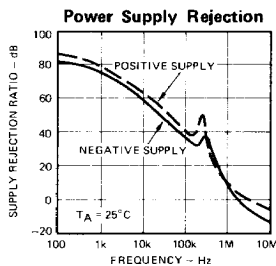
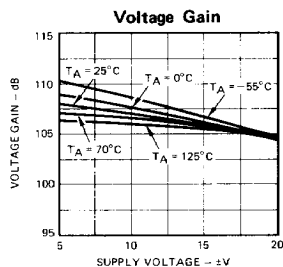
Supply Voltage	±20V
Internal Power Dissipation (Note 1)	500mW
Differential Input Voltage (Note 2)	±5V
Input Voltage (Note 3)	±15V
Output Short-Circuit Duration	Indefinite
Operating Temperature Range	
LM118	−55°C to +125°C
LM218	−25°C to +85°C
LM318	0°C to +70°C
Storage Temperature Range	−65°C to +150°C
Lead Temperature (Soldering, 60 sec.)	300°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified) (Note 4)

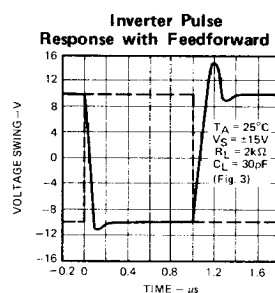
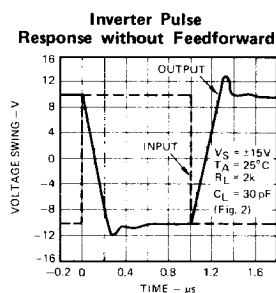
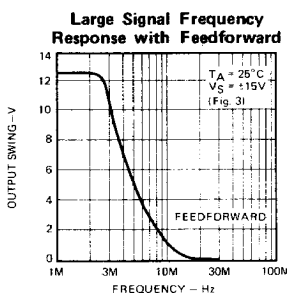
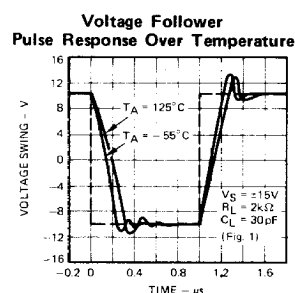
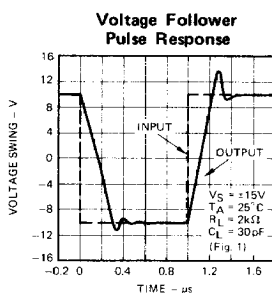
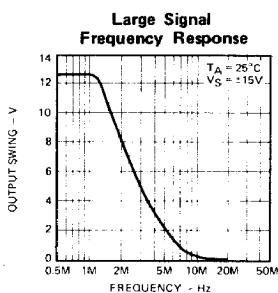
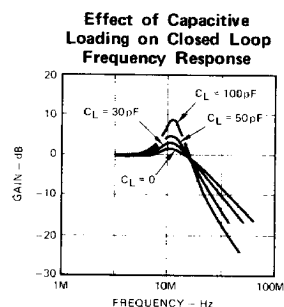
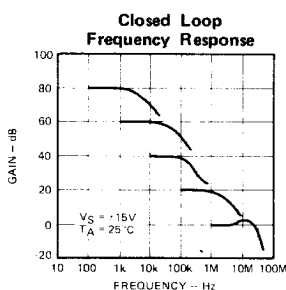
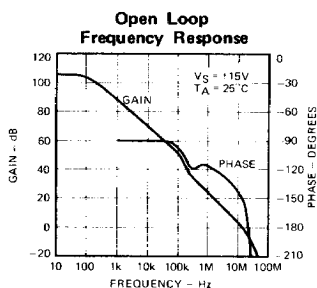
Parameter (see definitions)	Conditions	LM318			LM118 LM218			Units
		Min.	Typ.	Max.	Min.	Typ.	Max.	
Input Offset Voltage	$R_S \leq 5\text{k}\Omega$		4	10		2	4	mV
Input Offset Current			30	200		6	50	nA
Input Bias Current			150	500		120	250	nA
Input Resistance		0.5	3		1.0	3		M Ω
Supply Current	$V_S = \pm 20\text{V}$		5	10		5	8	mA
Large Signal Voltage Gain	$V_S = \pm 15\text{V}$, $V_{OUT} = \pm 10\text{V}$ $R_L \geq 2\text{k}\Omega$	25	200		50	200		V/mV
Slew Rate	$A_V = +1$, $V_S = \pm 15\text{V}$ (Fig.1) $R_L = 2\text{k}\Omega$, $C_L = 30\text{pF}$	50	70		50	70		V/ μs
Small Signal Bandwidth	$V_S = \pm 15\text{V}$		15			15		MHz
The Following Specifications Apply Over The Operating Temperature Ranges								
Input Offset Voltage	$R_S \leq 5\text{k}\Omega$			15			6	mV
Input Offset Current				300			100	nA
Input Bias Current				750			500	nA
Large Signal Voltage Gain	$V_S = \pm 15\text{V}$, $V_{OUT} = \pm 10\text{V}$ $R_L \geq 2\text{k}\Omega$	20			25			V/mV
Input Voltage Range	$V_S = \pm 15\text{V}$	±11.5			±11.5			V
Common Mode Rejection Ratio	$R_S \leq 5\text{k}\Omega$		70			80		dB
Supply Voltage Rejection Ratio	$R_S \leq 5\text{k}\Omega$		65			70		dB
Output Voltage Swing	$V_S = \pm 15\text{V}$, $R_L = 2\text{k}\Omega$	±12	±13		±12	±13		V
Supply Current	$V_S = \pm 20\text{V}$, $T_A = 125^\circ\text{C}$						7	mA

- Notes: 1. Derate Metal Can package at 6.8 mW/ $^\circ\text{C}$ for operation at ambient temperatures above 75 $^\circ\text{C}$, the Dual-In-Line package at 9 mW/ $^\circ\text{C}$ for operation at ambient temperatures above 95 $^\circ\text{C}$, and the Flat Package at 5.4 mW/ $^\circ\text{C}$ for operation at ambient temperatures above 57 $^\circ\text{C}$.
2. The inputs are shunted with diodes for overvoltage protection. To limit the current in the protection diodes, resistances of 2 k Ω or greater should be inserted in series with the input leads for differential input voltages greater than ±5 V.
3. For supply voltages less than ±15 V, the maximum input voltage is equal to the supply voltage.
4. Unless otherwise specified, these specifications apply for supply voltages from ±5 V to ±20 V.

PERFORMANCE CURVES



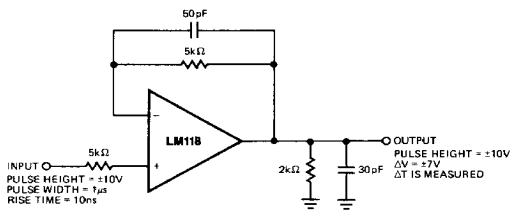
PERFORMANCE CURVES



The high gain and large bandwidth of the LM118 make it mandatory to observe the following precautions in using the device, as is the case with any high-frequency amplifier. Circuit layout should be arranged to keep all lead lengths as short as possible and the output separated from the inputs. The values of the feedback and source impedances should be kept small to reduce the effect of stray capacitance at the inputs. The power supplies must be bypassed to ground at the supply leads of the amplifier with low inductance capacitors. Capacitive loading must be kept to minimum, or the amplifier must be isolated as shown in the applications.

APPLICATIONS

**Voltage Follower
(Slew Rate Test Circuit)**



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Figure 1

Inverter

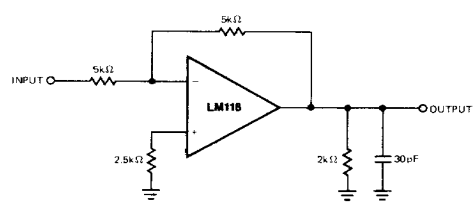
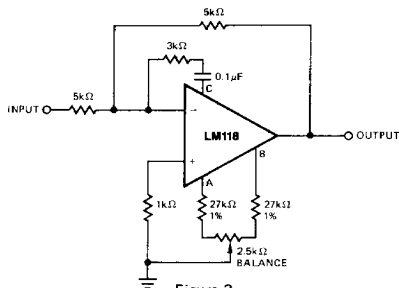


Figure 2

LIC-698

**Inverter with Feedforward
Compensation for Higher Slew Rate**



LIC-699

Figure 3

**Compensation for
Minimum Settling Time**

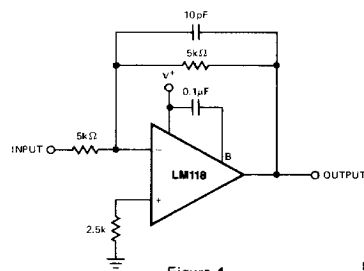
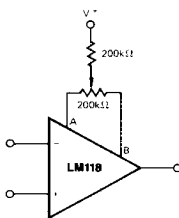


Figure 4

LIC-700

Offset Nulling



LIC-701

Figure 5

**Isolating Large
Capacitive Loads**

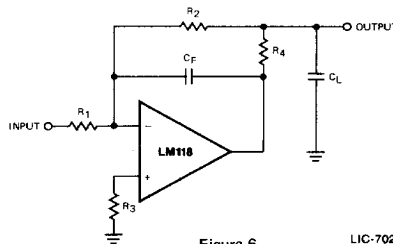


Figure 6

LIC-702

Over Compensation

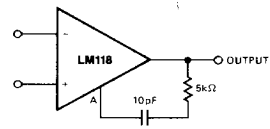
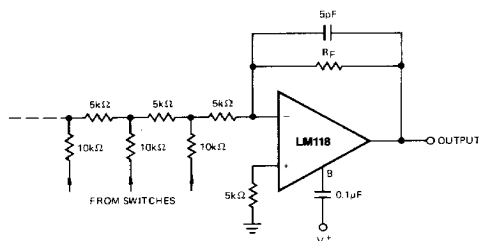


Figure 7

LIC-703

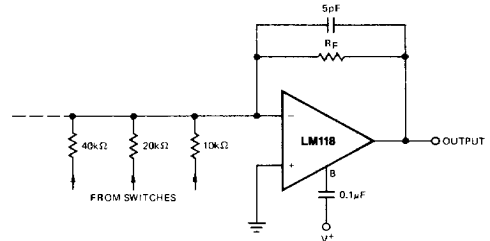
**D/A Converter
with Ladder Network**



LIC-704

Figure 8

**D/A Converter
with Binary Network**



LIC-705

Wien Bridge Oscillator

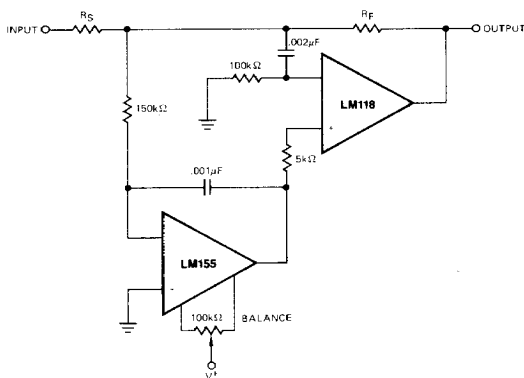


Figure 10

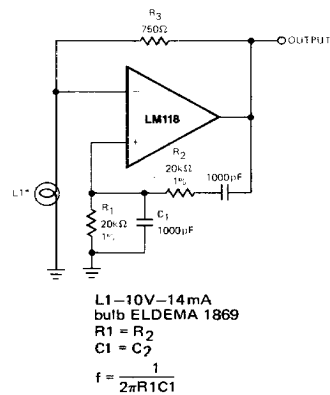
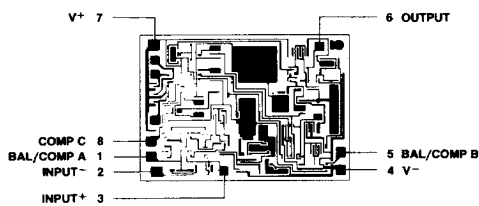


Figure 11

LIC-706

LIC-707

Metalization and Pad Layout



DIE SIZE: 0.065" X 0.087"