



Precision Monolithic Quad SPST CMOS Analog Switches

DESCRIPTION

The DG1411, DG1412, DG1413 are ± 15 V precision monolithic quad single-pole single-throw (SPST) CMOS analog switches. Built on a new CMOS process, the Vishay Siliconix DG1411, DG1412, and DG1413 offer low on-resistance of $1.5\ \Omega$. The low and flat resistance over the full signal range ensures excellent linearity and low signal distortion. The new CMOS platform provides low power dissipation, minimized parasitic capacitance, and low charge injection.

The devices operate from either a single 4.5 V to 24 V power supply, or from dual ± 4.5 V to ± 15 V power supplies. The analog switches don't require a V_L logic supply, while all digital inputs have 0.8 V and 2 V logic thresholds to ensure low-voltage TTL / CMOS compatibility.

The DG1411, DG1412, and DG1413 are bi-directional and support analog signals up to the supply voltage when on, and block them when off. The devices each feature four independently selectable SPST switches. The DG1411 is normally closed, while the DG1412 is normally open. The DG1413 has two normally open and two normally closed switches with guaranteed break-before-make operation.

Combined with fast 100 ns switching times, the improved performance of the DG1411, DG1412, and DG1413 make the devices ideal for signal switching and relay replacement in data acquisition, industrial control and automation, communication, and A/V systems, in addition to medical instrumentation and automated test equipment.

The switches are available in RoHS-compliant, halogen-free TSSOP16 and QFN16 4 mm by 4 mm packages.

FEATURES

- 35 V supply max. rating
- On-resistance: $1.5\ \Omega$
- On-resistance flatness: $0.3\ \Omega$
- Channel to channel ON-resistance match: $0.1\ \Omega$
- Supports single and dual supply operation
- Fully specified at ± 15 V, ± 5 V, and +12 V
- Integrated V_L supply
- 3 V logic compatible
- Low parasitic capacitance: $C_{S(OFF)}$: 11 pF, $C_{D(ON)}$: 87 pF
- Rail to rail signal handling
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

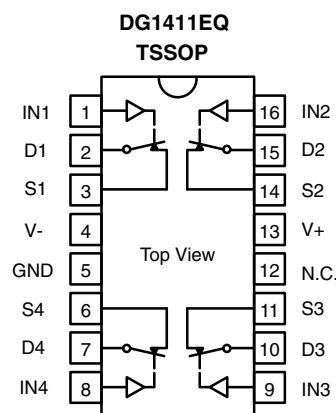
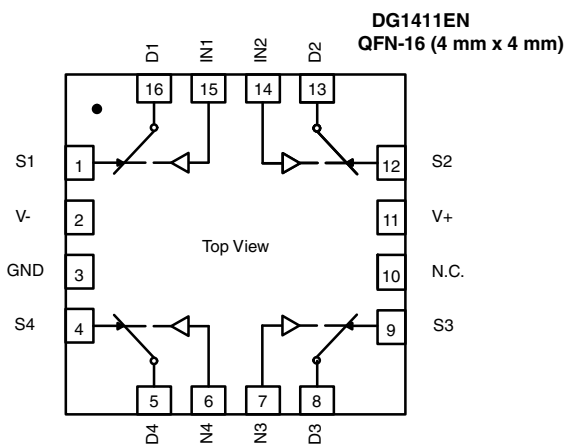
BENEFITS

- Low insertion loss
- Low distortion
- Break-before-make switching
- Low charge injection over the full signal range

APPLICATIONS

- Medical and Healthcare equipment
- Data acquisition system
- Industrial control and automation
- Test and measurement equipment
- Communication systems
- Battery powered systems
- Sample and hold circuits
- Audio and video signal switching
- Relay replacement

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION - DG1411

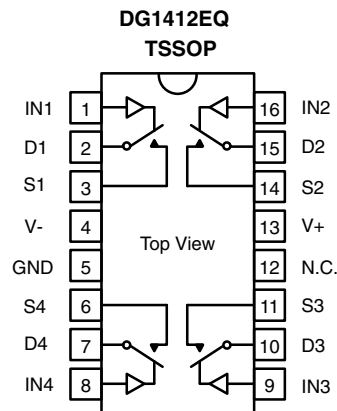
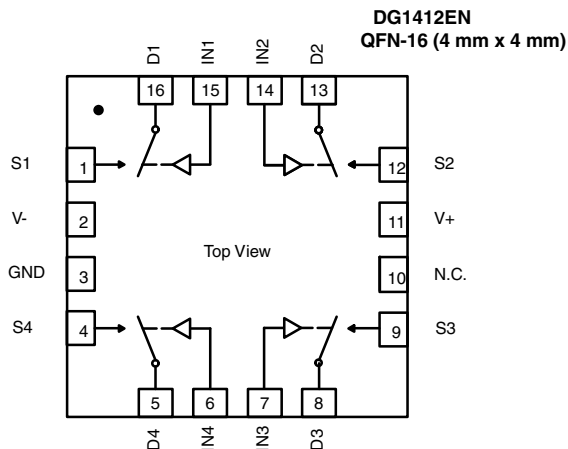


TRUTH TABLE - DG1411

LOGIC	SWITCH
0	On
1	Off

Notes

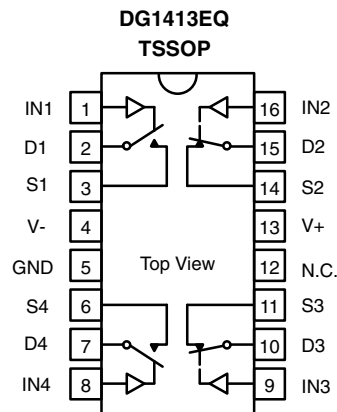
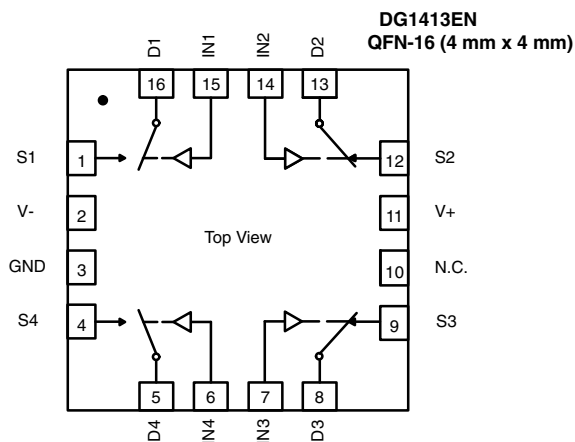
- QFN EXPOSED PAD TIED TO V-
- N.C. = NO CONNECT
- Switches Shown for Logic "0" Input

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION - DG1412

TRUTH TABLE - DG1412

LOGIC	SWITCH
0	Off
1	On

Notes

- QFN EXPOSED PAD TIED TO V-
- N.C. = NO CONNECT
- Switches Shown for Logic "0" Input

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION - DG1413

TRUTH TABLE - DG1413

LOGIC	SWITCHES 1, 4	SWITCHES 2, 3
0	Off	On
1	On	Off

Notes

- QFN EXPOSED PAD TIED TO V-
- N.C. = NO CONNECT
- Switches Shown for Logic "0" Input

**DEVICE OPTIONS**

PART NUMBER	CONFIGURATION	SWITCH FUNCTION	TEMPERATURE RANGE	PACKAGE
DG1411EN-T1-GE4	Quad SPST	NC	-40 °C to +125 °C	QFN (4 mm x 4 mm) 16L (Variation 2)
DG1412EN-T1-GE4	Quad SPST	NO	-40 °C to +125 °C	QFN (4 mm x 4 mm) 16L (Variation 2)
DG1413EN-T1-GE4	Quad SPST	NC/NO	-40 °C to +125 °C	QFN (4 mm x 4 mm) 16L (Variation 2)
DG1411EQ-T1-GE3	Quad SPST	NC	-40 °C to +125 °C	TSSOP-16
DG1412EQ-T1-GE3	Quad SPST	NO	-40 °C to +125 °C	TSSOP-16
DG1413EQ-T1-GE3	Quad SPST	NC/NO	-40 °C to +125 °C	TSSOP-16

ABSOLUTE MAXIMUM RATINGS

ELECTRICAL PARAMETER	CONDITIONS	LIMITS	UNIT
V+	Reference to GND	-0.3 V to +25 V	V
V-	Reference to GND	+0.3 V to -25 V	
V+ to V-		+35	
Analog Inputs (S or D)		V- (-0.3 V) to V+ (+0.3 V)	
Digital Inputs		GND (-0.3 V) to V+ (+0.3 V)	
Maximum Continuous Switch Current	TSSOP-16, T _A = 25 °C	190	mA
	QFN (4 mm x 4 mm) 16L, T _A = 25 °C	250	
	TSSOP-16, T _A = 125 °C	90	
	QFN (4 mm x 4 mm) 16L, T _A = 125 °C	100	
Maximum Pulse Switch Current	Pulse at 1 mS, 10 % duty cycle	500	
Thermal Resistance	TSSOP-16	130	°C/W
	QFN (4 mm x 4 mm) 16L	32	
Temperature			
Operating Temperature		-40 to 125	°C
Max. Operating Junction Temperature		150	
Operating Junction Temperature		125	
Storage Temperature		-65 to 150	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING RANGE

ELECTRICAL	MINIMUM	MAXIMUM	UNIT
IN	± 4.5	± 16.5	V



ELECTRICAL CHARACTERISTICS							
PARAMETER	SYMBOL	TEST CONDITIONS UNLESS OTHERWISE SPECIFIED V+ = 15 V, V- = -15 V VINH = 2 V, VINL = 0.8 V	+25 °C	-40 °C to +85 °C	-40 °C to +125 °C	TYP./MAX.	UNIT
Analog Switch							
Analog Signal Range	VANALOG		V- to V+			-	V
Drain-Source On-Resistance	RDS(on)	VS = ± 10 V, IS = -10 mA; see fig. 23 V+ = +13.5 V, V- = -13.5 V	1.5	-	-	Typ.	Ω
			1.8	2.3	2.6	Max.	
ON-Resistance Flatness	Rflat(on)	VS = ± 10 V, IS = -10 mA	0.3	-	-	-	Ω
			0.36	0.45	0.48	Max.	
ON-Resistance Matching	ΔRDS(on)		0.08	-	-	Typ.	
			0.18	0.19	0.21	Max.	
Switch Off Leakage Current	IS/Id(off)	V+ = +16.5 V, V- = -16.5 V VS = ± 10 V, VD = ± 10 V; see fig. 24	± 0.03	-	-	Typ.	nA
			± 0.55	± 2	± 12.5	Max.	
Channel On Leakage Current	Id(on)	VS = VD = ± 10 V; see fig. 25	± 0.15	-	-	Typ.	
			± 2	± 4	± 35	Max.	
Digital Control							
Input, High Voltage	VINH		-	-	2	Vmin.	V
Input, Low Voltage	VINL		-	-	0.8	Vmax.	
Input Leakage	IIN	VIN = VGND or V+	0.005	-	-	Typ.	μA
			-	-	± 0.1	Max.	
Digital Input Capacitance	CIN		3.5	-	-	Typ.	pF
Dynamic Characteristics							
Break-Before-Make Time	tOPEN	VS1= VS2 = 10 V, see fig. 31; RL = 300 Ω, CL = 35 pF	36	-	-	Typ.	ns
			-	-	10	Min.	
Turn-On Time	tON	VS = 10 V, see fig. 30 RL = 300 Ω, CL = 35 pF	100	-	-	Typ.	
			150	170	190	Max.	
Turn-Off Time	tOFF		64	-	-	Typ.	
			120	140	160	Max.	
Charge Injection	QINj	CL = 1 nF, RGEN = 0 Ω, VS = 0 V see fig. 32	-20	-	-	Typ.	pC
Off Isolation	OIRR	CL = 5 pF, RL = 50 Ω, 100 kHz	-80	-	-	Typ.	dB
Cross Talk	XTALK	CL = 5 pF, RL = 50 Ω, 1 MHz	-100	-	-	Typ.	
Insertion Loss		f = 1 MHz, RL = 50 Ω, CL = 5 pF	-0.08	-	-	Typ.	
Total Harmonic Distortion	THD	RL = 110 Ω, 15 Vp-p, f = 20 Hz to 20 kHz	0.014	-	-	Typ.	%
Bandwidth, -3dB	BW	CL = 5 pF, RL = 50 Ω	210	-	-	Typ.	MHz
Source Off Capacitance	CS(off)	f = 1 MHz, VS = 0 V	11	-	-	Typ.	pF
Drain Off Capacitance	CD(off)		24	-	-	Typ.	
Drain On Capacitance	CD(on)		87	-	-	Typ.	
Power Requirements							
Power Supply Range		GND = 0 V	± 4.5/± 16.5 min./max.				V
Power Supply Current	I+	Digital Inputs 0 or V+ V+ = +16.5 V, V- = -16.5 V	0.001	-	-	Typ.	μA
			-	-	1	Max.	
		IN1 = IN2 = IN3 = IN4 = 5 V	220	-	-	Typ.	
			-	-	380	Max.	
	I-	Digital Inputs 0 or V+	0.001	-	-	Typ.	
			-	-	1	Max.	



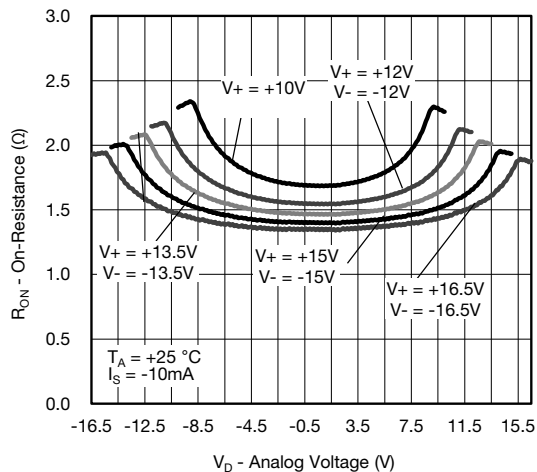
ELECTRICAL CHARACTERISTICS - SINGLE 12 V SUPPLY								
PARAMETER	SYMBOL	TEST CONDITIONS UNLESS OTHERWISE SPECIFIED V+ = 12 V, V- = 0 V VINH = 2 V, VINL = 0.8 V	+25 °C	-40 °C to +85 °C	-40 °C to +125 °C	TYP./MAX.	UNIT	
Analog Switch								
Analog Signal Range	VANALOG		0 V to V+				V	
Drain-Source On-Resistance	RDS(on)	VS = 0 V to 10 V, IS = -10 mA; see fig. 23, V+ = 10.8 V, V- = 0 V	2.8	-	-	Typ.	Ω	
			3.5	4.3	4.8	Max.		
ON-Resistance Flatness	Rflat(on)	VS = 0 V to 10 V, IS = -10 mA	0.6	-	-	Typ.	Ω	
			1.1	1.2	1.3	Max.		
ON-Resistance Matching	ΔRon		0.08	-	-	Typ.		
			0.21	0.23	0.25	Max.		
Switch Off Leakage Current	IS/Id(off)	V+ = 10.8 V, V- = 0 V VS = 1 V/10 V, VD = 10 V/0 V see fig. 24	± 0.02	-	-	Typ.	nA	
			± 0.55	± 2	± 12.5	Max.		
Channel On Leakage Current	Id(on)	VS = VD = 1 V/10 V; see fig. 25	± 0.15	-	-	Typ.		
			± 1.5	± 4	± 30	Max.		
Digital Control								
Input, High Voltage	VINH		-	-	2	Min.	V	
Input, Low Voltage	VINL		-	-	0.8	Max.		
Input Leakage	IIN	VIN = VGND or V+	0.001	-	-	Typ.	μA	
			-	-	± 0.1	Max.		
Digital Input Capacitance	CIN		3.5	-	-	Typ.	pF	
Dynamic Characteristics								
Break-Before-Make Time	tOPEN	VS1 = VS2 = 8 V; see fig. 31, RL = 300 Ω, CL = 35 pF	130	-	-	Typ.	ns	
			-	-	40	Min.		
Turn-On Time	tON	VS = 8 V; see fig. 30, RL = 300 Ω, CL = 35 pF	210	-	-	Typ.		
			250	320	360	Max.		
Turn-Off Time	tOFF		80	-	-	Typ.		
			135	165	190	Max.		
Charge Injection	QINj	CL = 1 nF, RGEN = 0 Ω, VS = 6 V see fig. 32	14	-	-	Typ.	pC	
Off Isolation	OIRR	RL = 50 Ω, CL = 5 pF	100 kHz	-80	-	-	Typ.	dB
Cross Talk	XTALK		1 MHz	-100	-	-	Typ.	
Insertion Loss		f = 1 MHz, RL = 50 Ω, CL = 5 pF	-0.16	-	-	Typ.		
Bandwidth, -3dB	BW	RL = 50 Ω, CL = 5 pF	200	-	-	Typ.	MHz	
Source Off Capacitance	CS(off)	f = 1 MHz, VS = 6 V	17	-	-	Typ.	pF	
Drain Off Capacitance	CD(off)		30	-	-	Typ.		
Drain On Capacitance	CD(on)		94	-	-	Typ.		
Power Requirements								
Power Supply Range		GND = 0 V, V- = 0 V	± 5/± 16.5 min./max.				V	
Power Supply Current	I+	Digital Inputs 0 or V+ V+ = 13.2 V	0.001	-	-	Typ.	μA	
			-	-	1	Max.		
		IN1 = IN2 = IN3 = IN4 = 5 V	220	-	-	Typ.		
			-	-	380	Max.		



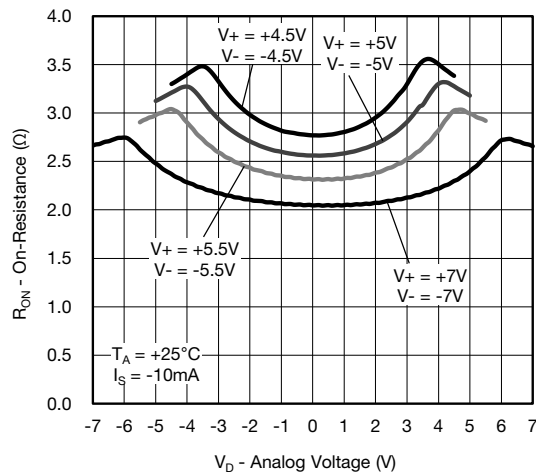
ELECTRICAL CHARACTERISTICS - DUAL ± 5 V SUPPLIES							
PARAMETER	SYMBOL	TEST CONDITIONS UNLESS OTHERWISE SPECIFIED V+ = 5 V, V- = -5 V VINH = 2 V, VINL = 0.8 V	+25 °C	-40 °C to +85 °C	-40 °C to +125 °C	TYP./MAX.	UNIT
Analog Switch							
Analog Signal Range	VANALOG		0 to V+				V
Drain-Source On-Resistance	RDS(on)	VS = ± 4.5 V, IS = -10 mA; see fig. 23, V+ = +4.5 V, V- = -4.5 V	3.3 4	- 4.9	- 5.4	Typ. Max.	Ω
ON-Resistance Flatness	Rflat(on)	VS = ± 4.5 V, IS = -10 mA	0.9 1.1	- 1.24	- 1.31	Typ. Max.	
ON-Resistance Matching	ΔRON		0.08 0.22	- 0.23	- 0.25	Typ. Max.	
Switch Off Leakage Current	IS/Id(off)	V+ = +5.5 V, V- = -5.5 V, VS = +/- 4.5 V, VD = -/+ 4.5 V; see fig. 24	± 0.03 ± 0.55	- ± 2	- ± 12.5	Typ. Max.	
Channel On Leakage Current	Id(on)	VS = VD = ± 4.5 V; see fig. 25	± 0.05 ± 1	- ± 4	- ± 30	Typ. Max.	
Digital Control							
Input, High Voltage	VINH		-	-	2	Min.	V
Input, Low Voltage	VINL		-	-	0.8	Max.	
Input Leakage	IIN	VIN = VGND or V+	0.001 -	- -	- ± 0.1	Typ. Max.	μA
Digital Input Capacitance	CIN		3.5	-	-	Typ.	pF
Dynamic Characteristics							
Break-Before-Make Time	tOPEN	VS1 = VS2 = 3 V; see fig. 31, RL = 300 Ω, CL = 35 pF	150 -	- -	- 50	Typ. Min.	ns
Turn-On Time	tON	VS = 3 V; see fig. 30, RL = 300 Ω, CL = 35 pF	300 400	- 465	- 510	Typ. Max.	
Turn-Off Time	tOFF		150 290	- 320	- 380	Typ. Max.	
Charge Injection	QINj		CL = 1 nF, RGEN = 0 Ω, VS = 0 V; see fig. 32	22	-	-	
Off Isolation	OIRR	RL = 50 Ω, CL = 5 pF	100 KHz 1 MHz	-80 -100	- -	Typ.	dB
Cross Talk	XTALK					Typ.	
Insertion Loss		f = 1 MHz, RL = 50 Ω, CL = 5 pF	-0.19	-	-	Typ.	
Bandwidth, -3dB	BW	RL = 50 Ω, CL = 5 pF	200	-	-	Typ.	MHz
Source Off Capacitance	CS(off)	f = 1 MHz, VS = 0 V	18	-	-	Typ.	pF
Drain Off Capacitance	CD(off)		31	-	-	Typ.	
Drain On Capacitance	CD(on)		95	-	-	Typ.	
Power Requirements							
Power Supply Range		GND = 0 V	± 4.5 V/± 16.5 min./max.				V
Power Supply Current	I+	Digital Inputs 0 V or V+ V+ = +5.5 V, V- = -5.5 V	0.001 -	- -	- 1	Typ. Max.	μA
	I-	Digital Inputs = 0 V or V+	0.001 -	- -	- 1	Typ. Max.	



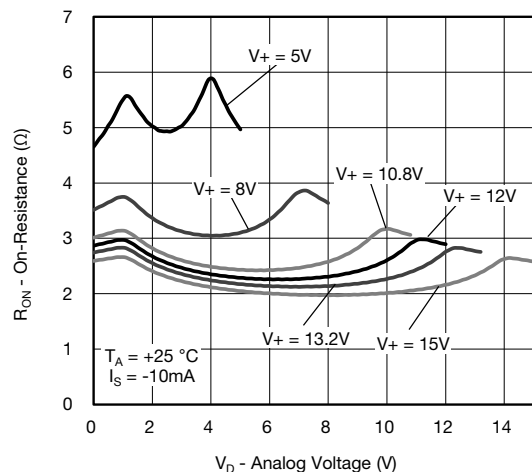
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



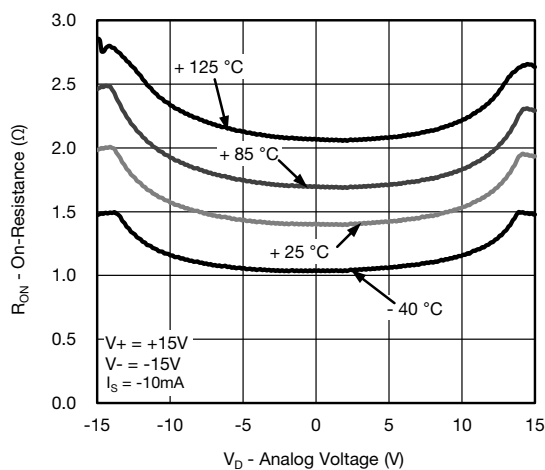
On-Resistance vs. Analog Voltage (DS1)



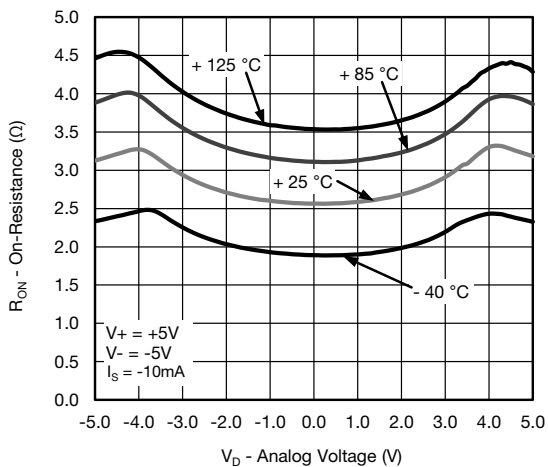
On-Resistance vs. Analog Voltage (DS2)



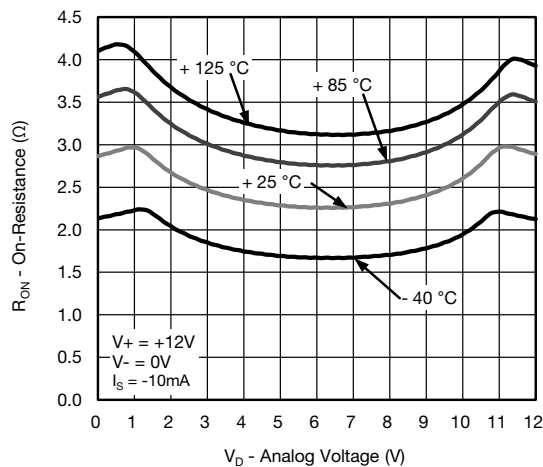
On-Resistance vs. Analog Voltage (DSS)



On-Resistance vs. Temperature ($\pm 15\text{ V}$)



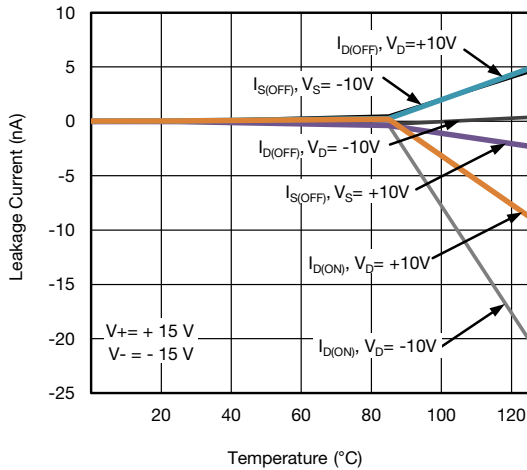
On-Resistance vs. Temperature ($\pm 5\text{ V}$)



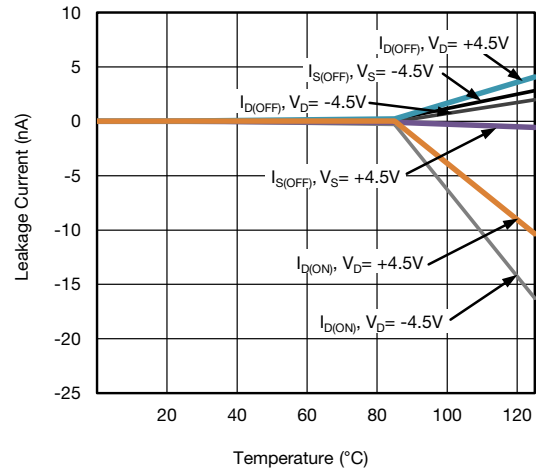
On-Resistance vs. Temperature (+12 V)



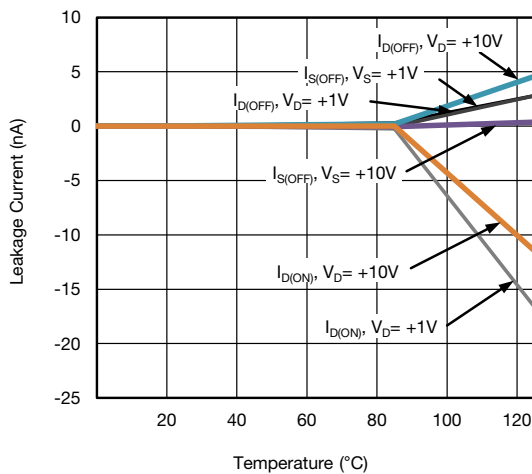
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



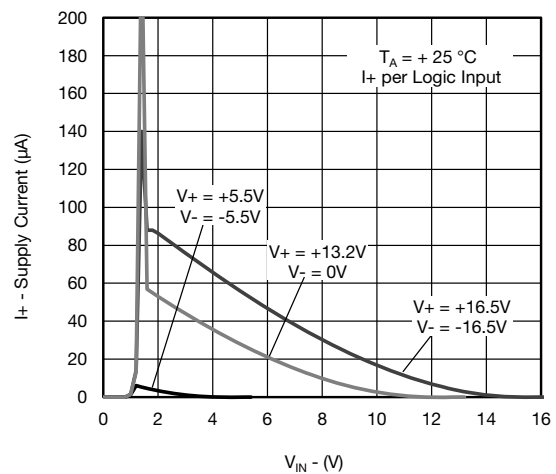
Leakage Current vs. Temperature ($\pm 15\text{V}$)



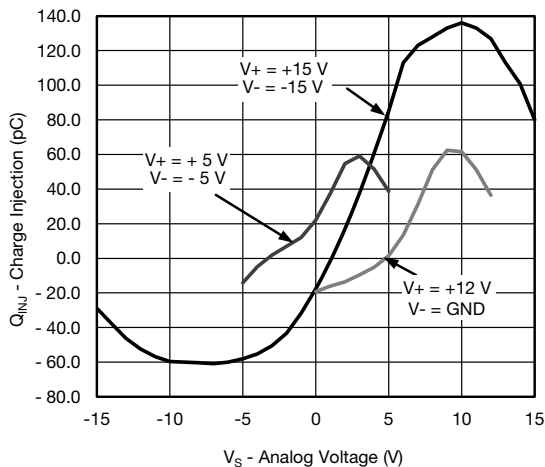
Leakage Current vs. Temperature ($\pm 5\text{V}$)



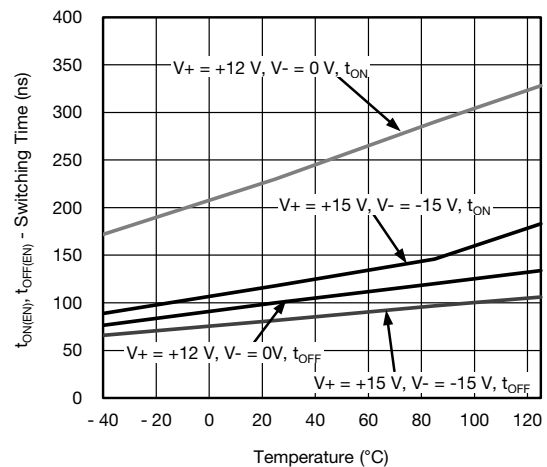
Leakage Current vs. Temperature (+12 V)



Supply Current vs. Logic Level



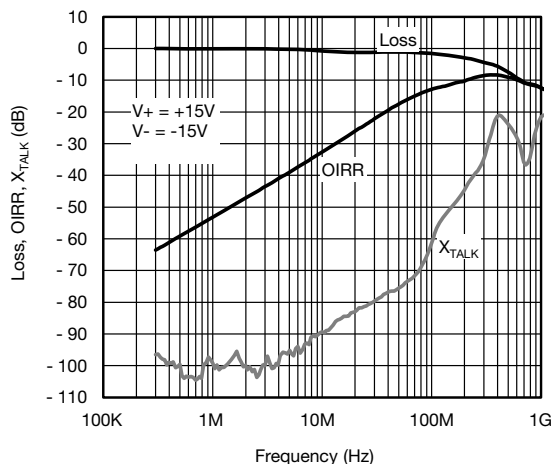
Charge Injection vs. Analog Voltage



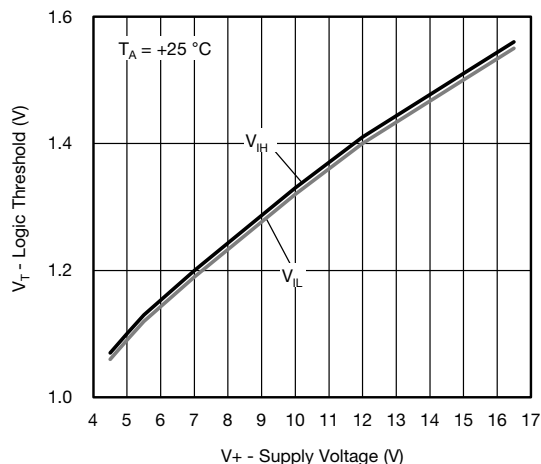
Switching Time vs. Temperature



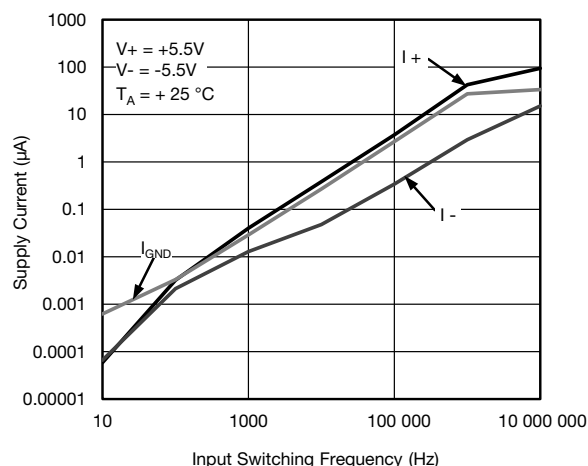
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



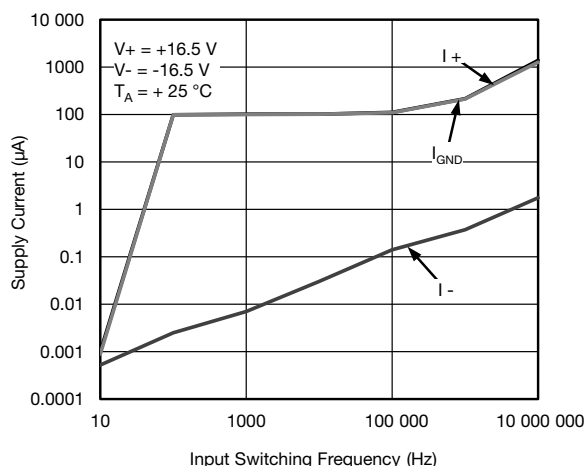
BW, OIRR, X_{TALK} vs. Frequency



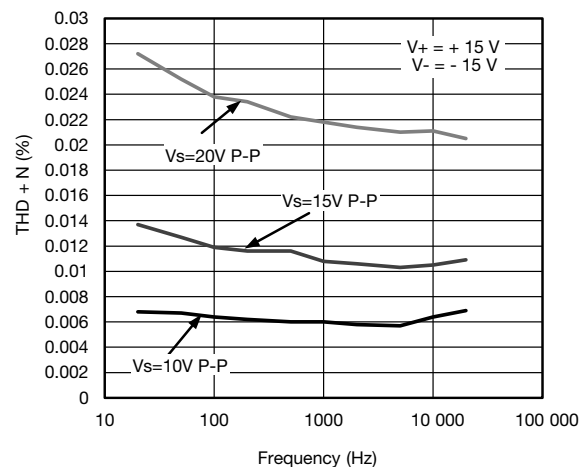
Logic Threshold vs. Supply Voltage



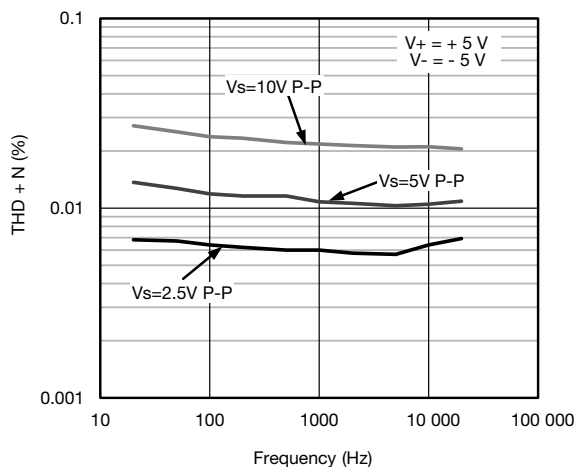
Supply Current vs. Switching Frequency ($\pm 5.5\text{ V}$)



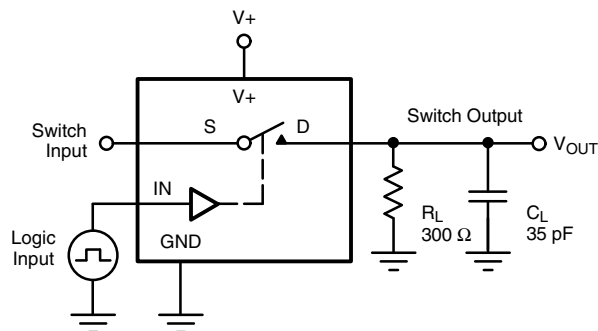
Supply Current vs. Switching Frequency ($\pm 16.5\text{ V}$)



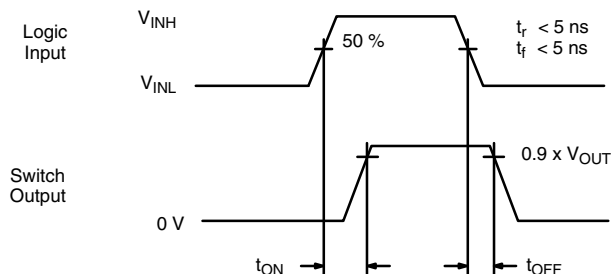
THD vs. Frequency ($\pm 15\text{ V}$)



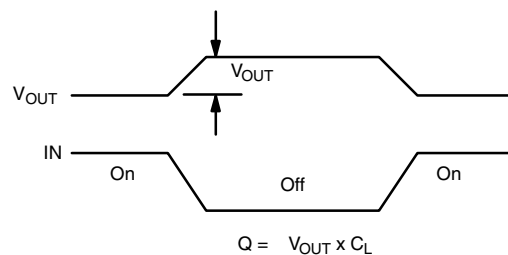
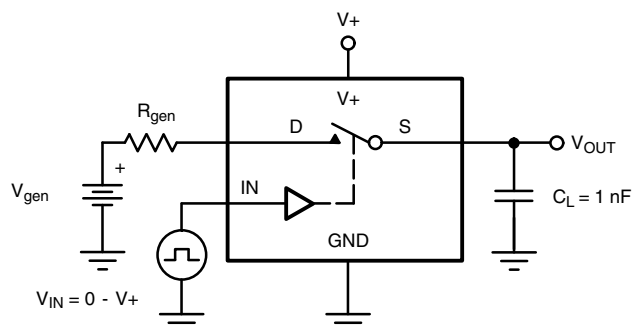
THD vs. Frequency ($\pm 5\text{ V}$)

TEST CIRCUITS

 C_L (includes fixture and stray capacitance)

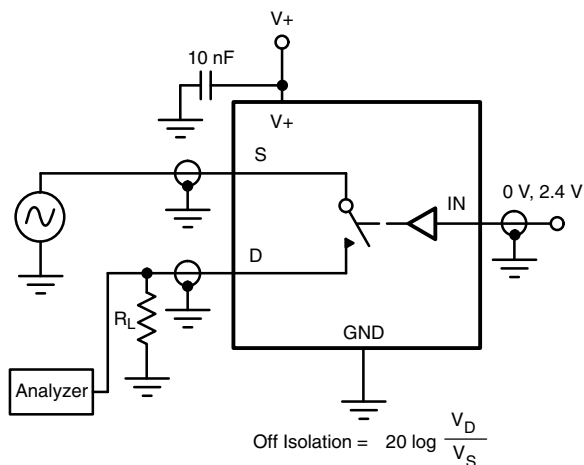
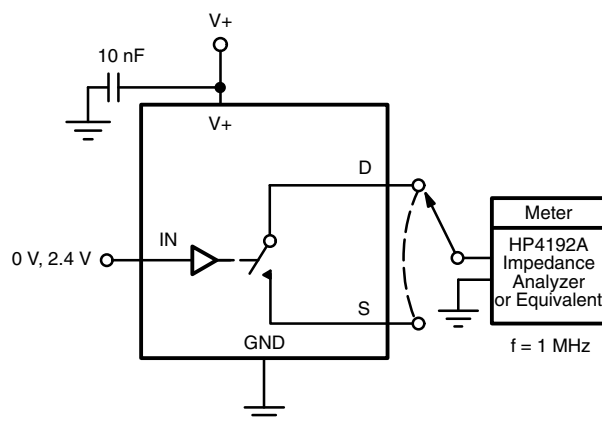
$$V_{OUT} = V_D \left(\frac{R_L}{R_L + R_{ON}} \right)$$



Logic "1" = Switch On
Logic input waveforms inverted for switches that have the opposite logic sense.

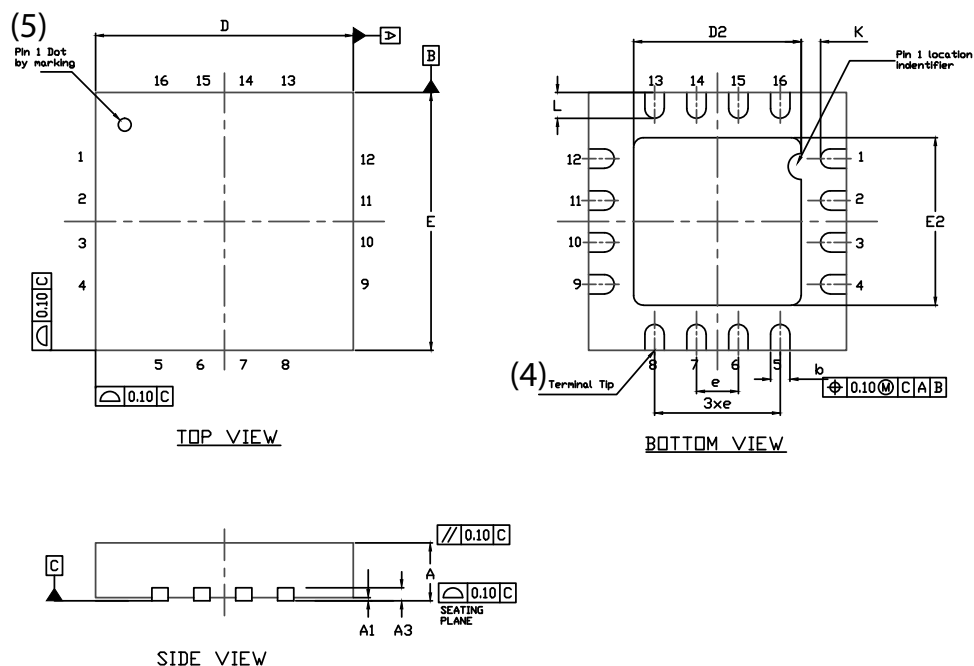
Fig. 1 - Switching Time


IN depends on switch configuration: input polarity determined by sense of switch.

Fig. 2 - Charge Injection

Fig. 3 - Off-Isolation

Fig. 4 - Channel Off/On Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62749.

QFN 4x4-16L Case Outline



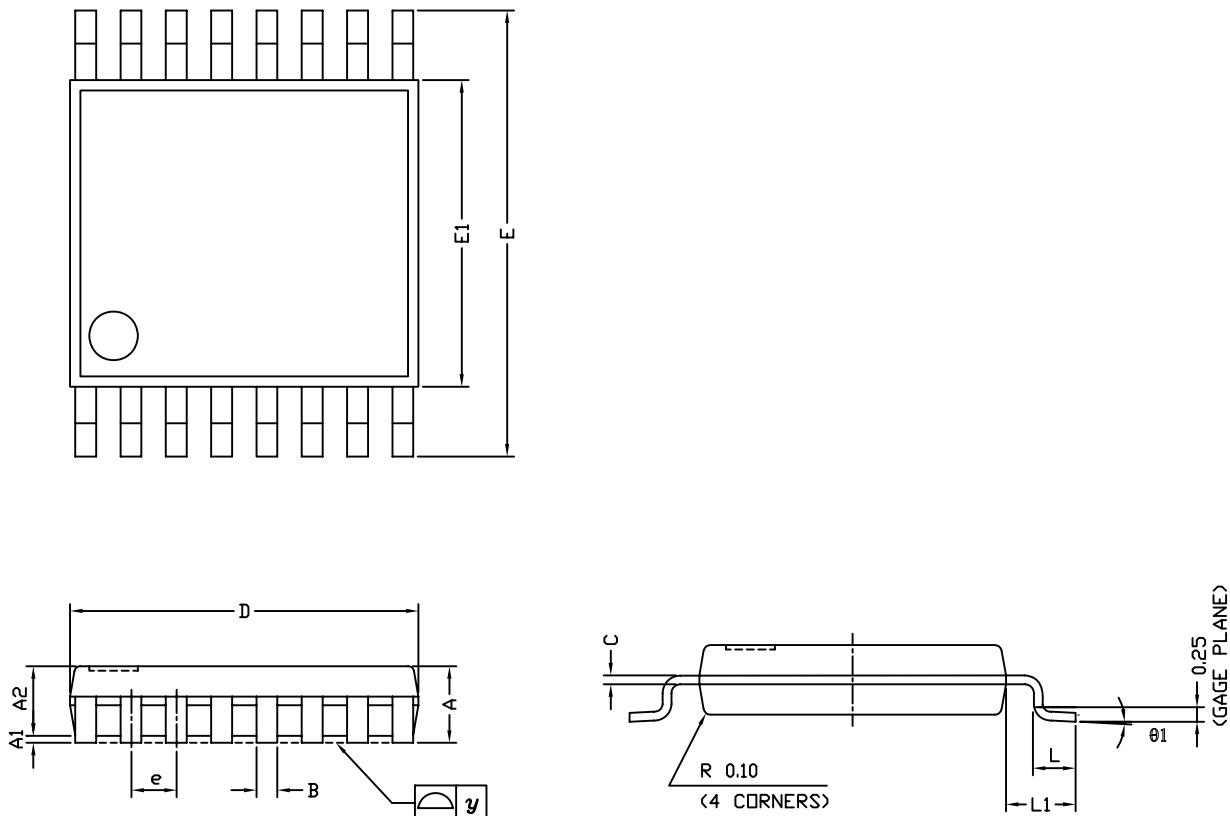
DIM	VARIATION 1						VARIATION 2					
	MILLIMETERS ⁽¹⁾			INCHES			MILLIMETERS ⁽¹⁾			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.75	0.85	0.95	0.029	0.033	0.037	0.75	0.85	0.95	0.029	0.033	0.037
A1	0	-	0.05	0	-	0.002	0	-	0.05	0	-	0.002
A3	0.20 ref.			0.008 ref.			0.20 ref.			0.008 ref.		
b	0.25	0.30	0.35	0.010	0.012	0.014	0.25	0.30	0.35	0.010	0.012	0.014
D	4.00 BSC			0.157 BSC			4.00 BSC			0.157 BSC		
D2	2.0	2.1	2.2	0.079	0.083	0.087	2.5	2.6	2.7	0.098	0.102	0.106
e	0.65 BSC			0.026 BSC			0.65 BSC			0.026 BSC		
E	4.00 BSC			0.157 BSC			4.00 BSC			0.157 BSC		
E2	2.0	2.1	2.2	0.079	0.083	0.087	2.5	2.6	2.7	0.098	0.102	0.106
K	0.20 min.			0.008 min.			0.20 min.			0.008 min.		
L	0.5	0.6	0.7	0.020	0.024	0.028	0.3	0.4	0.5	0.012	0.016	0.020
N ⁽³⁾	16			16			16			16		
Nd ⁽³⁾	4			4			4			4		
Ne ⁽³⁾	4			4			4			4		

Notes

- (1) Use millimeters as the primary measurement.
- (2) Dimensioning and tolerances conform to ASME Y14.5M. - 1994.
- (3) N is the number of terminals. Nd and Ne is the number of terminals in each D and E site respectively.
- (4) Dimensions b applies to plated terminal and is measured between 0.15 mm and 0.30 mm from terminal tip.
- (5) The pin 1 identifier must be existed on the top surface of the package by using identification mark or other feature of package body.
- (6) Package warpage max. 0.05 mm.

ECN: S13-0893-Rev. B, 22-Apr-13
DWG: 5890

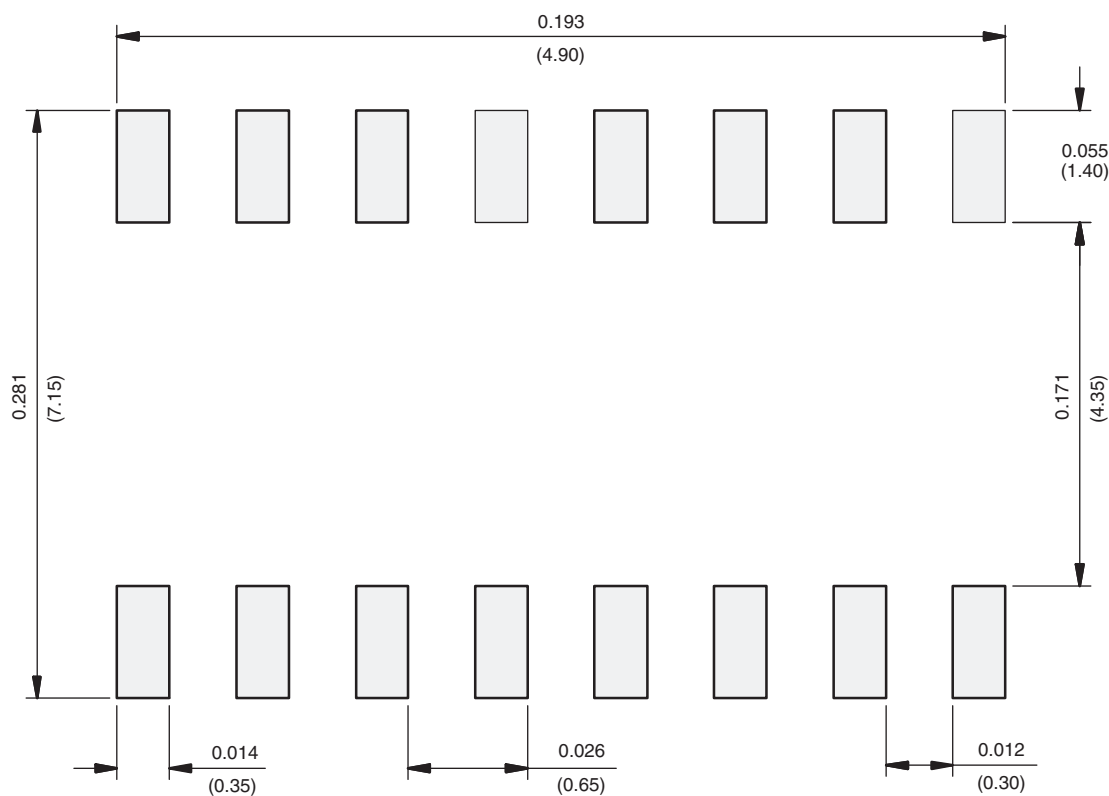
TSSOP: 16-LEAD



Symbols	DIMENSIONS IN MILLIMETERS		
	Min	Nom	Max
A	-	1.10	1.20
A1	0.05	0.10	0.15
A2	-	1.00	1.05
B	0.22	0.28	0.38
C	-	0.127	-
D	4.90	5.00	5.10
E	6.10	6.40	6.70
E1	4.30	4.40	4.50
e	-	0.65	-
L	0.50	0.60	0.70
L1	0.90	1.00	1.10
y	-	-	0.10
θ1	0°	3°	6°
ECN: S-61920-Rev. D, 23-Oct-06			
DWG: 5624			



RECOMMENDED MINIMUM PAD FOR TSSOP-16



Recommended Minimum Pads
Dimensions in inches (mm)



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