

M81705FP

HIGH VOLTAGE HIGH SIDE DRIVER

DESCRIPTION

M81705FP is high voltage Power MOSFET and IGBT module driver for half bridge applications.

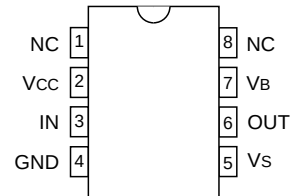
FEATURES

- FLOATING SUPPLY VOLTAGE 600V
- OUTPUT CURRENT +150mA
-125mA
- HIGH SIDE DRIVER
- SOP-8

APPLICATIONS

PDP, MOSFET and IGBT module inverter driver for AC-servomotor and general purpose.

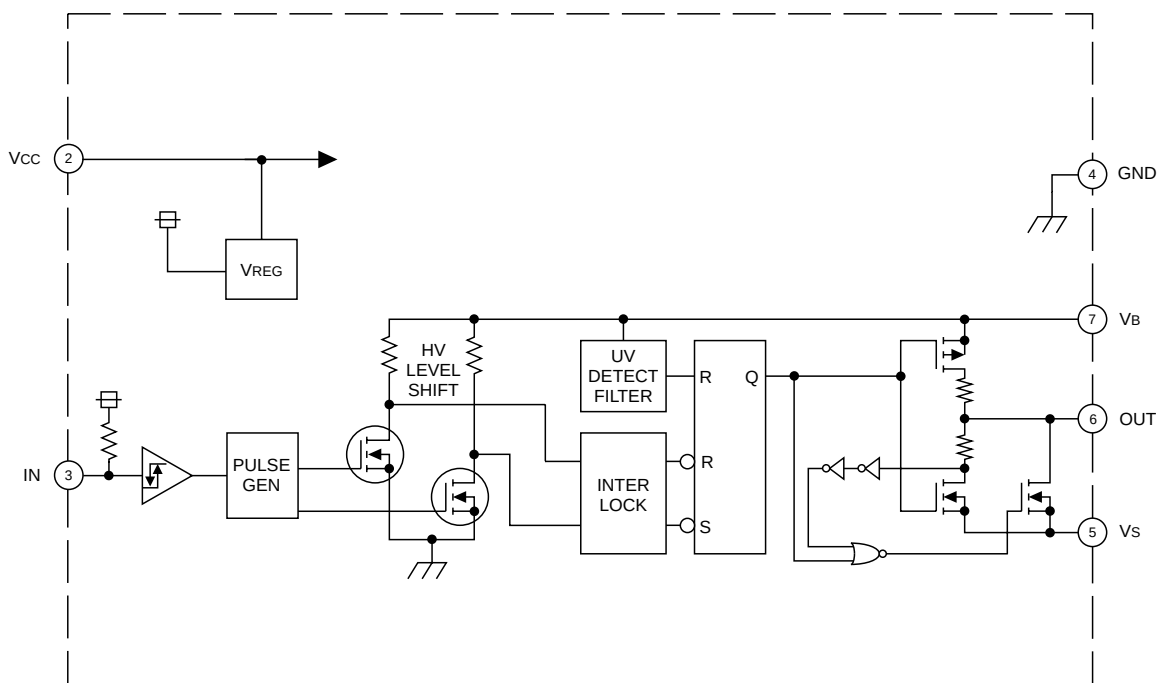
PIN CONFIGURATION (TOP VIEW)



NC:NO INTERNAL CONNECTION

Outline 8P2S

BLOCK DIAGRAM



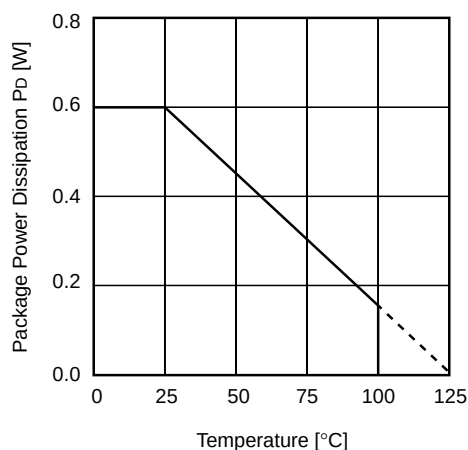
ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Conditions	Ratings	Unit
V_B	High Side Floating Supply Absolute Voltage		$-0.5 \sim 624$	V
V_S	High Side Floating Supply Offset Voltage		$V_B - 24 \sim V_B + 0.5$	V
V_{BS}	High Side Floating Supply Voltage	$V_{BS} = V_B - V_S$	$-0.5 \sim 24$	V
V_{OUT}	High Side Output Voltage		$V_S - 0.5 \sim V_B + 0.5$	V
V_{CC}	Low Side Fixed Supply Voltage		$-0.5 \sim 24$	V
V_{IN}	Logic Input Voltage		$-0.5 \sim 5.5$	V
dV_S/dt	Allowable Offset Supply Voltage Transient		± 50	V/ns
P_D	Package Power Dissipation	$T_a = 25^\circ\text{C}$, On Board	0.60	W
$K\theta$	Linear Derating Factor	$T_a > 25^\circ\text{C}$, On Board	-6.0	mW/°C
$R_{th(j-c)}$	Junction-Case Thermal Resistance		50	°C/W
T_j	Junction Temperature		$-20 \sim 125$	°C
T_{opr}	Operation Temperature		$-20 \sim 100$	°C
T_{stg}	Storage Temperature		$-40 \sim 125$	°C

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V_B	High Side Floating Supply Absolute Voltage		$V_S + 13.5$	—	$V_S + 20$	V
V_S	High Side Floating Supply Offset Voltage	$V_B > 13.5\text{V}$	-5	—	500	V
V_{BS}	High Side Floating Supply Voltage	$V_{BS} = V_B - V_S$	13.5	—	20	V
V_{CC}	Low Side Fixed Supply Voltage		13.5	—	20	V
V_{IN}	Logic Input Voltage		0	—	5	V

THERMAL DERATING FACTOR CHARACTERISTIC



ELECTRICAL CHARACTERISTICS (Ta=25°C, Vcc=Vbs (=Vb-Vs)=15V, unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.*	Max.	
IFS	Floating Supply Leakage Current	Vb=Vs=600V	—	—	1.0	μA
IBS	Vbs Standby Current		0.25	0.50	0.75	mA
ICC	Vcc Standby Current		0.50	0.75	1.00	mA
VOH	High Level Output Voltage	Io=0A	14.9	—	—	V
VOL	Low Level Output Voltage	Io=0A	—	—	0.1	V
VIH	High Level Input Threshold Voltage		2.5	3.0	4.0	V
VIL	Low Level Input Threshold Voltage		0.8	1.5	2.0	V
IiH	High Level Input Bias Current	VIN=5V	-50	-20	—	μA
IiL	Low Level Input Bias Current	VIN=0V	-200	-100	—	μA
VBSuvr	Vbs Supply UV Reset Voltage		10.5	11.5	12.5	V
VBSuvh	Vbs Supply UV Hysteresis Voltage		0.2	0.5	0.8	V
tVBSuv	Vbs Supply UV Filter Time		—	5	—	μs
IOH	Output High Level Short Circuit Pulsed Current	Vo=0V, VIN=0V, PW<10μs	—	-125	—	mA
IOL1	Output Low Level Short Circuit Pulsed Current	Vo=1V, VIN=5V, PW<10μs	—	40	—	mA
IOL2	Output Low Level Short Circuit Pulsed Current	Vo=15V, VIN=5V, PW<10μs	—	150	—	mA
ROH	Output High Level On Resistance	Io=-100mA, ROH=(VOH-Vo)/Io	—	120	160	Ω
ROL1	Output Low Level On Resistance1	Vo=1V, ROL1=Vo/Io	—	50	60	Ω
ROL2	Output Low Level On Resistance2	Vo=5V, ROL2=Vo/Io	—	100	130	Ω
tdLH	High Side Turn-On Propagation Delay	CL=1000pF between OUT – Vs	100	—	500	ns
tdHL	High Side Turn-Off Propagation Delay	CL=1000pF between OUT – Vs	100	—	500	ns
tr	High Side Turn-On Rise Time	CL=1000pF between OUT – Vs	—	220	—	ns
tf	High Side Turn-Off Fall Time	CL=1000pF between OUT – Vs	—	110	—	ns
VOth	ROL1/ROL2 Switching Output Voltage		1.5	2.5	4.0	V

* Typ. is not specified.

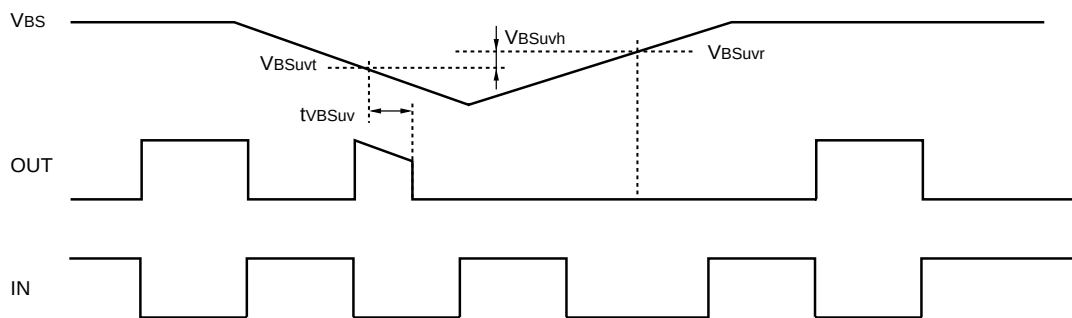
TIMING DIAGRAM

1. Input/Output Timing Diagram

When input signal is "L", then output signal "H".

2. VBS Supply Under Voltage Lockout Timing Diagram

When VBS Supply Voltage keeps lower UV Trip Voltage ($V_{BSUVt} = V_{BSUVr} - V_{BSUVh}$) for VBS Supply UV Filter Time, output signal becomes "L". And then, VBS Supply Voltage is higher UV Reset Voltage, output signal keeps "L" until next input signal is "L".

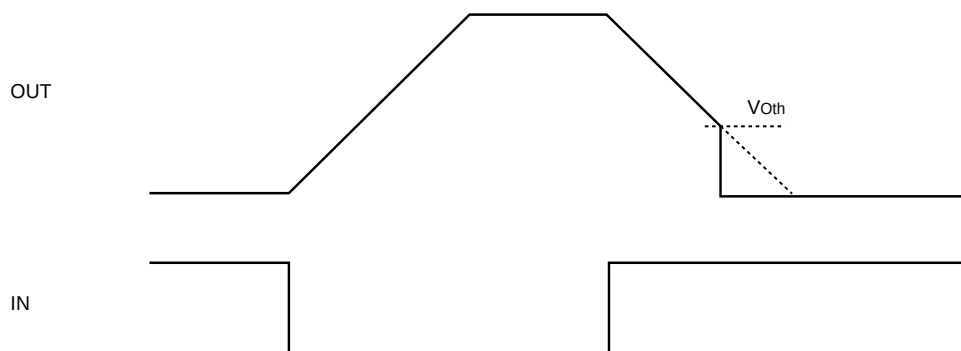


3. Allowable supply voltage transient

Firstly, supply VCC with voltage. Secondly, supply VBS with voltage. In the case of shutting off supply voltage, shut off VBS Supply Voltage firstly. Secondly, shut off VCC Supply Voltage.

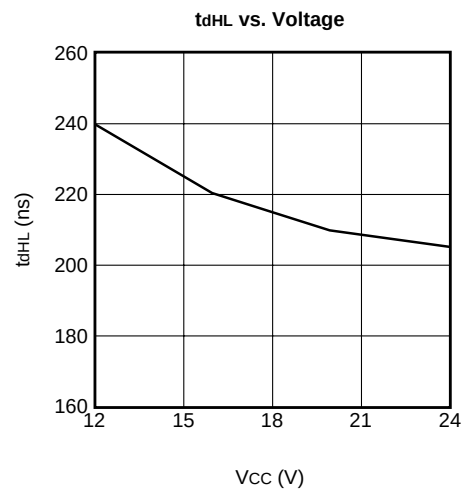
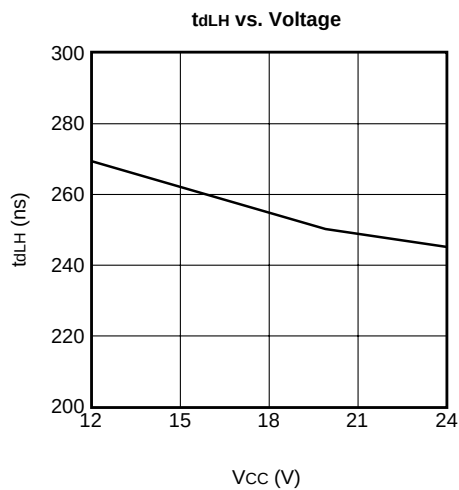
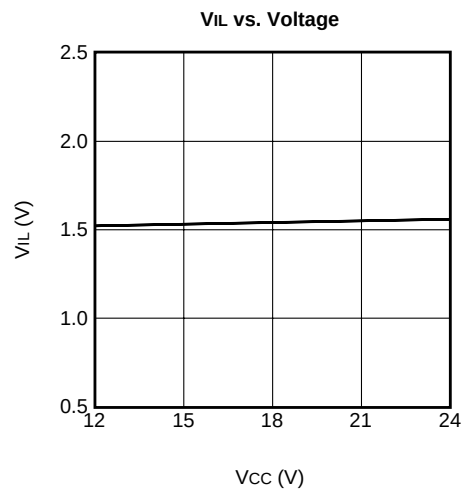
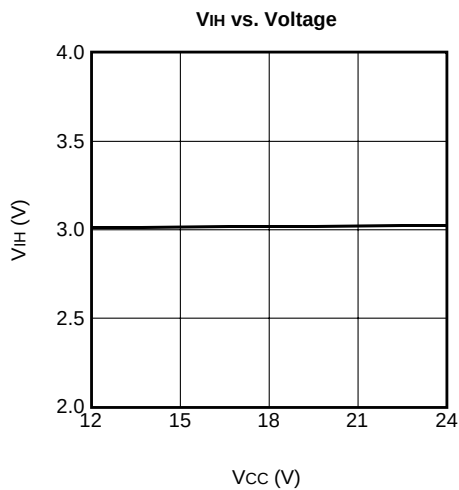
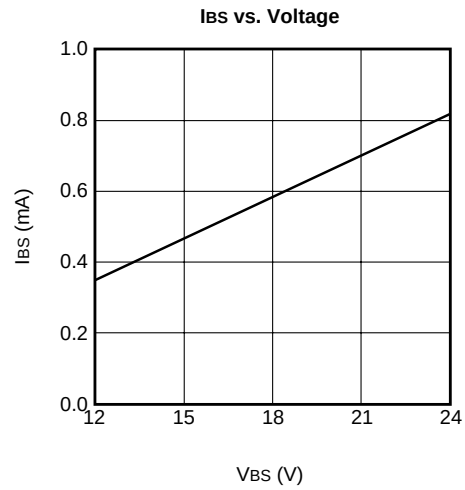
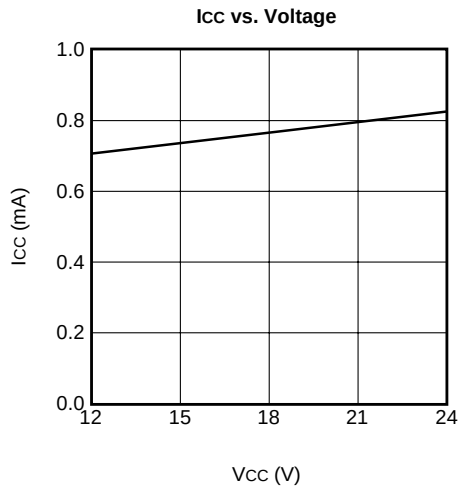
In case VBS or VCC is started too fast, output signal may be "H".

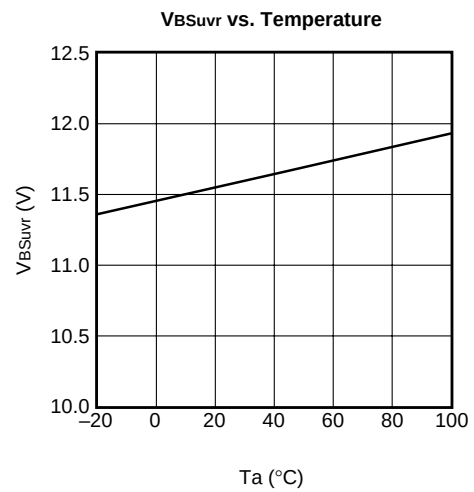
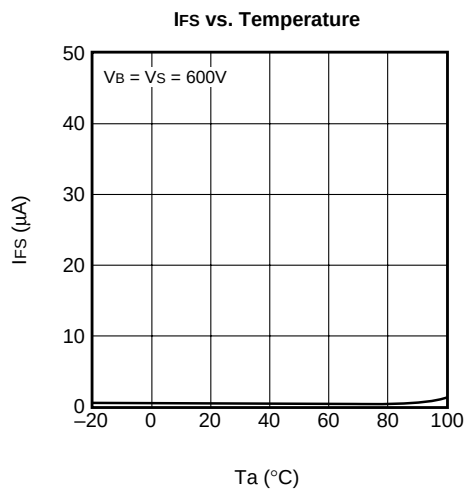
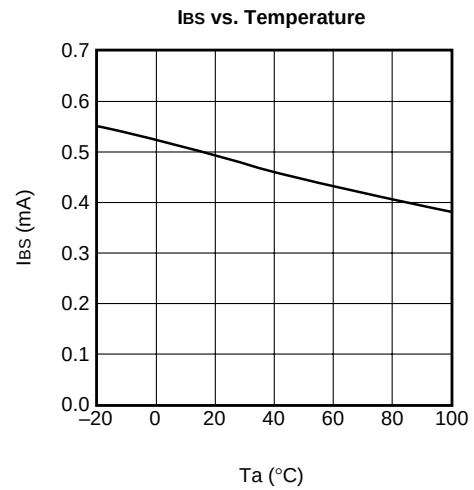
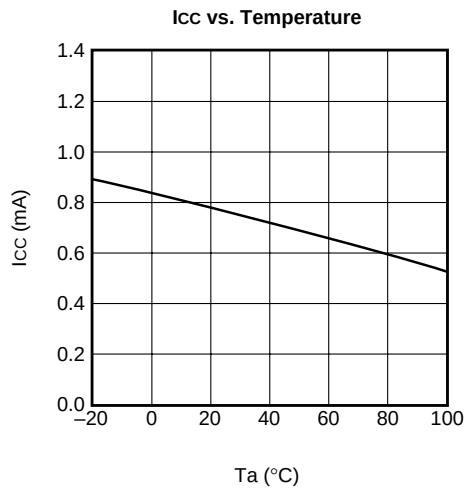
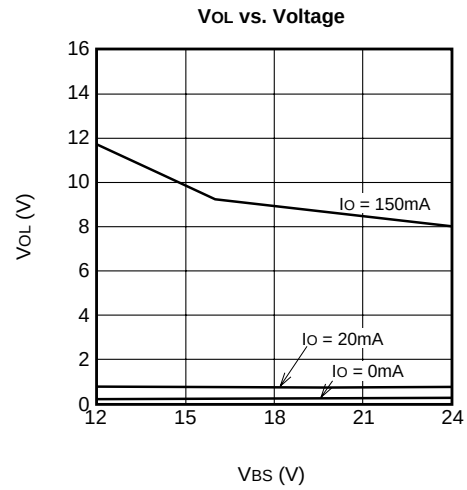
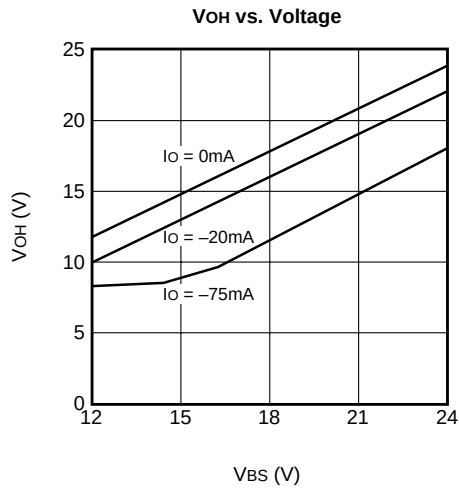
4. ROL1/ROL2 Switching Output Voltage V_{Oth}

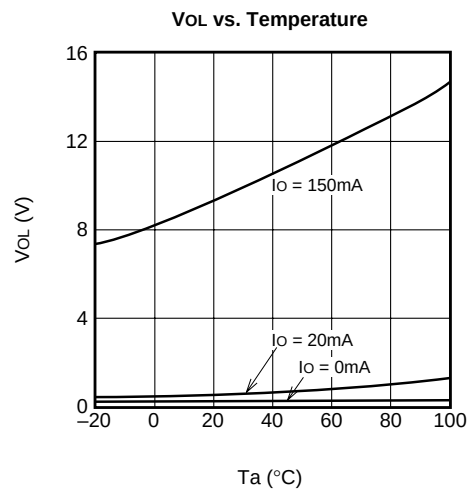
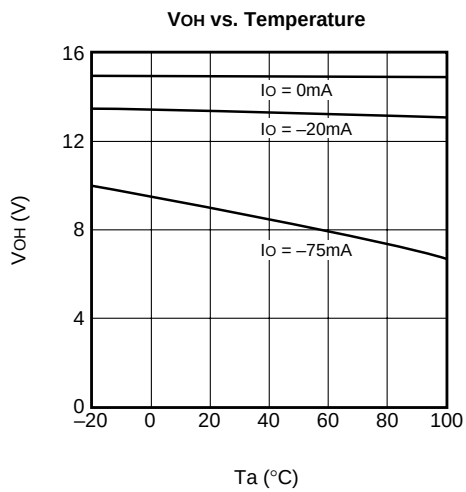
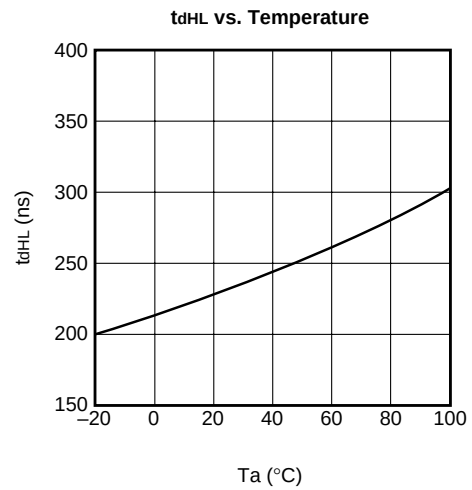
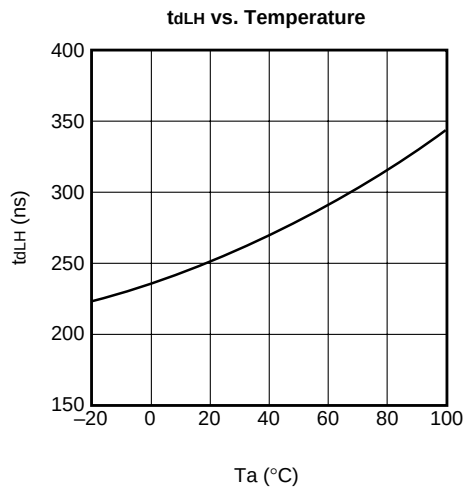
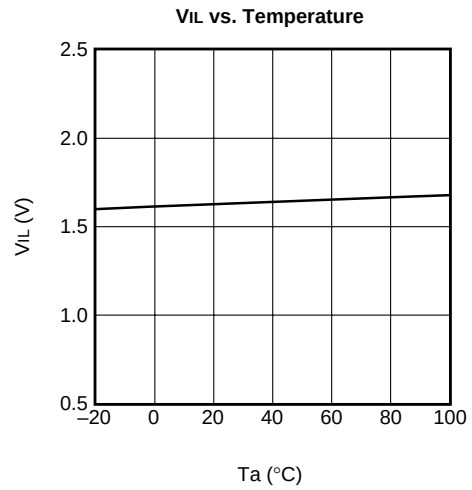
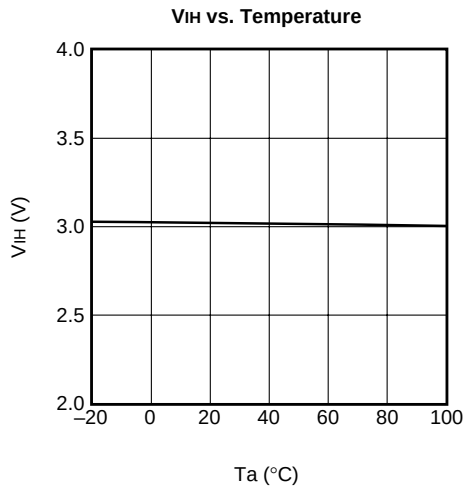


As shown by the solid line of the timing chart, the output on-resistance drops at ' V_{Oth} ' level when the output is in the 'L' state (output level falls). Below the ' V_{Oth} ' level, the output level falls more steeply.

PERFORMANCE CURVES

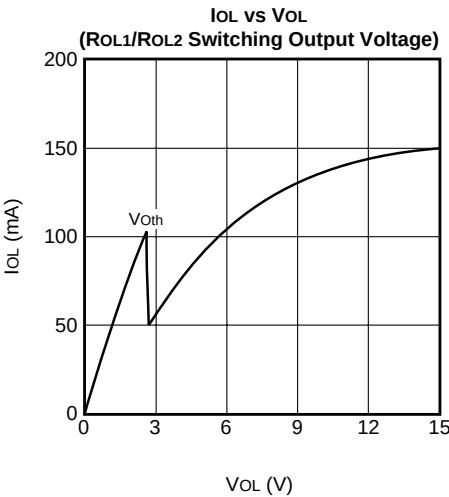






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PACKAGE OUTLINE

8P2S-A

EIAJ Package Code	JEDEC Code	Weight(g)	Lead Material
SOP8-P-225-1.27	—	0.07	Cu Alloy

Plastic 8pin 225mil SOP

