



Z8S18000ZCO

STATIC Z180/ESCC™
EVALUATION BOARD

DESCRIPTION

The kit contains an assembled circuit board, software and documentation to support software and hardware development for the Z8S180 and Z85230 system at 18.432 MHz.

The supplied cross assembler and link/loader package allows full assembly language programming support. A board resident debug monitor program allows executable code to be down-loaded and subsequently debugged.

The board comes with sample code to illustrate the use of Zilog's Z8S180 and Z85230 in a variety of communication applications.

Supported Devices

Z8S180, Z85230

Specifications

Power Requirements

+5Vdc @ 5A

Dimensions

Width: 5.65 in.
Length: 4.0 in.

Serial Interface

RS-232 @ 9600 baud

KIT CONTENTS

Z8S180/ESCC Evaluation Board

CMOS Z8S180 MPU
18.432 MHz Crystal
Socketed 64K/(8K) x 8 EPROM
(Programmed with Debug Monitor and
Device Driver Demonstration Software)
Socketed 32K/(8K) x 8 Static RAM
RS-232C PC Interface
Z8S180 Expansion Header
Z85230 Expansion Header
Reset Switch
NMI Switch

Cables

25-pin RS-232 Cable

Software (IBM® PC platform)

ASM800 Z800 Cross Assembler
MOBJ Link/Loader
Resident Debug Monitor and
Device Drivers Demonstration
Software Source Code
Z8S180 Example Software
(a) In ASM800 Assembly
(b) In Microtek MCC80C and Microtek ASM80 Assembly

**Note: Zilog is not responsible for support
and maintenance of the above software.**

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