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ADC1443D125W1
ADC1443D125W1 Demo Board with ARRIA
II GX FPGA

The IDT ADC demo board is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204B feature set.




Features

- SMA connector for clock and analog input signals
- On board ARRIA II GX FPGA for JESD204B data acquisition
- Single power supply (on-board regulators)

Product Options

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Technical Documentation

	Title	Type	Format	File Size	Date ▼
User Guides & Manuals					
	Demo Board Software ADC4	Manual - Software	ZIP	4.59 MB	06/03/2014
	ADC1443D Quick Start Guide	Manual - User Reference	PDF	1.80 MB	06/28/2012
Other					
	ADC1443D-53D Evaluation Board Schematics&Layout	Schematic	PDF	4.20 MB	06/03/2014

Related Products

Product ID	Title
ADC1443D125HD	Dual channel 14-bit ADC; 125 Msps; JESD204B serial outputs

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