TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SA32F,TC7SA32FU

2-Input OR Gate

Features

• Low voltage operation: V_{CC} = 1.8 to 3.6 V

• High speed operation : t_{pd} = 2.8 ns (max) (V_{CC} = 3.0 to 3.6 V)

: t_{pd} = 3.7 ns (max) (V_{CC} = 2.3 to 2.7 V)

: t_{pd} = 7.4 ns (max) (V_{CC} = 1.8 V)

• High output current : $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

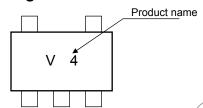
 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$

3.6-V tolerant inputs.

• 3.6-V power down protection output.

TC74VCX32FT equivalent.

Marking



Absolute Maximum Ratings (Ta = 25°C)

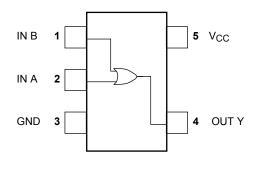
Unit V
V
V
V
V
mA
mA
mA
mW
mA
°C

TC7SA32FU SSOP5-P-0.95 (SMV) SSOP5-P-0.65A (USV)

Pin Assignment (top view)

SSOP5-P-0.95 : 0.016 g (typ.)

SSOP5-P-0.65A: 0.006 g (typ.)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

IEC Logic Symbol



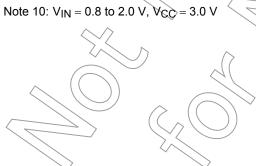
Truth Table

Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	H ((

Operating Ranges

			/
Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	1.8 to 3.6 1.2 to 3,6 (Note 4)	v (
Input voltage	V _{IN}	-0.3 to 3.6	V
Output voltage	Vout	0 to 3.6 (Note 5)	(V)
Cutput voltage	VO01	0 to V _{CC} (Note 6)	
		± 24 (Note 7)	77/\
Output current	I _{OH} /I _{OL}	± 18 (Note 8)	_mA
	<	± 6 (Note 9)	
Operating temperature range	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 10)	ns/V

- Note 4: Data retention only
- Note 5: $V_{CC} = 0 V$
- Note 6: High or low state
- Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{-V}$
- Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
- Note 9: $V_{CC} = 1.8 \text{ V}$



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Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C, 2.7 V < V_{CC} \leq 3.6 V)

Charac	cteristics	Symbol	Test Condition		Test Condition		V _{CC} (V)	Min	Max	Unit
lanut valtana	High level	V _{IH}			2.7 to 3.6	2.0	_	V		
Input voltage	Low level	V _{IL}	-	_	2.7 to 3.6	1	0.8	V		
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_			
	High level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -12 mA	2.7	2.2	_			
	3			I _{OH} = -18 mA	3.0	2.4	_			
Output voltage				I _{OH} = -24 mA	> 3.0	2.2	_	V		
			$V_{IN} = V_{IL}$	I _{OL} = 100 μA	2.7 to 3.6		0.2			
	Low level	V _{OL}		I _{OL} 12 mA	2.7	\mathcal{A}	0.4			
	Low level	VOL		10L = 18 mA	3.0	1	0.4			
				lou = 24 mA	3.0(0.55			
Input leakage curre	Input leakage current I_{IN} $V_{IN} = 0$ to 3,6 V			2.7 to 3.6	4	±5.0	μΑ			
Power off leakage	current	I _{OFF}	V _{IN} , V _{OUT} = 0 to 3	3.6 V	0	\ 	10.0	μΑ		
Quiescent supply current		Icc	V _{IN} = V _{CC} or GNI	\bigcirc	2.7 to 3.6	_	20.0			
			V _C C ≤ (V _{IN} , V _{OUT}	-) ≤ 3.6 V	2.7 to 3.6	_	±20.0	μΑ		
Increase in I _{CC} pe	r input	Δl _{CC}	V _H =V _{CC} -0.6 V		2.7 to 3.6	_	750			

DC Characteristics (Ta = -40 to 85° C, 2.3 V \leq V_{CC} \leq 2.7 V)

Charac	teristics	Symbol	Test ©	ondition	V _{CC} (V)	Min	Max	Unit
Input voltage	High level	VIII		7	2.3 to 2.7	1.6	_	V
Input voltage	Low level	// \YıL		\rightarrow	2.3 to 2.7		0.7	V
			(7/3)	I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2		
	High level	VoH	VIN = VIH or VIL	$I_{OH} = -6 \text{ mA}$	2.3	2.0		
		1		I _{OH} = -12 mA	2.3	1.8	_	
Output voltage	\checkmark			I _{OH} = -18 mA	2.3	1.7	_	V
2	\sim	\wedge		I _{OL} = 100 μA	2.3 to 2.7	_	0.2	
	Low level	(NOF	$V_{IN} = V_{IL}$	I _{OL} = 12 mA	2.3	_	0.4	
	$\bigcirc)$			I _{OL} = 18 mA	2.3	_	0.6	
Input leakage curre	nt	(lin)	V _{IN} = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μА
Power off leakage	current	OFF	V_{IN} , $V_{OUT} = 0$ to 3	3.6 V	0	_	10.0	μΑ
Quippont six alive	urrant	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	V _{IN} = V _{CC} or GNE)	2.3 to 2.7	_	20.0	^
Quiescent supply c	urrent	^V ICC	V _{CC} ≤ (V _{IN} , V _{OUT}	r) ≤ 3.6 V	2.3 to 2.7		±20.0	μА

DC Characteristics (Ta = -40 to 85° C, $1.8 \text{ V} \le \text{V}_{CC} < 2.3 \text{ V}$)

Characteristics		Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	High level	V _{IH} — 1		1.8 to 2.3	V _{CC} × 0.7		V	
Input voltage	Low level	V _{IL}	-	_		_	V _{CC} × 0.2	V
	High level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.8	VCC 0.2	_	V
Output voltage				$I_{OH} = -6 \text{ mA}$	7/1,8	1.4	_	
	Low level	Va	$V_{IN} = V_{IL}$	I _{OL} = 100 μA	1.8	_	0.2	
	Low level	V_{OL}		I _{OL} = 6 mA	1.8		0.3	
Input leakage current		I _{IN}	$V_{IN} = 0$ to 3.6 V		1.8		±5.0	μА
Power off leakage current		l _{OFF}	V _{IN} , V _{OUT} = 0 to 3.6 V 0		7	10.0	μА	
Quiescent supply current		loo	V _{IN} = V _{CC} or GND		1.8	<i>></i>	20.0	μΑ
		Icc	V _{CC} ≤ (V _{IN} , V _{OU} T	-(≦/3.6 ♦\	1.8	J-	±20.0	μΑ

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	Vcc (V)	Min	Max	Unit
	+		1.8	1.5	7.4	
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.5 ± 0.2	1.0	3.7	ns
	t _{pHL}		3.3 ± 0.3	0.8	2.8	

For C_L = 50 pF, add approximately 300 ps to the AC maximum specification.

Capacitive Characteristics (Ta=25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	CIN			1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD} <	f _{IN} = 10 MHz	(Note 11)	1.8, 2.5, 3.3	20	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

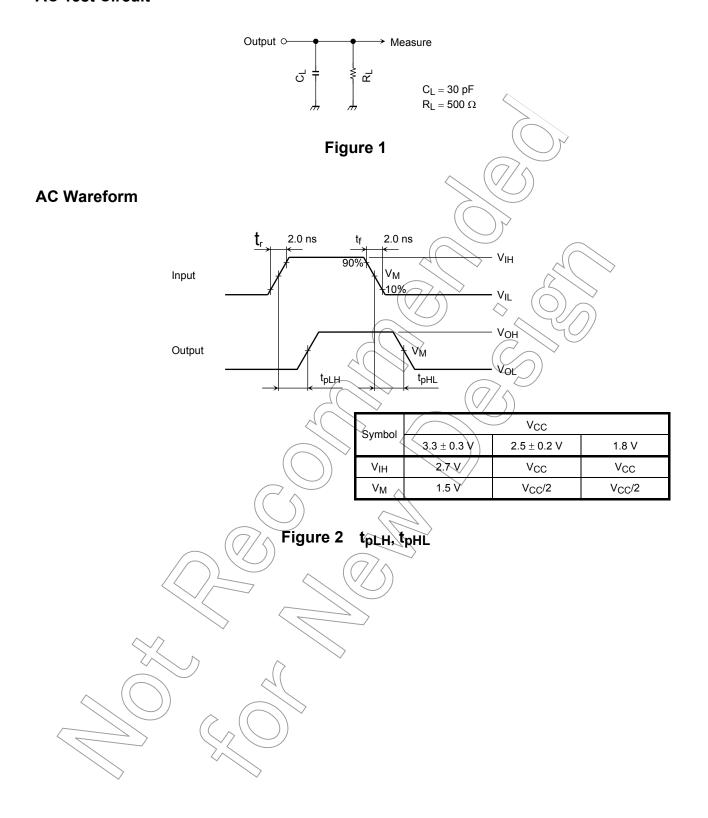
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Average operating current can be obtained by the equation.

ICC (opr.) = CPD·VCC·fIN + ICC

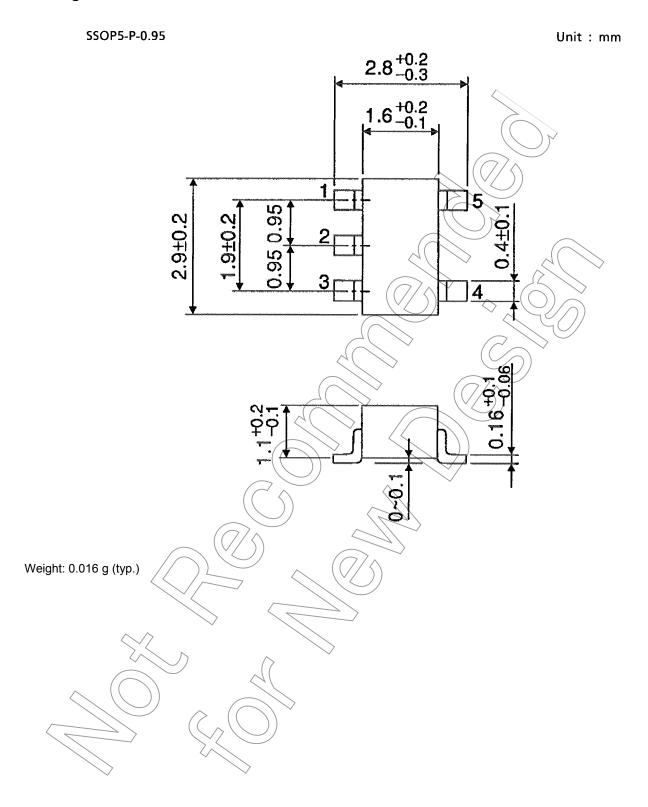
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AC Test Circuit



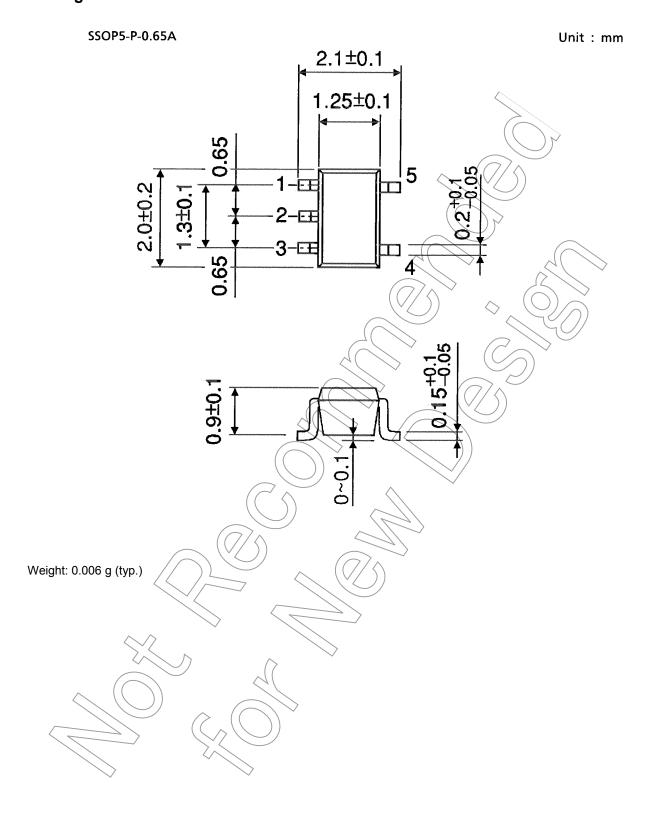
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Package Dimensions



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Package Dimensions



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