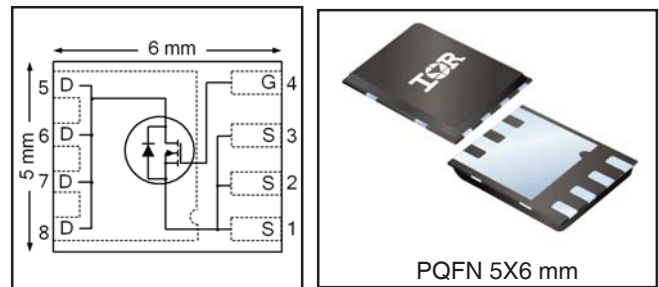


HEXFET® Power MOSFET

V_{DS}	200	V
$R_{DS(on) \max}$ (@ $V_{GS} = 10V$)	55	mΩ
Q_g (typical)	36	nC
R_G (typical)	1.9	Ω
I_D (@ $T_{c(Bottom)} = 25^\circ C$)	34	A



Applications

- Secondary Side Synchronous Rectification
- Inverters for DC Motors
- DC-DC Brick Applications
- Boost Converters

Features and Benefits

Features

Low $R_{DS(on)}$
Low Thermal Resistance to PCB ($\leq 0.8^\circ C/W$)
100% R_g tested
Low Profile (≤ 0.9 mm)
Industry-Standard Pinout
Compatible with Existing Surface Mount Techniques
RoHS Compliant Containing no Lead, no Bromide and no Halogen
MSL1, Industrial Qualification

results in
⇒

Benefits

Lower Conduction Losses
Enable better thermal dissipation
Increased Reliability
Increased Power Density
Multi-Vendor Compatibility
Easier Manufacturing
Environmentally Friendlier
Increased Reliability

Orderable part number	Package Type	Standard Pack		Note
		Form	Quantity	
IRFH5020TRPbF	PQFN 5mm x 6mm	Tape and Reel	4000	
IRFH5020TR2PbF	PQFN 5mm x 6mm	Tape and Reel	400	EOL notice #259

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain-to-Source Voltage	200	V
V_{GS}	Gate-to-Source Voltage	± 20	
I_D @ $T_A = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	5.1	A
I_D @ $T_A = 70^\circ C$	Continuous Drain Current, V_{GS} @ 10V	4.1	
I_D @ $T_{c(Bottom)} = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	34	
I_D @ $T_{c(Bottom)} = 100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	21	
I_D @ $T_{c(Top)} = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	7.8	
I_D @ $T_{c(Top)} = 100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	4.9	
I_{DM}	Pulsed Drain Current ①	63	
P_D @ $T_A = 25^\circ C$	Power Dissipation ⑤	3.6	W
P_D @ $T_{c(Top)} = 25^\circ C$	Power Dissipation ④	8.3	
	Linear Derating Factor ④	0.07	W/°C
T_J	Operating Junction and	-55 to + 150	°C
T_{STG}	Storage Temperature Range		

Notes ① through ⑤ are on page 8

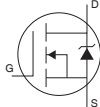
Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.22	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1mA$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	47	55	m Ω	$V_{GS} = 10V, I_D = 7.5A$ ③
$V_{GS(th)}$	Gate Threshold Voltage	3.0	—	5.0	V	$V_{DS} = V_{GS}, I_D = 150\mu A$
$\Delta V_{GS(th)}$	Gate Threshold Voltage Coefficient	—	-12	—	mV/ $^\circ\text{C}$	
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	$V_{DS} = 200V, V_{GS} = 0V$
		—	—	1.0	mA	$V_{DS} = 200V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100	nA	$V_{GS} = -20V$
g_{fs}	Forward Transconductance	18	—	—	S	$V_{DS} = 50V, I_D = 7.5A$
Q_g	Total Gate Charge	—	36	54	nC	$V_{DS} = 100V$ $V_{GS} = 10V$ $I_D = 7.5A$ See Fig.17 & 18
Q_{gs1}	Pre-Vth Gate-to-Source Charge	—	8.6	—		
Q_{gs2}	Post-Vth Gate-to-Source Charge	—	2.1	—		
Q_{gd}	Gate-to-Drain Charge	—	11	—		
Q_{godr}	Gate Charge Overdrive	—	14	—		
Q_{sw}	Switch Charge ($Q_{gs2} + Q_{gd}$)	—	13	—	nC	$V_{DS} = 16V, V_{GS} = 0V$
Q_{oss}	Output Charge	—	13	—		
R_G	Gate Resistance	—	1.9	—	Ω	
$t_{d(on)}$	Turn-On Delay Time	—	9.3	—	ns	$V_{DD} = 100V, V_{GS} = 10V$ $I_D = 7.5A$ $R_G = 1.8\Omega$ See Fig.15
t_r	Rise Time	—	7.7	—		
$t_{d(off)}$	Turn-Off Delay Time	—	21	—		
t_f	Fall Time	—	6.0	—		
C_{iss}	Input Capacitance	—	2290	—	pF	$V_{GS} = 0V$ $V_{DS} = 100V$ $f = 1.0MHz$
C_{oss}	Output Capacitance	—	120	—		
C_{rss}	Reverse Transfer Capacitance	—	33	—		

Avalanche Characteristics

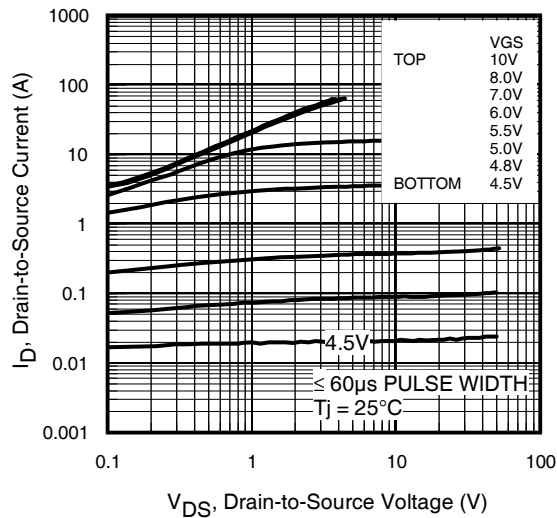
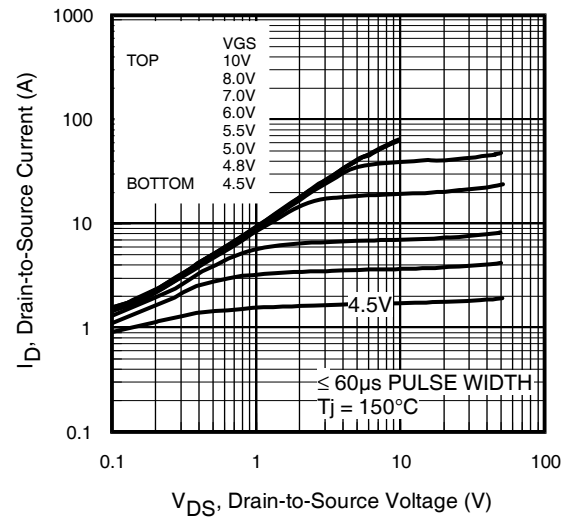
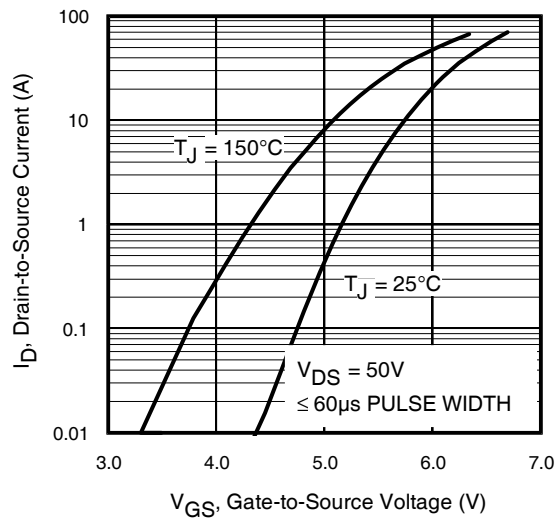
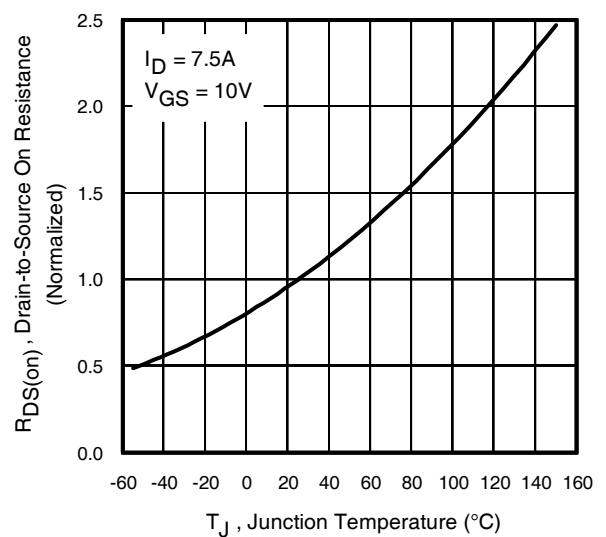
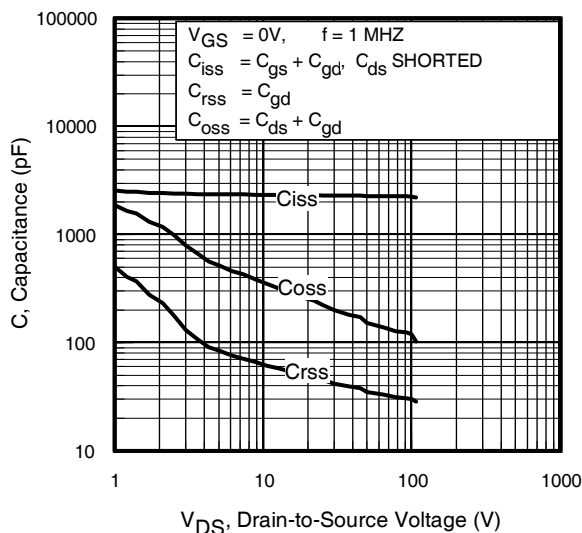
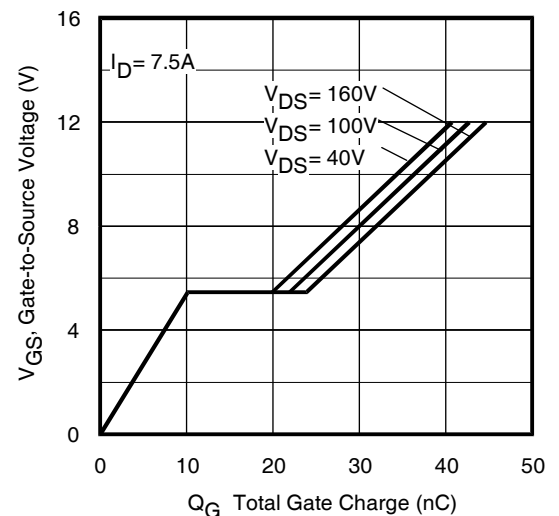
	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy ②	—	320	mJ
I_{AR}	Avalanche Current ①	—	7.5	A

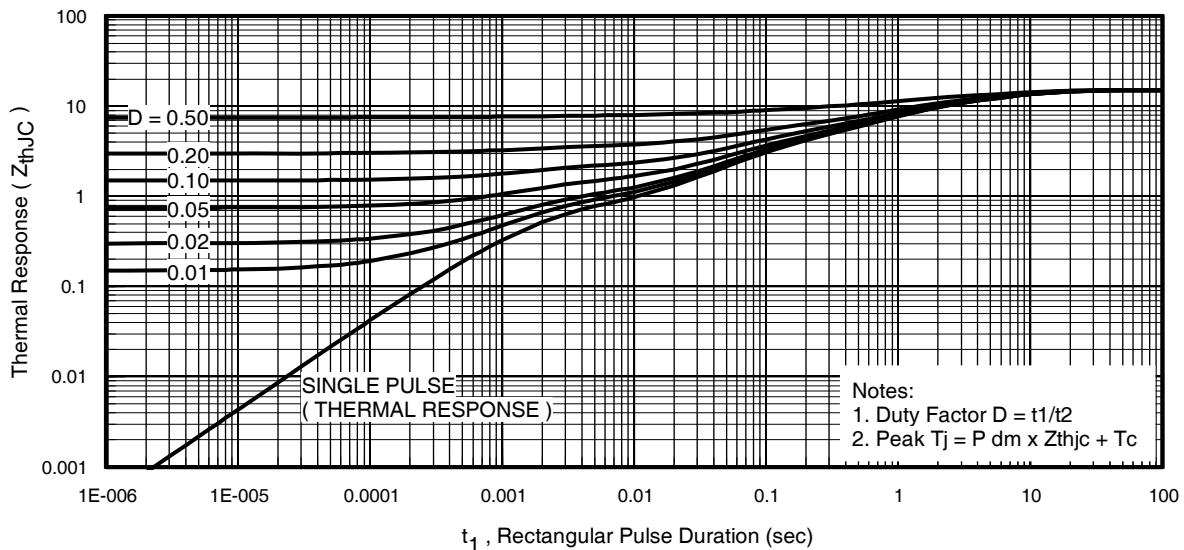
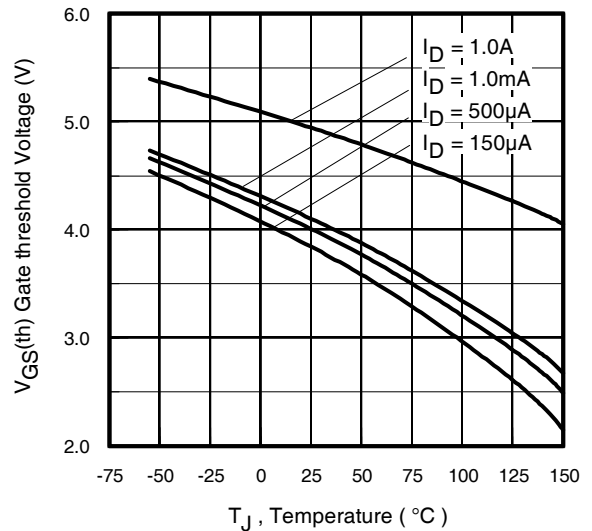
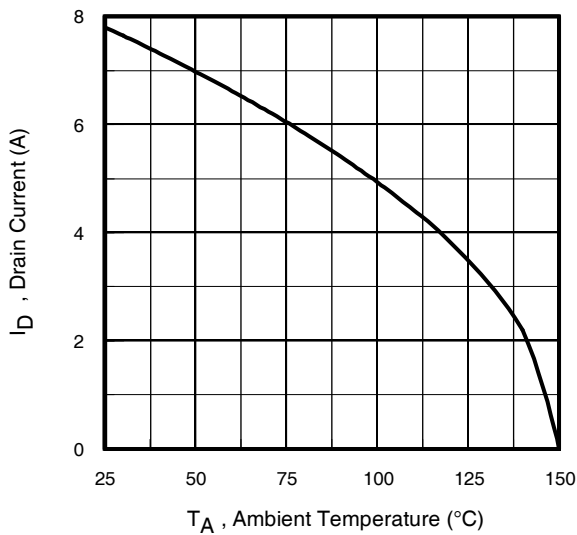
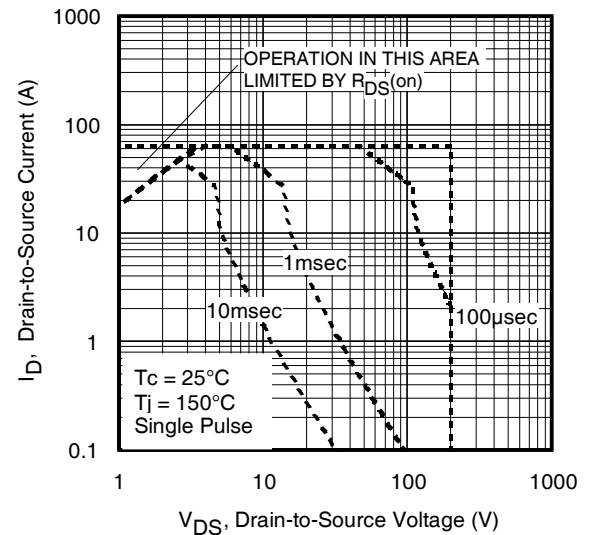
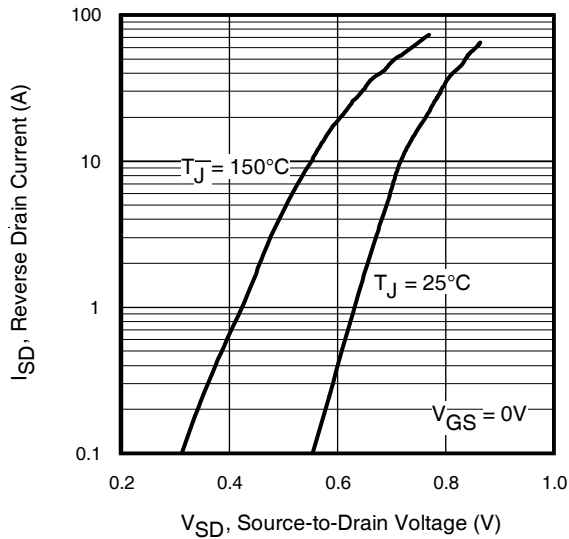
Diode Characteristics

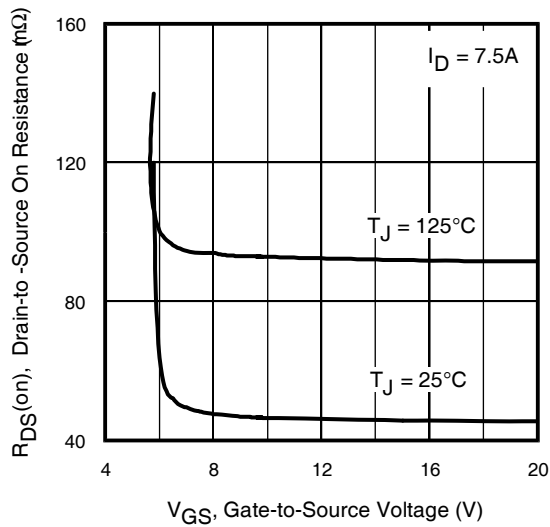
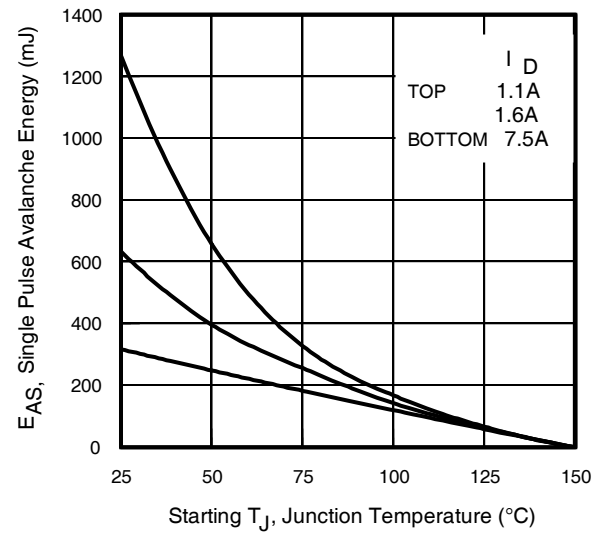
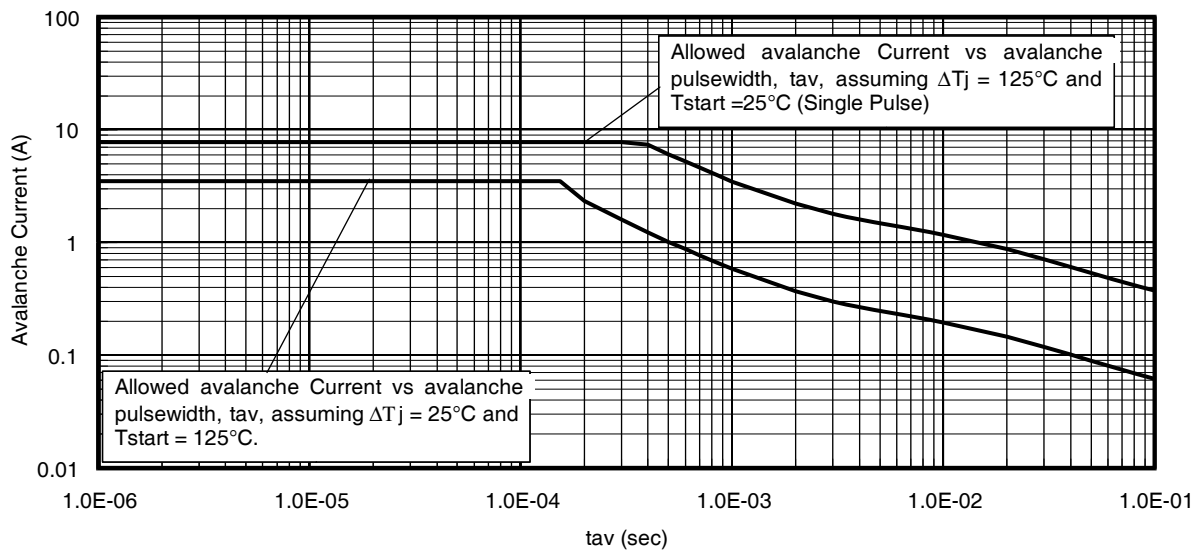
	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	7.5	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	63		
V_{SD}	Diode Forward Voltage	—	—	1.3	V	$T_J = 25^\circ\text{C}, I_S = 7.5A, V_{GS} = 0V$ ③
t_{rr}	Reverse Recovery Time	—	45	68	ns	$T_J = 25^\circ\text{C}, I_F = 7.5A, V_{DD} = 100V$
Q_{rr}	Reverse Recovery Charge	—	459	689	nC	$di/dt = 500A/\mu s$ ③
t_{on}	Forward Turn-On Time	Time is dominated by parasitic Inductance				

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$ (Bottom)	Junction-to-Case	0.5	0.8	$^\circ\text{C/W}$
$R_{\theta JC}$ (Top)	Junction-to-Case ④	—	15	
$R_{\theta JA}$	Junction-to-Ambient ⑤	—	35	
$R_{\theta JA}$ (<10s)	Junction-to-Ambient ⑤	—	21	


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics

Fig 3. Typical Transfer Characteristics

Fig 4. Normalized On-Resistance Vs. Temperature

Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage




Fig 12. On-Resistance vs. Gate Voltage

Fig 13. Maximum Avalanche Energy vs. Drain Current

Fig 14. Typical Avalanche Current vs. Pulsewidth

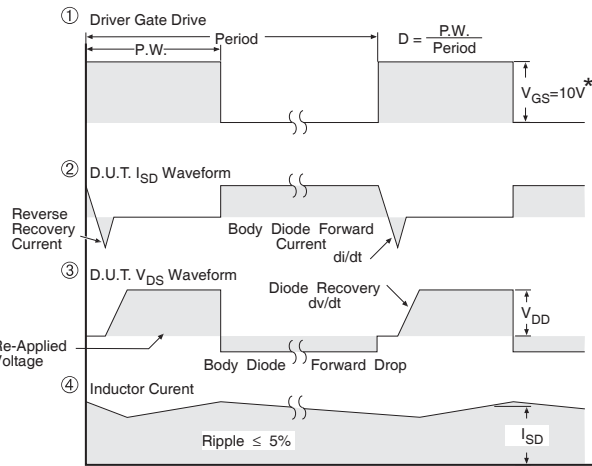
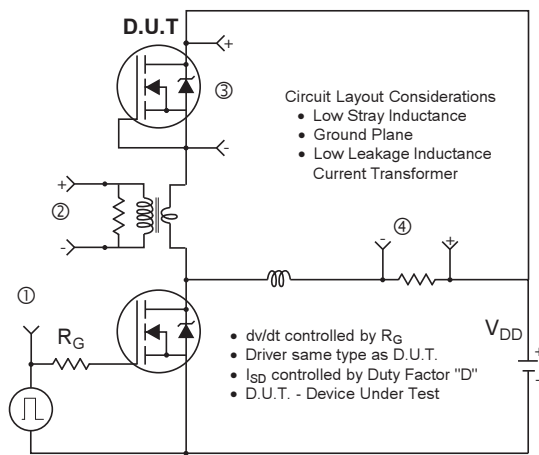


Fig 15. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

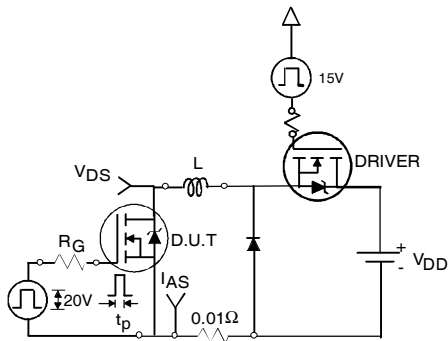


Fig 16a. Unclamped Inductive Test Circuit

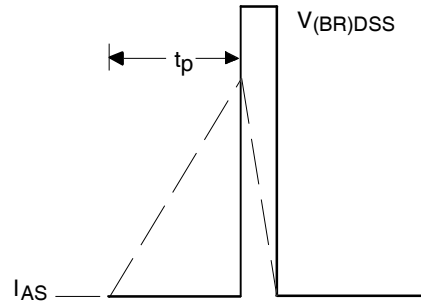


Fig 16b. Unclamped Inductive Waveforms

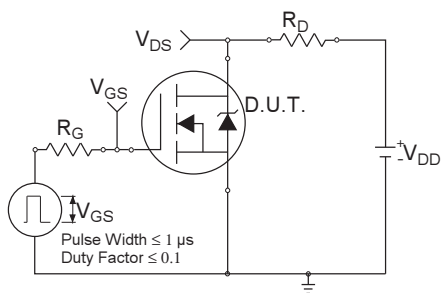


Fig 17a. Switching Time Test Circuit

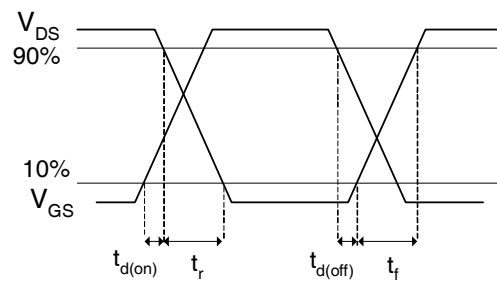


Fig 17b. Switching Time Waveforms

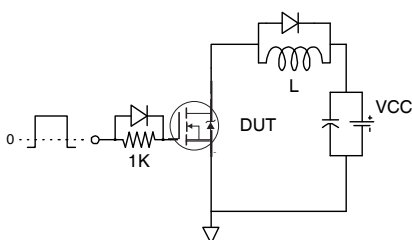


Fig 18a. Gate Charge Test Circuit

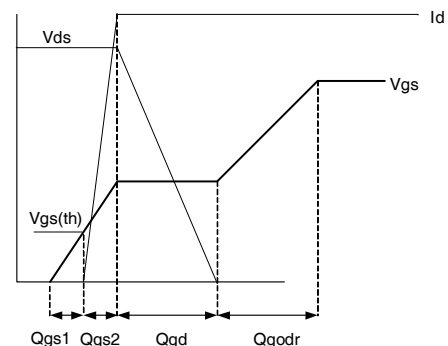
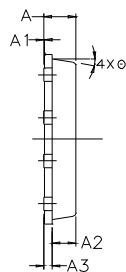
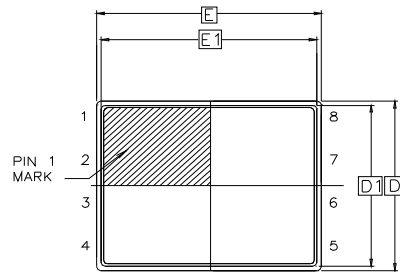


Fig 18b. Gate Charge Waveform

PQFN 5x6 Outline "B" Package Details

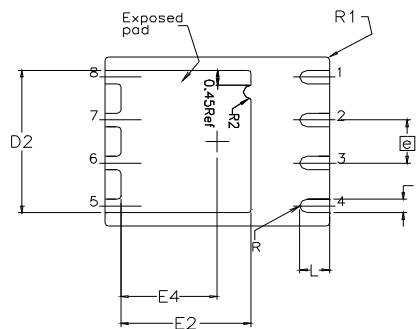


SIDE VIEW



TOP VIEW

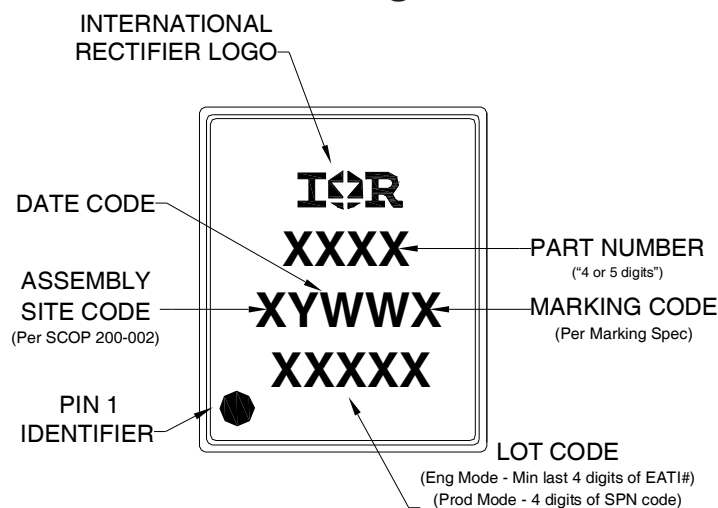
DIM SYMBOL	MIN	NOM	MAX
A	0.800	0.830	1.05
A1	0.000	0.020	0.050
A2	0.580	0.630	0.680
A3		0.254 REF	
Ø	0"	10"	12"
b	0.350	0.400	0.470
D	4.850	5.000	5.150
D1	4.675	4.750	5.000
D2	3.700	4.210	4.300
e		1.270 BSC	
E	5.850	6.000	6.150
E1	5.675	5.750	6.000
E2	3.380	3.480	3.760
E4	2.480	2.580	2.680
L	0.550	0.800	0.900
R		0.200 REF	
R1		0.100 REF	
R2	0.150	0.200	0.250



BOTTOM VIEW

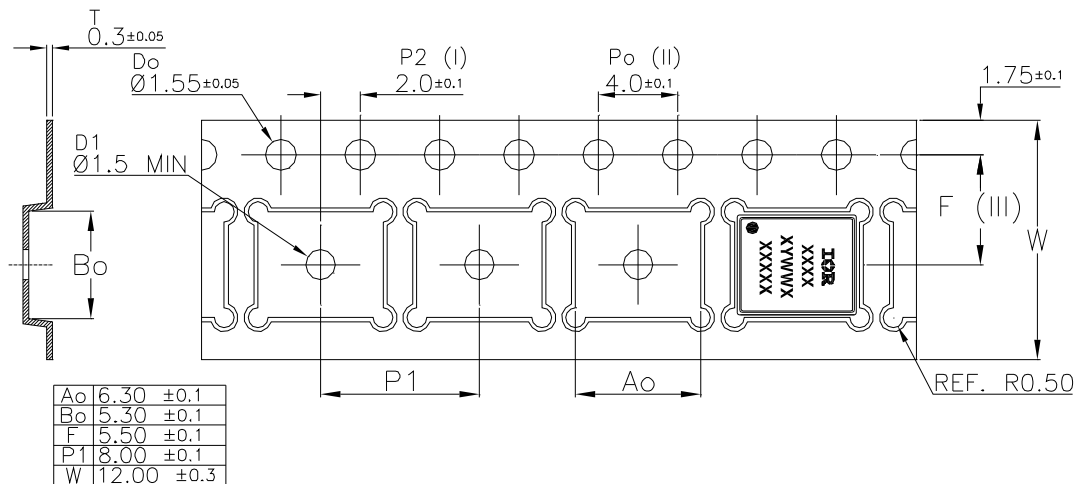
For footprint and stencil design recommendations, please refer to application note AN-1154 at <http://www.irf.com/technical-info/appnotes/an-1154.pdf>

PQFN 5x6 Outline "B" Part Marking



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

PQFN Tape and Reel



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

Qualification information[†]

Qualification level	Industrial ^{††} (per JEDEC JESD47F ^{†††} guidelines)	
Moisture Sensitivity Level	PQFN 5mm x 6mm	MSL1 (per JEDEC J-STD-020D ^{†††})
RoHS compliant	Yes	

[†] Qualification standards can be found at International Rectifier's web site

<http://www.irf.com/product-info/reliability>

^{††} Higher qualification ratings may be available should the user have such requirements.

Please contact your International Rectifier sales representative for further information:

<http://www.irf.com/whoto-call/salesrep/>

^{†††} Applicable version of JEDEC standard at the time of product release.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting T_J = 25°C, L = 11.3mH, R_G = 25Ω, I_{AS} = 7.5A.
- ③ Pulse width ≤ 400μs; duty cycle ≤ 2%.
- ④ R_θ is measured at T_J of approximately 90°C.
- ⑤ When mounted on 1 inch square 2 oz copper pad on 1.5x1.5 in. board of FR-4 material.

Date	Comment
4/14/2014	- Updated ordering information to reflect the End-Of-Life (EOL) of the mini-reel option (EOL notice #259) on page1 - Corrected typo on Breakdown Voltage Temp. Coefficient from "0.02V/C" to "0.22V/C" on page 2. - Updated Package outline on page7. - Updated data sheet with the new IR corporate template.
5/5/2014	- Updated T_{rr} Typ/Max from "46/69ns" to "45/68ns" on page2 - Updated Q_{rr} Typ/Max from "97/150nC" to "459/689nC" on page2

International
IR Rectifier

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To contact International Rectifier, please visit <http://www.irf.com/whoto-call/>