## End of line programmable Hall-effect Latch / Switch



#### 1. Features and Benefits

- Programmable parameters in the application
  - Wide magnetic Latch range: ±0.4mT to +80mT
  - Wide magnetic Switch range: ±1.5mT to ±66mT
  - Programmable Hysteresis: 1mT to 36mT
  - Programmable field: North or South
  - Programmable Output polarity: Direct or Inverted
  - Built-in Negative TC coefficient: 0 to -2000 ppm/degC
  - Increased Traceability: 32 bits ID on chip
- Wide operating voltage rang : from 2.7V to 24V
- Reverse Supply Voltage Protection
- Output Current Limit with Auto-Shutoff
- Under-Voltage Lockout Protection
- Thermal Protection

# 2. Application Examples

- Automotive, Consumer and Industrial
- Solid-state switch
- 3-phase BLDC motor commutation
- Wiper motor
- Window lifter
- Sunroof/Tailgate opener
- Seat motor adjuster
- Electrical power steering
- Brake Light switch

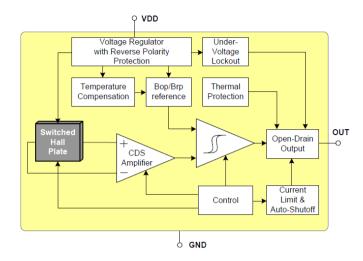
# 3. Ordering Information

Part No.	Temperature Code	Package Code	Comment
MLX92232LSE-AAA-000-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch / Latch, TC = 0 ppm/°C
MLX92232LUA-AAA-000-BU	L (-40°C to 150°C)	UA (TO-92)	3-wire Switch / Latch, TC = 0 ppm/°C
MLX92232LSE-AAA-001-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch / Latch, TC = -400 ppm/°C
MLX92232LUA-AAA-001-BU	L (-40°C to 150°C)	UA (TO-92)	3-wire Switch / Latch, TC = -400 ppm/°C
MLX92232LSE-AAA-002-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch / Latch, TC = -1100 ppm/°C
MLX92232LUA-AAA-002-BU	L (-40°C to 150°C)	UA (TO-92)	3-wire Switch / Latch, TC = -1100 ppm/°C
MLX92232LSE-AAA-003-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch / Latch, TC = -2000 ppm/°C
MLX92232LUA-AAA-003-BU	L (-40°C to 150°C)	UA (TO-92)	3-wire Switch / Latch, TC = -2000 ppm/°C

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# 4. Functional Diagram



# 5. General description

The Melexis MLX92232 is the second generation programmable Hall-effect sensor designed in mixed signal CMOS technology. The device integrates a voltage regulator, Hall sensor with advanced offset cancellation system and an open-drain output driver, all in a single package.

With the built-in reverse voltage protection, a serial resistor or diode on the supply line is not required so that even remote sensors can be specified for low voltage operation down to 2.7V while being reverse voltage tolerant. In the event of a drop below the minimum supply voltage during operation, the undervoltage lock-out protection will automatically freeze the device, preventing the electrical perturbation to affect the magnetic measurement circuitry.

The open drain output is fully protected against short-circuit with a built-in current limit. An additional automatic output shut-off is activated in case of a prolonged short-circuit condition. A self-check is then periodically performed to switch back to normal operation if the short-circuit condition is released.

The on-chip thermal protection also switches off the output if the junction temperature increases above an abnormally high threshold. It will automatically recover once the temperature decreases below a safe value.

Furthermore the MLX92232 features a full set of programmable parameters that can be adjusted in the application in order to achieve the highest possible system accuracy by compensating the mechanical tolerances.



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# **6. Absolute Maximum Ratings**

Parameter	Symbol	Value	Units
Supply Voltage <sup>1, 2</sup>	V	+27V	V
Supply Voltage (Load Dump) 1, 4	V	+32V	V
Supply Current <sup>1, 2, 3</sup>	   DD	+20	mA
Supply Current <sup>1, 4, 3</sup>	 DD	+50	mA
Reverse Supply Voltage <sup>1, 2</sup>	V DDREV	-24	V
Reverse Supply Current 1, 2, 5	   DDREV	-20	mA
Reverse Supply Current 1, 4, 5	   DDREV	-50	mA
Output Voltage 1, 2	V	+27	V
Output Current 1, 2, 5	l out	+20	mA
Output Current <sup>1, 4, 6</sup>	l out	+75	mA
Reverse Output Voltage <sup>1</sup>	V	-0.5	V
Reverse Output Current <sup>1,2</sup>	OUTREV	-50	mA
Maximum Junction Temperature <sup>7</sup>	TJ	+165	2C
ESD Sensitivity – HBM <sup>8</sup>	-	4500	V
ESD Sensitivity – MM <sup>9</sup>	-	500	V

<sup>&</sup>lt;sup>1</sup>The maximum junction temperature should not be exceeded

<sup>&</sup>lt;sup>2</sup>For maximum 1 hour

<sup>&</sup>lt;sup>3</sup>Including current through protection device

<sup>&</sup>lt;sup>4</sup>For maximum 500ms

<sup>&</sup>lt;sup>5</sup>Through protection device

<sup>&</sup>lt;sup>6</sup>For  $V_{OUT}$  ≤27V.

<sup>&</sup>lt;sup>7</sup>For 1000 hours.

<sup>&</sup>lt;sup>8</sup>Human Model according AEC-Q100-002 standard

<sup>&</sup>lt;sup>9</sup>Machine Model according AEC-Q100-003 standard

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ESD Sensitivity – CDM <sup>10</sup>	-	1000	V
Magnetic Flux Density	В	Unlimited	mT

Table 1 - Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

<sup>&</sup>lt;sup>10</sup>Charged Device Model according AEC-Q100-011 standard



# 7. General Electrical Specifications

DC Operating Parameters  $V_{DD}$  = 2.7 to 24V,  $T_A$  = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ <sup>11</sup>	Max	Units
Supply Voltage	V <sub>DD</sub>	Operating	2.7	-	24	V
Supply Current	IDD		1.9	3.0	3.9	mA
Reverse supply current	Iddrev	V <sub>DD</sub> = -16V	-1	-	-	mA
Output Saturation Voltage	Vol	V <sub>DD</sub> = 3.5 to 24V, I <sub>OUT</sub> = 20mA	-	0.25	0.5	V
Output Leakage	loff	V <sub>OUT</sub> = 12V, V <sub>DD</sub> = 12V	-	-	10	μΑ
Output Rise Time <sup>12, 15</sup> (R <sub>PU</sub> dependent)	tr	$R_{PU} = 1k\Omega$ , $V_{DD} = 12V$ , $V_{PU} = 5V$ $C_{LOAD} = 50pF$ to GND	0.1	0.3	1	μs
Output Fall Time <sup>12, 15</sup> (On-chip controlled )	t <sub>F</sub>	$R_{PU} = 1k\Omega$ , $V_{DD} = 12V$ , $V_{PU} = 5V$ $C_{LOAD} = 50pF$ to GND	0.1	0.3	1	μs
Power-On Time <sup>13, 14</sup>	ton	V <sub>DD</sub> = 5V, dV <sub>DD</sub> /dt > 2V/us, activated output with >1mT overdrive	-	40	70	μs
Power-On Output State	-	t < ton	High (VPU)		-	
Output Current Limit	Icl	V <sub>DD</sub> =3.5 to 24V, V <sub>OUT</sub> = 12V	25	40	70	mA
Output ON Time under Current Limit conditions <sup>15, 16</sup>	tclon	$V_{PU} = 12V$ , $R_{PU} = 100\Omega$	180	240	-	μs
Output OFF Time under Current Limit conditions <sup>15, 16</sup>	<b>t</b> cloff	$V_{PU} = 12V$ , $R_{PU} = 100\Omega$	-	3.5	-	ms
Chopping Frequency	f <sub>снор</sub>			350	-	kHz
Refresh Period	<b>t</b> PER		-	6	-	μs
Delay Time <sup>12, 17</sup>	t₀	Average over 1000 successive	-	7.5	-	μs

<sup>&</sup>lt;sup>11</sup> Typical values are defined at TA = +25°C and VDD = 12V

<sup>&</sup>lt;sup>12</sup> Guaranteed by design and verified by characterization, not production tested

<sup>&</sup>lt;sup>13</sup> The Power-On Time represents the time from reaching  $V_{\rm DD}$  = 2.7V to the first refresh of the output

<sup>&</sup>lt;sup>14</sup> Power-On Slew Rate is not critical for the proper device start-up.

 $<sup>^{15}</sup>$  R<sub>PU</sub> and V<sub>PU</sub> are respectively the external pull-up resistor and pull-up power supply

<sup>&</sup>lt;sup>16</sup> If the Output is in Current Limitation longer than  $t_{CLON}$  the Output is switched off in high-impedance state. The Output returns back in active state at next reaching of  $B_{OP}$  or after  $t_{CLOFF}$  time interval REVISION 2 – DECEMBER 20, 2016

End of line programmable Hall-effect Latch / Switch



Parameter	Symbol	Test Conditions	Min	Typ <sup>11</sup>	Max	Units
		switching events @10kHz, Latch, Bop set to 5mT, square wave magnetic field with B > ±20mT, t <sub>RISE</sub> = t <sub>FALL</sub> ≤ 20µs				
Output Jitter (p-p) <sup>12, 18</sup>	<b>t</b> jitter	Over 1000 successive switching events @ 1kHz, Latch, Bop set to 5mT, square wave magnetic field with B > ±20mT, trise = tfall ≤ 20µs	-	±4	-	μѕ
Maximum Switching Frequency 12, 19	fsw	Latch, Bop set to 5mT, square wave magnetic field with B > ±20mT	30	50	-	kHz
Under-voltage Lockout Threshold	Vuvl		-	-	2.7	V
Under-voltage Lockout Reaction time <sup>12</sup>	tuvl		-	1	-	μs
Thermal Protection Threshold	Тргот	Junction temperature	-	190	-	°C
Thermal Protection Release	$T_{REL}$	Junction temperature	-	180	-	°C

Table 2 - General Electrical Specifications

<sup>&</sup>lt;sup>17</sup> The Delay Time is the time from magnetic threshold reached to the start of the output switching

<sup>&</sup>lt;sup>18</sup> Output jitter is the unpredictable deviation of the Delay Time

<sup>&</sup>lt;sup>19</sup> Maximum switching frequency corresponds to the maximum frequency of the applied magnetic field which is detected without loss of pulses



# 8. Magnetic Specifications

DC Operating Parameters V<sub>DD</sub> = 3.5V to 24V, T<sub>A</sub> = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	<b>Test Conditions</b>	Min	Typ <sup>1</sup>	Max	Units
Latch Threshold Programming Range (2, 6)	Вьтн	V <sub>DD</sub> =12V, T <sub>A</sub> =25°C	±0.4		±80	mT
Switch Operating Point Programming Range (3, 6)	Вор	V <sub>DD</sub> =12V, T <sub>A</sub> =25°C	±1.5		±66	mT
Proportional Hysteresis Ratio Programming Range HYSRATIO (4, /)	HYSratio	V <sub>DD</sub> =12V, T <sub>A</sub> =25°C	0.10		0.55	
Fixed Hysteresis Value 0 (5)	Вгнуѕо			0		mT
Fixed Hysteresis Value 1 (5, 8)	B <sub>FHYS1</sub>			1		mT
Fixed Hysteresis Value 2 (5)	B <sub>FHYS2</sub>			1.2		mT
Fixed Hysteresis Value 3 (5)	Вгнуѕз			1.4		mT
Fixed Hysteresis Value 4 (5)	Вгнүѕ4			1.8		mT
Fixed Hysteresis Value 5 (5)	Вгнуѕѕ			2.2		
Latch sensor Magnetic Offset (Bop +	Boffset	T <sub>A</sub> = 25°C	-0.5		+0.5	mT
Brp)/2	BOFFSET (8)	T <sub>A</sub> = -40°C to 150°C	-0.9		+0.9	mT
		Flat		0		
(9)		SmCo,		- 400		
Temperature Coefficient <sup>(9)</sup>	TC	NdFeB,		- 1100		ppm/ºC
		Hard Ferrite,		- 2000		
Factory Programmed Bop, Switch	Вор	$V_{DD} = 12V$ , $T_A = 25$ °C, programming target 28mT	26	28	30	mT
Factory Programmed B <sub>RP</sub> , Switch	Brp	$V_{DD} = 12V$ , $T_A = 25$ °C, programming target 28mT, $HYS_{RATIO} = 0.75$	19	21	23	mT

Table 3: Magnetic specifications

The hysteresis is programmable for each Bop point with a fixed value or proportional (ratiometric) to BOP:

1) Ratiometric hysteresis example:  $B_{OP} = 10mT \implies 4.5mT \le B_{RP} \le 9mT$  2) Fixed hysteresis example:  $B_{OP} = 10mT \implies 7.8mT \le B_{RP} \le 9mT$ 

$$TC = \frac{B_{150} - B_{-40}}{190 * B_{25}} * 10^6, \text{ ppm/}^{\circ}\text{C}$$

<sup>1.</sup> The typical values are defined at  $T_A = 25$  °C and  $V_{DD} = 12$ V

<sup>2.</sup> For Latch sensor  $B_{LTH} = (B_{OP} - B_{RP})/2$ . The Latch programming step is typically between 0.7% and 1.5% of the programmed  $B_{LTH}$  value for  $|B_{LTH}| \le 1.2$ mT and 0.018mT for  $|B_{LTH}| \le 1.2$ mT

<sup>3.</sup> For Switch sensor the  $B_{OP}$  programming step is typically between 0.7% and 1.5% of the programmed  $B_{OP}$  value for  $|B_{OP}| \ge 4.8$ mT and 0.072mT for  $|B_{OP}| \le 4.8$ mT

<sup>4.</sup> For Switch sensor with proportional hysteresis HYSRATIO = BHYS / BOP. The HYSRATIO programming step is 0.05

<sup>5.</sup> For Switch sensor with fixed hysteresis value

<sup>6.</sup> Guaranteed by design and verified by characterization. The programming ranges for BLTH and Bop include some margin for process deviations

<sup>7.</sup> The given min/max limits are typical values

<sup>8.</sup> Guaranteed by design and verified by characterization

<sup>9.</sup> The Temperature Coefficient is calculated using following formula:



# 9. Programming parameters

Parameter	Symbol	Comments	Value	Units
Bop programming resolution	BOPFINE	Fine programming of thresholds Bop (Switch) and Blth (Latch)		Bit
Bop sub-range	Boprange	Selection of the appropriate Switch sensor sub-range	2	Bit
Вітн sub-range	Boprange	Selection of the appropriate Latch sensor sub-range	3	Bit
Programmable Hysteresis	Внуѕт	Hysteresis can be fixed or proportional (ratiometric)		Bit
		Part can be programmed for South (default) or North magnetic pole		
Active Pole Selection	BPOLE	active	1	Bit
Output Polarity Selection	РоІоит	Selects Direct or Inverted sensor Output Polarity	1	Bit
Switch/Latch Function				
Selection		Selects Latch (default) or Switch sensor function	1	Bit
Malayis programmed ID	ID	A unique fixed ID is implemented for device traceability, no	22	D:+
Melexis programmed ID	ID	overwriting allowed	32	Bit

Table 4: Programmable Parameters

# 10. Magnetic Behaviour

#### 10.1. Definitions:

Operation Point BOP – magnetic threshold for activation of the device output, turning in ON (low) state.

Release Point B<sub>RP</sub> – magnetic threshold for release of the device output, turning in OFF (high) state.

Hysteresis  $B_{HYS}$  – magnetic hysteresis,  $B_{HYS} = B_{OP} - B_{RP}$ 

United Latch Threshold B<sub>LTH</sub> – used for Latch sensor programming:

 $B_{LTH}$  =  $(B_{OP} - B_{RP})/2$  =  $B_{HYS}/2$ , targeting symmetrical placement of both Latch sensor thresholds vs. zero  $B_{RP} \approx -B_{OP}$  **Proportional Hysteresis to Operation Point Ratio** – used for Switch sensor with proportional hysteresis:  $HYS_{RATIO}$  =  $B_{HYS}/B_{OP}$ 

#### 10.2. Latch sensor

Parameter	Pole Active	Remark
Option 1	South	Fig.1
Option 3	North	Fig.2

Note: Latch sensors are inherently Direct South or Direct North Pole Active only.

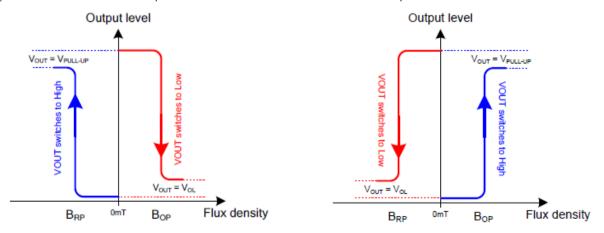


Fig.1 - South Pole Active

Fig.2 -North Pole Active



# 10.3. Unipolar Switch Sensor

Parameter	Pole Active	Magnetic Polarity	Remark
Option 1	South	Direct	Fig.1
Option 2	South	Inverted	Fig.2
Option 3	North	Direct	Fig.3
Option 4	North	Inverted	Fig.4

Table 5: Unipolar switch sensor

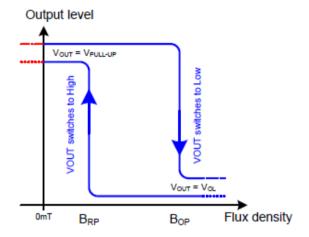


Fig.1 - Direct South Pole Active

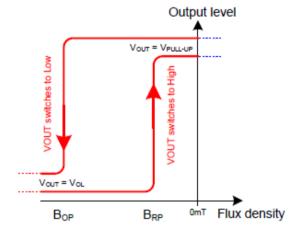


Fig.3 - Direct North Pole Active

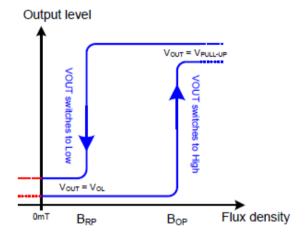


Fig.2 - Inverted South Pole Active

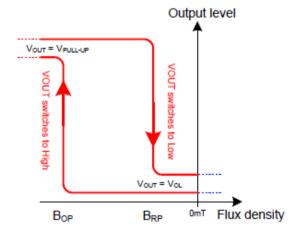
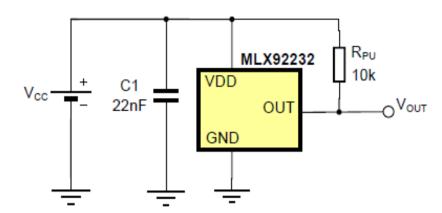


Fig.4 - Inverted North Pole Active



# 11. Application Information

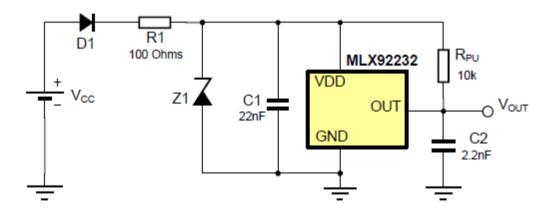
### 11.1. Typical Three-Wire Application Circuit



#### Notes:

- 1. For proper operation, a 10nF to 22nF bypass capacitor should be placed as close as possible to the VDD and ground pin.
- 2. The pull-up resistor RPU value should be chosen in to limit the current through the output pin below the maximum allowed continuous current for the device.
- 3. A capacitor connected to the output is not needed, because the output slope is generated internally.

## 11.2. Harsh, Noisy Environments Three-Wire Circuit



#### Notes:

- 1. For proper operation, a 10nF to 22nF bypass capacitor should be placed as close as possible to the V<sub>DD</sub> and ground pin.
- 2. The device could tolerate negative voltage down to -27V, so if negative transients over supply line  $V_{PEAK} < -32V$  are expected, usage of the diode D1 is recommended. Otherwise only R1 is sufficient.

When selecting the resistor R1, three points are important:

- the resistor has to limit  $I_{\mbox{\scriptsize DD}}/I_{\mbox{\scriptsize DDREV}}$  to 50mA maximum
- the resistor has to withstand the power dissipated in both over voltage conditions (V<sub>R1</sub><sup>2</sup>/R1)
- the resulting device supply voltage  $\rm V_{DD}$  has to be higher than  $\rm V_{DD}$  min ( $\rm V_{DD}$  =  $\rm V_{CC}$  R1.I  $_{DD}$ )
- 3. The device could tolerate positive supply voltage up to +27V (until the maximum power dissipation is not exceeded), so if positive transients over supply line with  $V_{PEAK} > 32V$  are expected, usage a zener diode Z1 is recommended. The R1-Z1 network should be sized to limit the voltage over the device below the maximum allowed.
- 4. Limit C1 to max. 22nF and C2 to 2.2nF in case end of line programming is requested.



# 12. Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

#### Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020
   Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
   Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

#### Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20
   Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15
   Resistance to soldering temperature for through-hole mounted devices

#### Iron Soldering THD's (Through Hole Devices)

EN60749-15
 Resistance to soldering temperature for through-hole mounted devices

#### Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

 EIA/JEDEC JESD22-B102 and EN60749-21 Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis recommends reviewing on our web site the General Guidelines <u>soldering recommendation</u> (<a href="http://www.melexis.com/Quality\_soldering.aspx">http://www.melexis.com/Quality\_soldering.aspx</a>) as well as <a href="mailto:trim&form recommendations">trim&form recommendations</a> (<a href="http://www.melexis.com/Assets/Trim-and-form-recommendations-5565.aspx">http://www.melexis.com/Assets/Trim-and-form-recommendations-5565.aspx</a>).

Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: http://www.melexis.com/quality.aspx

#### 13. ESD Precautions

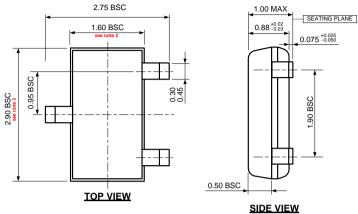
Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).

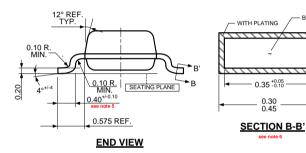
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.



# 14. Package Information SE

## 14.1. (TSOT-3L) Package Information





# <u>N</u> 1 2 3

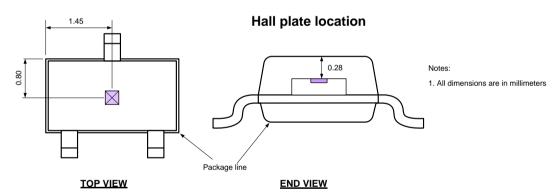
#### Notes:

- 1. All dimensions are in millimeters
- Outermost plastic extreme width does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15mm per side.
- Outermost plastic extreme length does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.25mm per side.
- The lead width dimension does not include dambar protrusion.
   Allowable dambar protrusion shall be 0.07mm total in excess of the lead width dimension at maximum material condition.
- 5. Dimension is the length of terminal for soldering to a substrate.
- Dimension on SECTION B-B' applies to the flat section of the lead between 0.08mm and 0.15mm from the lead tip.
- 7. Formed lead shall be planar with respect to one another with 0.076mm at seating plane.

#### Marking:

Top mark: 31ww ==> ww; assembly week

Bottom mark: YLLL ==> Y; year LLL= last 3 digits of lotnr



0.127

SE Pin №	Name	Туре	Function
1	VDD	Supply	Supply
2	OUT	Outpu	Open Drain
3	GND	Groun	Ground pin

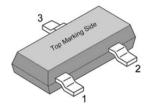
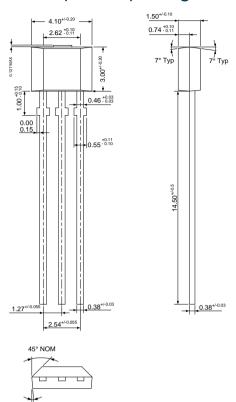


Table 3: SE Package pinout



# 14.2. UA (TO92-3L) Package Information



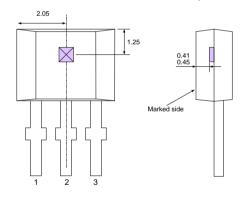
#### Notes:

- 1. All dimensions are in millimeters
- 2. Package dimension exclusive molding flash.
- 3. The end flash shall not exceed 0.127 mm on the top side.

#### Marking:

 $1^{\text{st}}$  Line : 31WW  $\Rightarrow$  WW - calendar week  $2^{\text{nd}}$  Line : YLLL:  $\Rightarrow$  Y - last digit of year LLL- Lot nr (3 digits)

#### Hall plate location



#### Notes:

1. All dimensions are in millimeters



UA Pin №	Name	Туре	Function
1	VDD	Supply	Supply
2	GND	Ground	Ground pin
3	OUT	Output	Open Drain

Table 4: UA Package pinout



#### 15. Contact

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For additional information, please contact our Direct Sales team and get help for your specific needs:

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