

TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

## 2SC5464

## VHF~UHF Band Low Noise Amplifier Applications

Unit: mm

- Low noise figure, high gain.
- $NF = 1.1\text{dB}$ ,  $|S_{21e}|^2 = 12\text{dB}$  ( $f = 1\text{GHz}$ )

Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	20	V
Collector-emitter voltage	$V_{CEO}$	12	V
Emitter-base voltage	$V_{EBO}$	3	V
Collector current	$I_C$	60	mA
Base current	$I_B$	30	mA
Collector power dissipation	$P_C$	100	mW
Junction temperature	$T_j$	125	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-55 to 125	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

		1. BASE
		2. EMITTER
		3. COLLECTOR
SSM		
JEDEC	—	
JEITA	—	
TOSHIBA	2-2H1A	

Weight: 2.4 mg (typ.)

Microwave Characteristics ( $T_a = 25^\circ\text{C}$ )

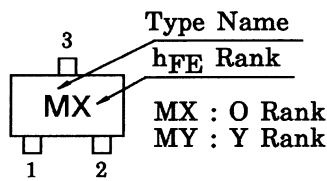
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Transition frequency	$f_T$	$V_{CE} = 8\text{V}$ , $I_C = 15\text{mA}$	5	7	—	GHz
Insertion gain	$ S_{21e} ^2 (1)$	$V_{CE} = 8\text{V}$ , $I_C = 15\text{mA}$ , $f = 500\text{MHz}$	—	17.5	—	dB
	$ S_{21e} ^2 (2)$	$V_{CE} = 8\text{V}$ , $I_C = 15\text{mA}$ , $f = 1\text{GHz}$	8	12	—	
Noise figure	NF (1)	$V_{CE} = 8\text{V}$ , $I_C = 5\text{mA}$ , $f = 500\text{MHz}$	—	1	—	dB
	NF (2)	$V_{CE} = 8\text{V}$ , $I_C = 5\text{mA}$ , $f = 1\text{GHz}$	—	1.1	2	

Electrical Characteristics ( $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	$I_{CBO}$	$V_{CB} = 10\text{V}$ , $I_E = 0$	—	—	1	$\mu\text{A}$
Emitter cut-off current	$I_{EBO}$	$V_{EB} = 1\text{V}$ , $I_C = 0$	—	—	1	$\mu\text{A}$
DC current gain	$h_{FE}$ (Note 1)	$V_{CE} = 8\text{V}$ , $I_C = 15\text{mA}$	80	—	240	
Output capacitance	$C_{ob}$	$V_{CB} = 8\text{V}$ , $I_E = 0$ , $f = 1\text{MHz}$ (Note 2)	—	0.6	—	pF
Reverse transfer capacitance	$C_{re}$		—	0.35	—	pF

Note 1:  $h_{FE}$  classification O: 80 to 160, Y: 160 to 240Note 2:  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

Marking



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