

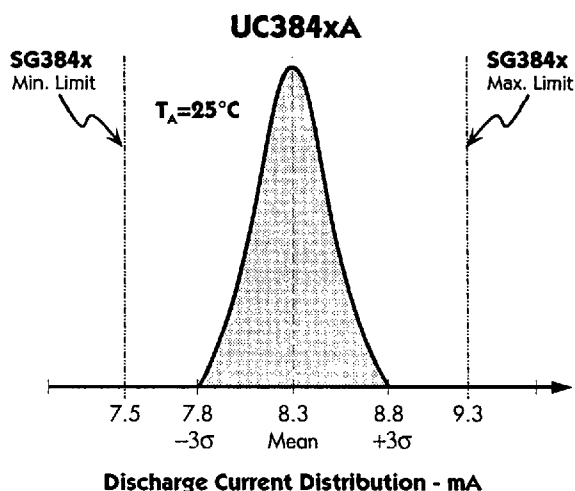
DESCRIPTION

The UC184xA family of control IC's provides all the necessary features to implement off-line fixed-frequency, current-mode switching power supplies with a minimum of external components. The current mode architecture demonstrates improved load regulation, pulse-by-pulse current limiting and inherent protection of the power supply output switch. The IC includes: A bandgap reference trimmed to $\pm 1\%$ accuracy, an error amplifier, a current sense comparator with internal clamp to 1V, a high current totem pole output stage for fast switching of power

mosfet's, and an externally program-mable oscillator to set frequency and maximum duty cycle. The under-voltage lock-out is designed to operate with 250 μ A typ. start-up current, allowing an efficient bootstrap supply voltage design. Available options for this family of products, such as start-up voltage hysteresis and duty cycle, are summarized below in the Available Options section. The UC184xA family of control IC's is also available in 14-pin SOIC package which makes the Power Output Stage Collector and Ground pins available.

PRODUCT HIGHLIGHT

COMPARISON OF UC384xA VS. SG384x DISCHARGE CURRENT



KEY FEATURES

- **LOW START-UP CURRENT.** (0.5mA max.)
- **TRIMMED OSCILLATOR DISCHARGE CURRENT.** (See Product Highlight)
- OPTIMIZED FOR OFF-LINE AND DC-TO-DC CONVERTERS.
- AUTOMATIC FEED FORWARD COMPENSATION.
- PULSE-BY-PULSE CURRENT LIMITING.
- ENHANCED LOAD RESPONSE CHARACTERISTICS.
- UNDER-VOLTAGE LOCKOUT WITH HYSTERESIS.
- DOUBLE PULSE SUPPRESSION.
- HIGH-CURRENT TOTEM POLE OUTPUT.
- INTERNALLY TRIMMED BANDGAP REFERENCE.
- 500KHz OPERATION.
- LOW R_o ERROR AMPLIFIER.

APPLICATIONS

- ECONOMICAL OFF-LINE FLYBACK OR FORWARD CONVERTERS.
- DC-DC BUCK OR BOOST CONVERTERS.
- LOW COST DC MOTOR CONTROL.

AVAILABLE OPTIONS

Part #	Start-Up Voltage	Hysteresis	Max. Duty Cycle
UCx842A	16V	6V	<100%
UCx843A	8.4V	0.8V	<100%
UCx844A	16V	6V	<50%
UCx845A	8.4V	0.8V	<50%

PACKAGE ORDER INFORMATION

T_A (°C)	M Plastic DIP 8-pin	DM Plastic SOIC 8-pin	D Plastic SOIC 14-pin	Y Ceramic DIP 8-pin
0 to 70	UC384xAM	UC384xADM	UC384xAD	—
-40 to 85	UC284xAM	UC284xADM	UC284xAD	UC284xAY
-55 to 125	—	—	—	UC184xAY

Note: All surface-mount packages are available in Tape & Reel. Append the letter "T" to part number. (i.e. UC3842ADMT)

FOR FURTHER INFORMATION CALL (714) 898-8121

11861 WESTERN AVENUE, GARDEN GROVE, CA. 92641

8253880 0005133 940

UC184xA/284xA/384xA

CURRENT MODE PWM CONTROLLER

PRODUCTION DATA SHEET

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage (Low Impedance Source) (V_{CC})	30V
Supply Voltage ($I_{CC} < 30mA$)	Self Limiting
Output Current	$\pm 1A$
Output Energy (Capacitive Load)	5 μJ
Analog Inputs (V_{FB} & I_{SENSE})	-0.3V to +6.3V
Error Amp Output Sink Current	10mA
Power Dissipation at $T_A = 25^\circ C$ (M Package)	1W
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 Seconds)	300°C

Note 1. Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of the specified terminal. Pin numbers refer to DIL packages only.

THERMAL DATA

M PACKAGE:

THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	95°C/W
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DM PACKAGE:

THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	165°C/W
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D PACKAGE:

THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	120°C/W
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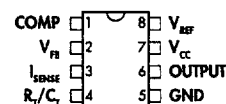
Y PACKAGE:

THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	130°C/W
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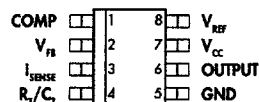
Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$.

The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow

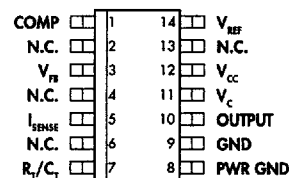
PACKAGE PIN OUTS



M & Y PACKAGE
(Top View)



DM PACKAGE
(Top View)



D PACKAGE
(Top View)

CURRENT MODE PWM CONTROLLER

PRODUCTION DATA SHEET

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, these specifications apply over the operating ambient temperatures for UC384xA with $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$, UC284xA with $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$, UC184xA with $-55^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$; $V_{CC}=15\text{V}$; $R_T=10\text{K}$; $C_T=3.3\text{nF}$. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

Parameter	Symbol	Test Conditions	UC184xA/284xA			UC384xA			Units
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Reference Section									
Output Voltage	V_{REF}	$T_J = 25^{\circ}\text{C}, I_L = 1\text{mA}$	4.95	5.00	5.05	4.90	5.00	5.10	V
Line Regulation		$12 \leq V_{IN} \leq 25\text{V}$		6	20		6	20	mV
Load Regulation		$1 \leq I_O \leq 20\text{mA}$		6	25		6	25	mV
Temperature Stability (Note 2 & 7)				0.2	0.4		0.2	0.4	mV/ $^{\circ}\text{C}$
Total Output Variation		Over Line, Load, and Temperature	4.9		5.1	4.82		5.18	V
Output Noise Voltage (Note 2)	V_N	$10\text{Hz} \leq f \leq 10\text{kHz}, T_J = 25^{\circ}\text{C}$		50			50		μV
Long Term Stability (Note 2)		$T_A = 125^{\circ}\text{C}, t = 1000\text{hrs}$		5	25		5	25	mV
Output Short Circuit Current	I_{SC}		-30	-100	-180	-30	-100	-180	mA
Oscillator Section									
Initial Accuracy (Note 6)		$T_J = 25^{\circ}\text{C}$	47	52	57	47	52	57	kHz
Voltage Stability		$12 \leq V_{CC} \leq 25\text{V}$		0.2	1		0.2	1	%
Temperature Stability (Note 2)		$T_{MIN} \leq T_A \leq T_{MAX}$		5			5		%
Amplitude (Note 2)				1.7			1.7		V
Discharge Current		$T_J = 25^{\circ}\text{C}, V_{PIN4} = 2\text{V}$	7.8	8.3	8.8	7.8	8.3	8.8	mA
		$V_{PIN4} = 2\text{V}, T_{MIN} \leq T_A \leq T_{MAX}$	7.5		8.8	7.6		8.8	mA
Error Amp Section									
Input Voltage		$V_{PIN1} = 2.5\text{V}$	2.45	2.50	2.55	2.42	2.50	2.58	V
Input Bias Current	I_B			-0.3	-1		-0.3	-2	μA
Open Loop Gain	A_{VOL}	$2 \leq V_O \leq 4\text{V}$	65	90		65	90		dB
Unity Gain Bandwidth (Note 2)	UGBW	$T_J = 25^{\circ}\text{C}$	0.7	1		0.7	1		MHz
Power Supply Rejection Ratio (Note 3)	PSRR	$12 \leq V_{CC} \leq 25\text{V}$	60	70		60	70		dB
Output Sink Current	I_{OL}	$V_{PIN2} = 2.7\text{V}, V_{PIN1} = 1.1\text{V}$	2	6		2	6		mA
Output Source Current	I_{OH}	$V_{PIN2} = 2.3\text{V}, V_{PIN1} = 5\text{V}$	-0.5	-0.8		-0.5	-0.8		mA
Output Voltage High Level	V_{OH}	$V_{PIN2} = 2.3\text{V}, R_L = 15\text{K to ground}$	5	6		5	6		V
Output Voltage Low Level	V_{OL}	$V_{PIN2} = 2.7\text{V}, R_L = 15\text{K to } V_{REF}$		0.7	1.1		0.7	1.1	V
Current Sense Section									
Gain (Note 3 & 4)	A_{VOL}		2.85	3	3.15	2.85	3	3.15	V/V
Maximum Input Signal (Note 3)		$V_{PIN1} = 5\text{V}$	0.9	1	1.1	0.9	1	1.1	V
Power Supply Rejection Ratio (Note 3)	PSRR	$12 \leq V_{CC} \leq 25\text{V}$		70			70		dB
Input Bias Current	I_B			-2	-10		-2	-10	μA
Delay to Output (Note 2)	T_{pd}	$V_{PIN3} = 0 \text{ to } 2\text{V}$		150	300		150	300	ns
Output Section									
Output Low Level	V_{OL}	$I_{SINK} = 20\text{mA}$		0.1	0.4		0.1	0.4	V
		$I_{SINK} = 200\text{mA}$		1.5	2.2		1.5	2.2	V
Output High Level	V_{OH}	$I_{SOURCE} = 20\text{mA}$	13	13.5		13	13.5		V
		$I_{SOURCE} = 200\text{mA}$	12	13.5		12	13.5		V
Rise Time (Note 2)	T_R	$T_J = 25^{\circ}\text{C}, C_L = 1\text{nF}$		50	150		50	150	ns
Fall Time (Note 2)	T_F	$T_J = 25^{\circ}\text{C}, C_L = 1\text{nF}$		50	150		50	150	ns
UVLO Saturation	V_{SAT}	$V_{CC} = 5\text{V}, I_{SINK} = 10\text{mA}$		0.7	1.2		0.7	1.2	V

(Electrical Characteristics continue next page.)

UC184xA/284xA/384xA

CURRENT MODE PWM CONTROLLER

PRODUCTION DATA SHEET

ELECTRICAL CHARACTERISTICS (Con't.)

Parameter	Symbol	Test Conditions	UC184xA/284xA			UC384xA			Units
			Min.	Typ.	Max.	Min.	Typ.	Max.	
► Under-Voltage Lockout Section									
Start Threshold		x842A/4A	15	16	17	14.5	16	17.5	V
		x843A/5A	7.8	8.4	9.0	7.8	8.4	9.0	V
Min. Operation Voltage After Turn-On		x842A/4A	9	10	11	8.5	10	11.5	V
		x843A/5A	7.0	7.6	8.2	7.0	7.6	8.2	V
► PWM Section									
Maximum Duty Cycle		x842A/3A	94	96	100	94	96	100	%
		x844A/5A	47	48	50	47	48	50	%
Minimum Duty Cycle					0			0	%
► Total Standby Section									
Start-Up Current				0.3	0.5		0.3	0.5	mA
Operating Supply Current	I _{cc}			11	17		11	17	mA
Zener Voltage	V _z	I _{cc} = 25mA	30	35		30	35		V

Notes: 2. These parameters, although guaranteed, are not 100% tested in production.

3. Parameter measured at trip point of latch with $V_{VFB} = 0$.

4. Gain defined as: $A_{VOL} = \frac{\Delta V_{COMP}}{\Delta V_{ISENSE}}$; $0 \leq V_{ISENSE} \leq 0.8V$.

5. Adjust V_{CC} above the start threshold before setting at 15V.

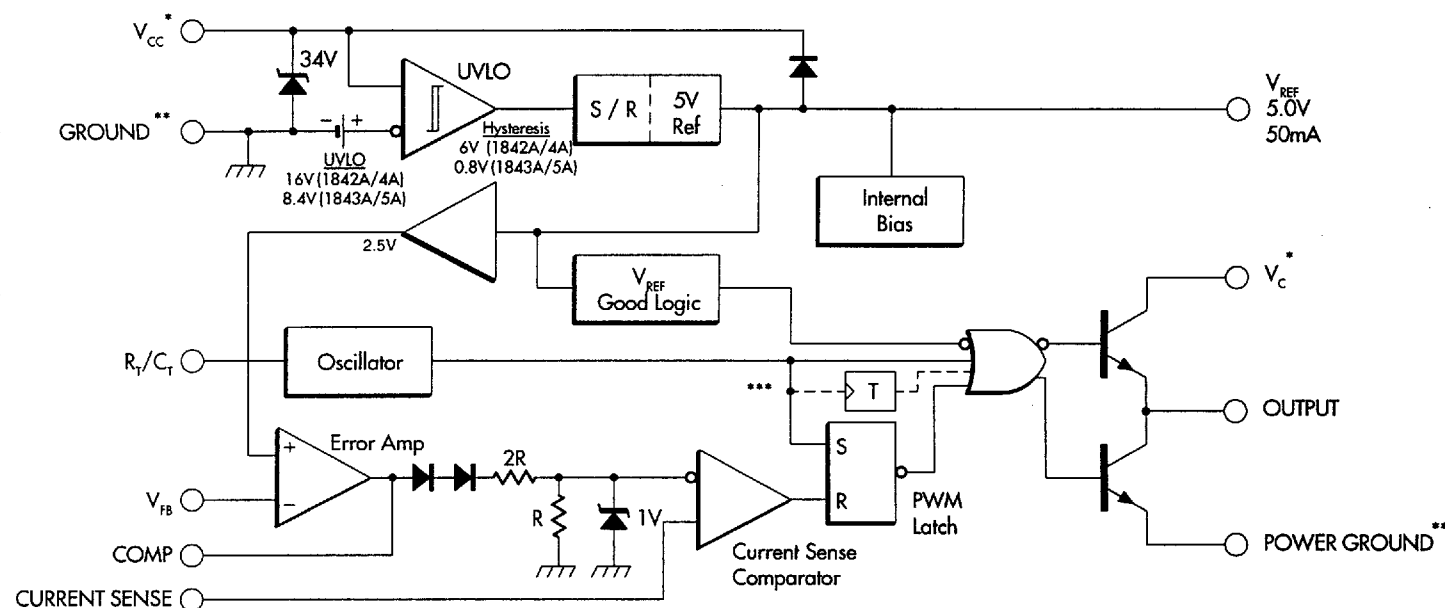
6. Output frequency equals oscillator frequency for the UC1842A and UC1843A. Output frequency is one half oscillator frequency for the UC1844A and UC1845A.

7. "Temperature stability, sometimes referred to as average temperature coefficient, is described by the equation:

$$\text{Temp Stability} = \frac{V_{REF}(\text{max.}) - V_{REF}(\text{min.})}{T_j(\text{max.}) - T_j(\text{min.})}$$

$V_{REF}(\text{max.})$ & $V_{REF}(\text{min.})$ are the maximum & minimum reference voltage measured over the appropriate temperature range. Note that the extremes in voltage do not necessarily occur at the extremes in temperature."

BLOCK DIAGRAM



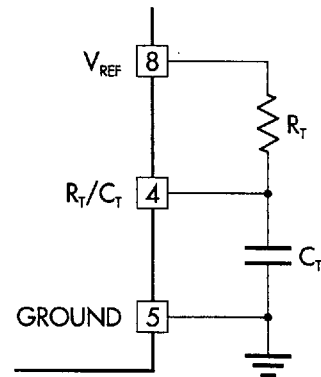
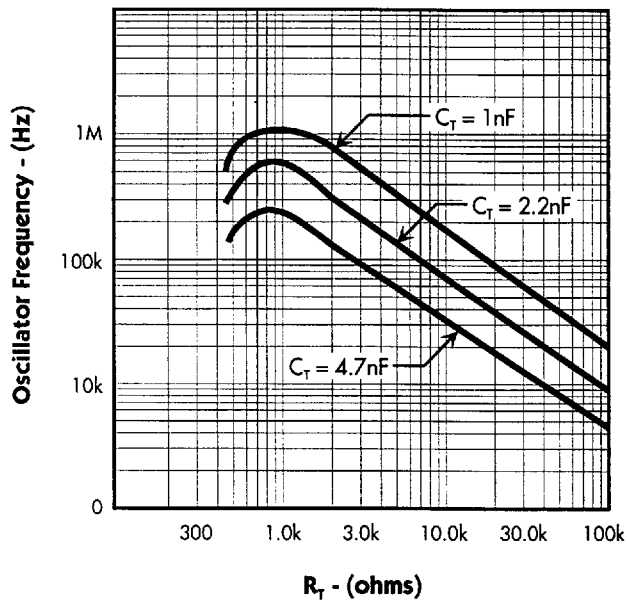
* - V_{CC} and V_C are internally connected for 8 pin packages.

** - POWER GROUND and GROUND are internally connected for 8 pin packages.

*** - Toggle flip flop used only in x844A and x845A series.

CHARACTERISTIC CURVES

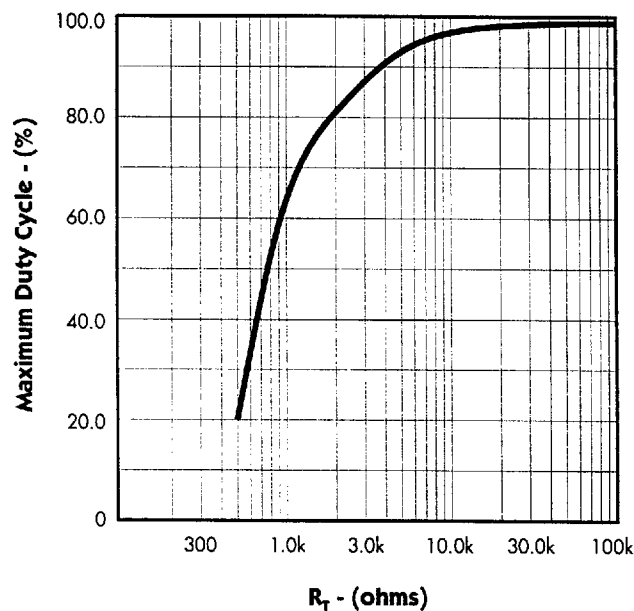
FIGURE 1. — OSCILLATOR FREQUENCY vs. TIMING RESISTOR



$$\text{For } R_T > 5k, f \approx \frac{1.72}{R_T C_T}$$

Note: Output drive frequency is half the oscillator frequency for the UCx844A/5A devices.

FIGURE 2. — MAXIMUM DUTY CYCLE vs. TIMING RESISTOR



PRODUCTION DATA SHEET

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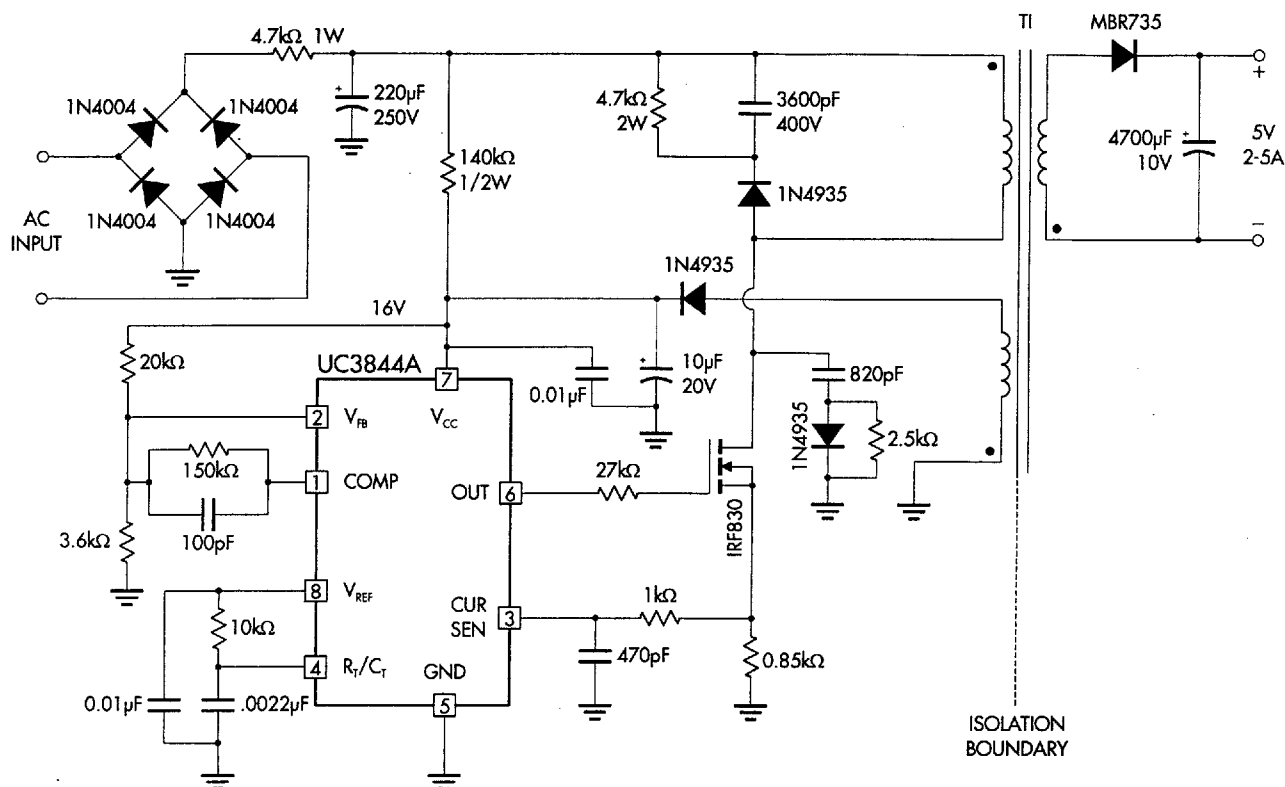
UC184xA/284xA/384xA

CURRENT MODE PWM CONTROLLER

PRODUCTION DATA SHEET

TYPICAL APPLICATION CIRCUITS (continued)

FIGURE 8. — OFF-LINE FLYBACK REGULATOR



SPECIFICATIONS

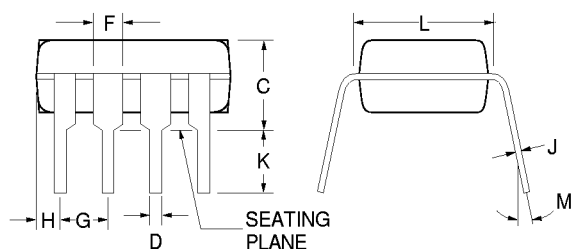
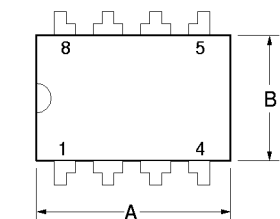
Input line voltage:	90VAC to 130VAC
Input frequency:	50 or 60Hz
Switching frequency:	40KHz $\pm 10\%$
Output power:	25W maximum
Output voltage:	5V $\pm 5\%$
Output current:	2 to 5A
Line regulation:	0.01%/V
Load regulation:	8%/A*
Efficiency @ 25 Watts,	
$V_{IN} = 90VAC$:	70%
$V_{IN} = 130VAC$:	65%
Output short-circuit current:	2.5Amp average

* This circuit uses a low-cost feedback scheme in which the DC voltage developed from the primary-side control winding is sensed by the UC3844A error amplifier. Load regulation is therefore dependent on the coupling between secondary and control windings, and on transformer leakage inductance.

Package Information

MECHANICAL DIMENSIONS

M 8-Pin Plastic Mini-Dip

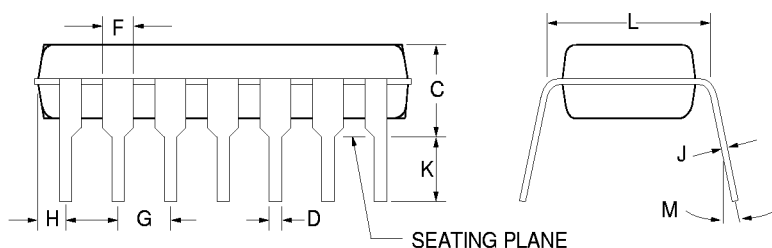
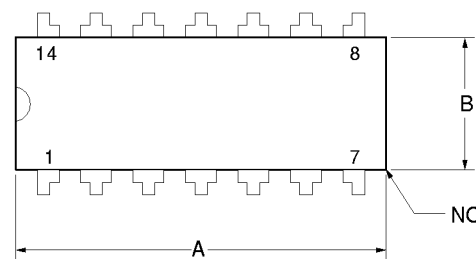


DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A		10.16		0.400
B	6.10	6.60	0.240	0.260
C		5.08		0.200
D	0.38	0.51	0.015	0.020
F	0.76	1.52	0.030	0.060
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.38	0.008	0.015
K	3.18		0.125	
L	7.62 BSC		0.300 BSC	
M		15		15

Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.15mm (.006") on any side. Lead dimension shall not include solder coverage.

N 14-Pin Plastic Dip



DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	18.54	20.57	0.730	0.810
B	6.09	6.60	0.240	0.260
C		5.08		0.200
D	0.38	0.51	0.015	0.020
F	0.76	1.52	0.030	0.060
G	2.54 BSC		0.100 BSC	
H	1.27	2.28	0.050	0.090
J	0.20	0.38	0.008	0.015
K	3.18		0.125	
L	7.62 BSC		0.300 BSC	
M		15		15

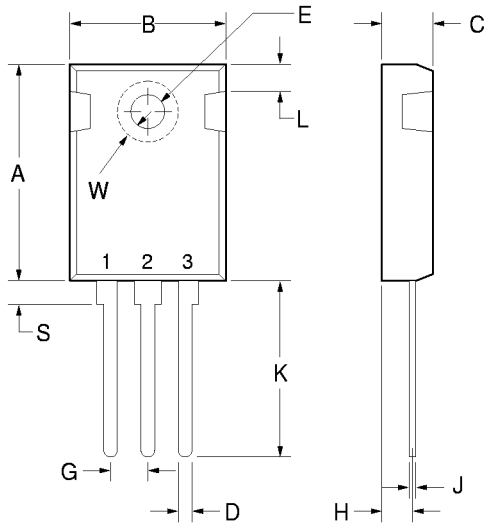
Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.15mm (.006") on any side. Lead dimension shall not include solder coverage.

Package Information

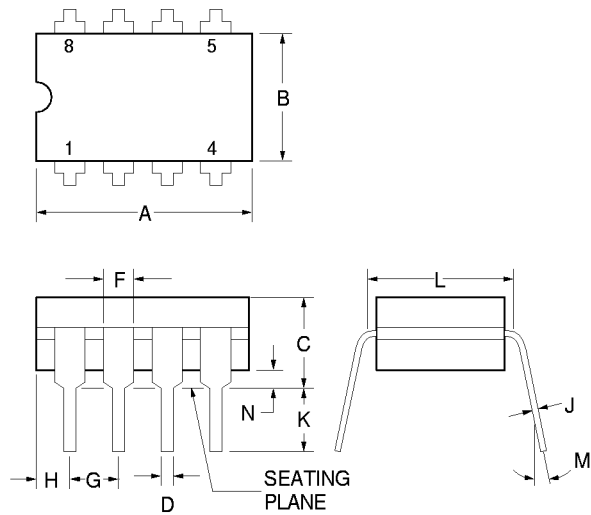
MECHANICAL DIMENSIONS

V 3-Pin Plastic TO-247



DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	20.83	21.34	0.820	0.840
B		16.26		0.640
C	4.93	5.33	0.190	0.210
D	1.02	1.40	0.040	0.055
E	3.18	3.43	0.125	0.135
G	5.46 BSC		0.215 BSC	
H		2.79		0.110
J	0.51	0.71	0.020	0.028
K	19.81	20.32	0.780	0.800
L	3.05	3.56	0.120	0.140
S		4.50		0.177
W		3.43		0.135

Y 8-Pin Ceramic Dip

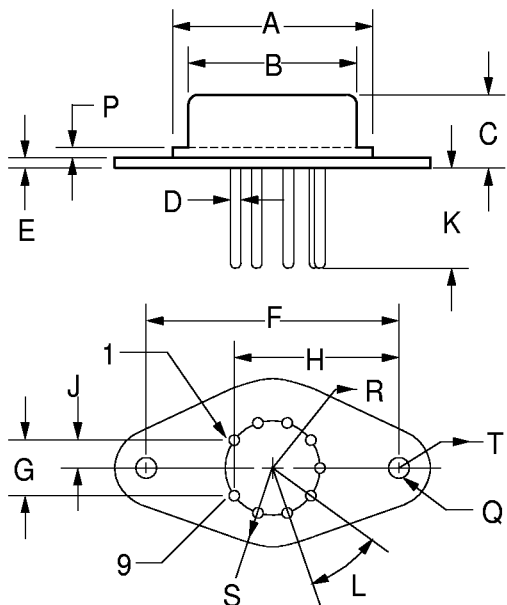


DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	9.91	10.92	0.390	0.430
B	5.59	7.11	0.220	0.280
C	4.32	5.08	0.170	0.200
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	1.14	1.65	0.045	0.065
J	0.20	0.38	0.008	0.015
K	3.18	4.06	0.125	0.160
L	7.37	7.87	0.290	0.310
M		15		15
N	0.51	1.02	0.020	0.040

Package Information

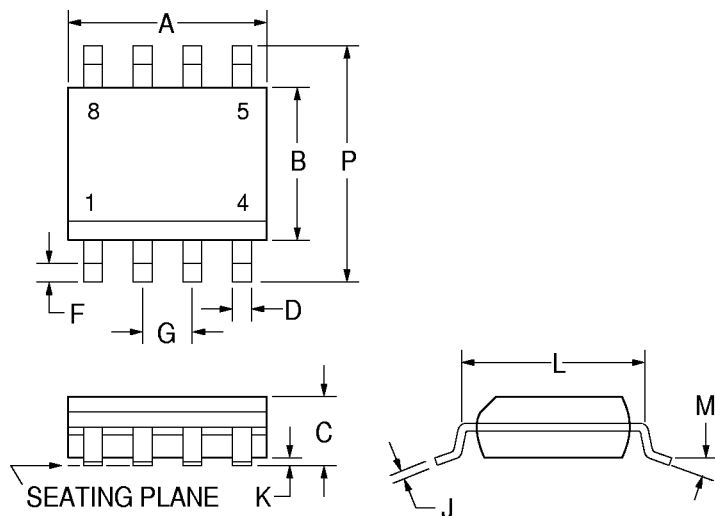
MECHANICAL DIMENSIONS

R 9-Pin Metal Can TO-66



DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A		15.75		0.620
B	11.94	12.70	0.470	0.500
C	6.60	7.62	0.260	0.300
D	0.71	0.86	0.028	0.034
E	1.27	1.91	0.050	0.075
F	24.33	24.43	0.958	0.962
G	4.83	5.33	0.190	0.210
H	14.48	14.99	0.570	0.590
J	2.36	2.72	0.093	0.107
K	9.14	10.41	0.360	0.410
L	36 TYP		36 TYP	
P		0.64		0.025
Q	3.61	3.86	0.142	0.152
R		4.11R		0.162R
S		8.89R		0.350R
T		3.68R		0.145R

DM 8-Pin Plastic SOIC



DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	4.83	5.00	0.190	0.197
B	3.81	3.94	0.150	0.155
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
F		0.77		0.030
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.007	0.010
K	0.13	0.25	0.005	0.010
L	4.80	5.21	0.189	0.205
M		8		8
P	5.79	6.20	0.228	0.244
*LC		0.10		0.004

* Lead Coplanarity

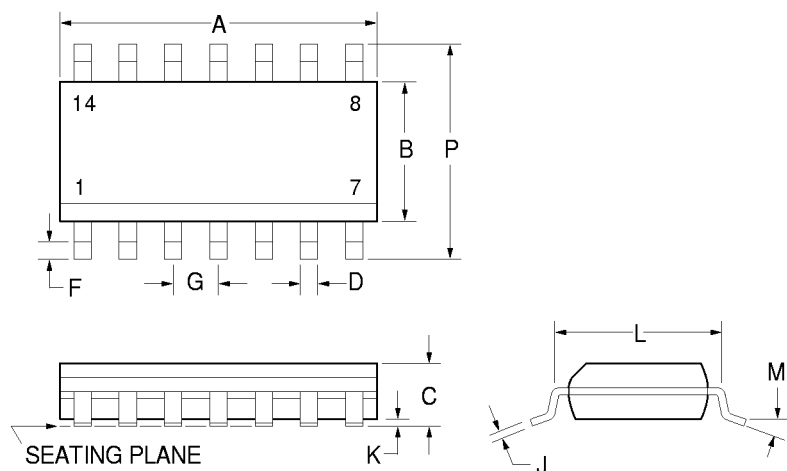
Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.15mm (.006") on any side. Lead dimension shall not include solder coverage.

Package Information

MECHANICAL DIMENSIONS

D 14-Pin Plastic SOIC



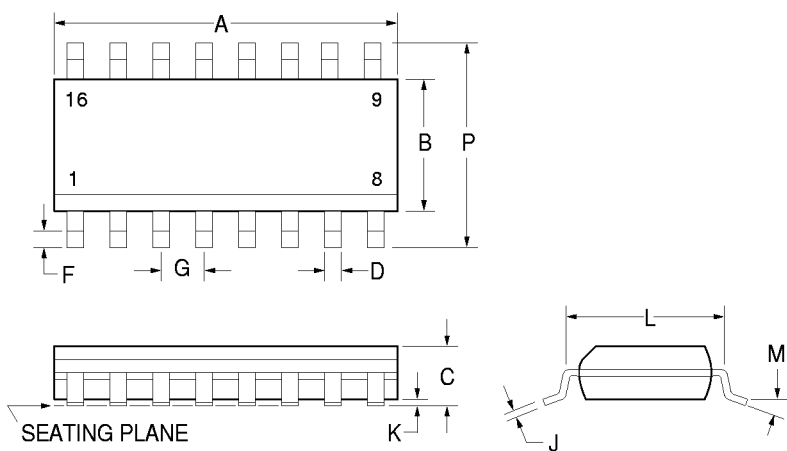
DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	8.54	8.74	0.336	0.344
B	3.81	3.94	0.150	0.155
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
F		0.77		0.030
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.007	0.010
K	0.10	0.25	0.004	0.010
L	4.82	5.21	0.189	0.205
M	0	8	0	8
P	5.79	6.20	0.228	0.244
*LC		0.10		0.004

* Lead Coplanarity

Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.15mm (.006") on any side. Lead dimension shall not include solder coverage.

D 16-Pin Plastic SOIC



DIM	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	9.78	10.01	0.385	0.394
B	3.81	4.01	0.150	0.158
C	1.35	1.75	0.053	0.069
D	0.35	0.46	0.014	0.018
F		0.77		0.030
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.007	0.010
K	0.10	0.25	0.004	0.010
L	4.82	5.21	0.189	0.205
M	0	8	0	8
P	5.79	6.20	0.228	0.244
*LC		0.10		0.004

* Lead Coplanarity

Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.15mm (.006") on any side. Lead dimension shall not include solder coverage.