

# DATA SHEET

**74F157A, 74F158A**

**Data selectors/multiplexers**

Product specification  
Supersedes data of 1996 Mar 12  
IC15 Data Handbook

2000 Jun 30

Data selectors/multiplexers

74F157A, 74F158A

74F157A: Quad 2-input data selector/multiplexer, non-inverting  
74F158A: Quad 2-input data selector/multiplexer, inverting

DESCRIPTION

The 74F157A is a high speed Quad 2-Input Multiplexer which selects 4 bits of data from one of two sources under the control of a common Select input (S). The Enable input ( $\bar{E}$ ) is active when Low. When  $\bar{E}$  is High, all of the outputs (Yn) are forced Low regardless of all other input conditions.

Moving data from two registers to a common output bus is a common use of the 74F157A. The state of the Select input determines the particular register from which the data comes.

The device is the logic implementation of a 4-pole, 2-position switch where the position of the switch is determined by the logic levels supplied to the Select input.

The 74F158A is similar, but has inverting outputs ( $\bar{Y}_n$ ).

- Industrial temperature range (−10°C to +85°C) available for 74F157A

| TYPE    | TYPICAL PROPAGATION DELAY | TYPICAL SUPPLY CURRENT (TOTAL) |
|---------|---------------------------|--------------------------------|
| 74F157A | 4.6ns                     | 15mA                           |
| 74F158A | 3.7ns                     | 10mA                           |

ORDERING INFORMATION

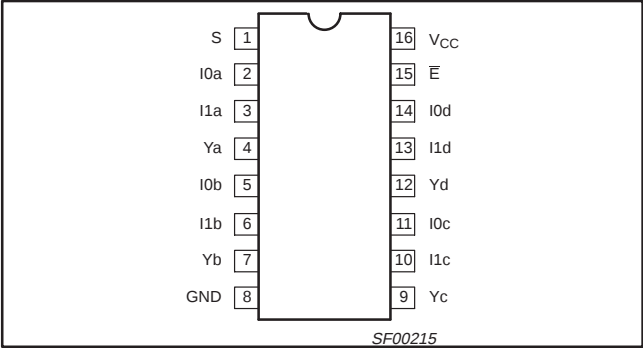
| DESCRIPTION        | ORDER CODE                                                                     |                                                                                  | PKG. DWG. # |
|--------------------|--------------------------------------------------------------------------------|----------------------------------------------------------------------------------|-------------|
|                    | COMMERCIAL RANGE<br>V <sub>CC</sub> = 5V ±10%, T <sub>amb</sub> = 0°C to +70°C | INDUSTRIAL RANGE<br>V <sub>CC</sub> = 5V ±10%, T <sub>amb</sub> = −40°C to +85°C |             |
| 16-pin plastic DIP | N74F157AN, N174F158AN                                                          | I74F157AN                                                                        | SOT38-4     |
| 16-pin plastic SO  | N74F157AD, N74F158AD                                                           | I74F157AD                                                                        | SOT109-1    |

INPUT AND OUTPUT LOADING AND FAN OUT TABLE

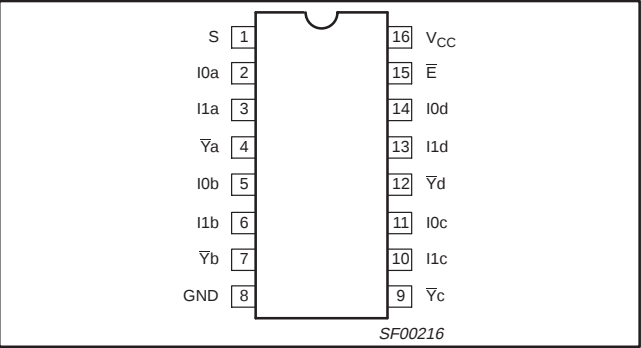
| PINS                                                                  | DESCRIPTION            | 74F (U.L.) HIGH/LOW | LOAD VALUE HIGH/LOW |
|-----------------------------------------------------------------------|------------------------|---------------------|---------------------|
| I <sub>1a</sub> , I <sub>1b</sub> , I <sub>1c</sub> , I <sub>1d</sub> | Data inputs            | 1.0/1.0             | 20μA/0.6mA          |
| S                                                                     | Select input           | 1.0/1.0             | 20μA/0.6mA          |
| $\bar{E}$                                                             | Enable input           | 1.0/1.0             | 20μA/0.6mA          |
| Y <sub>a</sub> –Y <sub>d</sub>                                        | Data outputs (74F157A) | 50/33               | 1.0mA/20mA          |
| $\bar{Y}_a$ – $\bar{Y}_d$                                             | Data outputs (74F158A) | 50/33               | 1.0mA/20mA          |

NOTE: One (1.0) FAST unit load is defined as: 20μA in the High state and 0.6mA in the Low state.

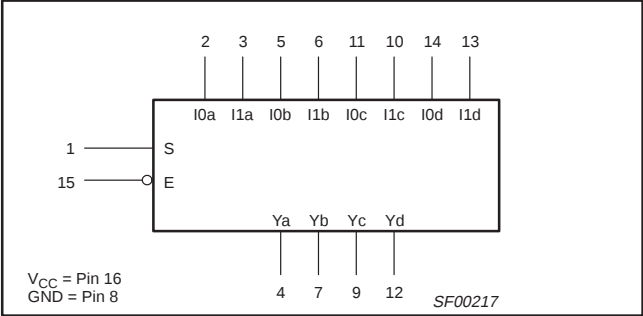
PIN CONFIGURATIONS, 74F157A



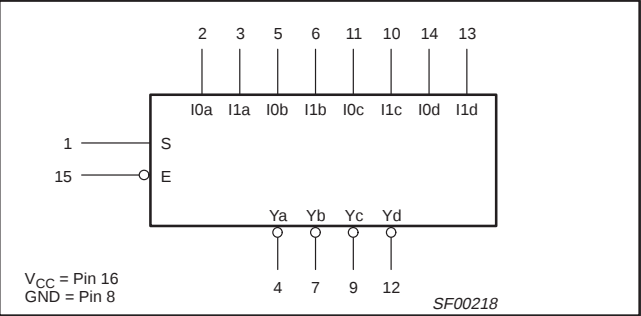
74F158A



LOGIC SYMBOLS, 74F157A



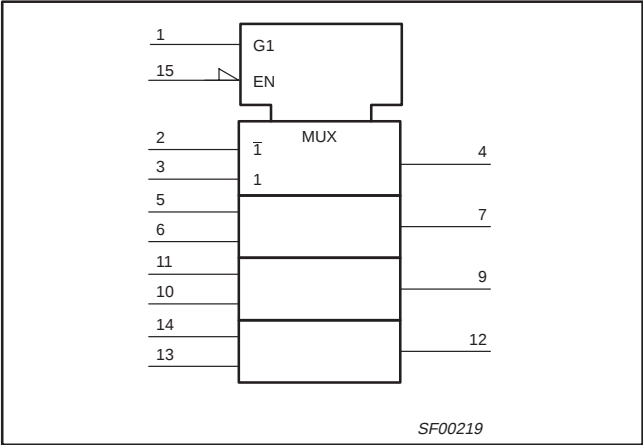
74F158A



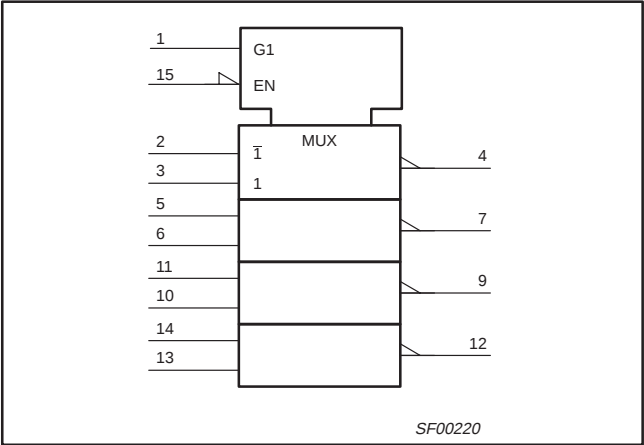
Data selectors/multiplexers

74F157A, 74F158A

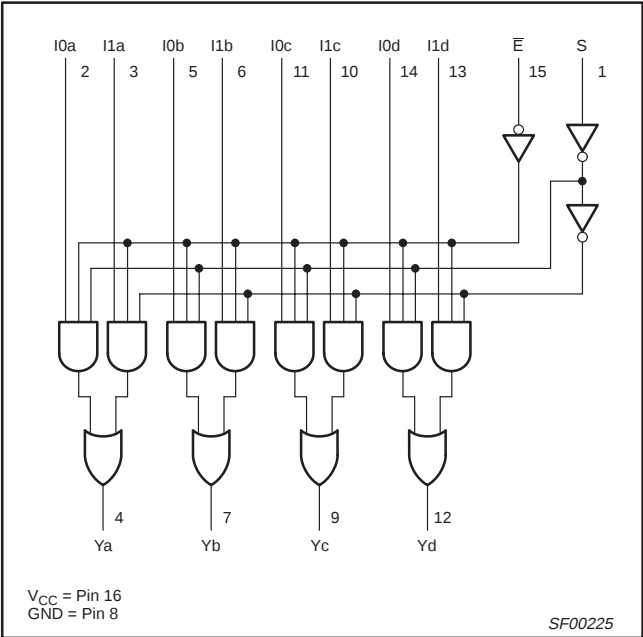
IEEE/IEC SYMBOLS, 74F157A



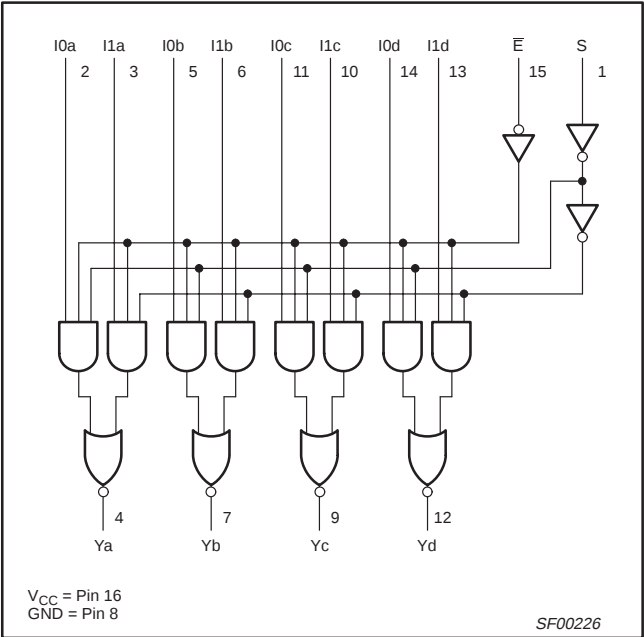
74F158A



LOGIC DIAGRAM, 74F157A



74F158A



FUNCTION TABLE, 74F157A

| INPUTS    |   |     |     | OUTPUT |
|-----------|---|-----|-----|--------|
| $\bar{E}$ | S | I0n | I1n | Yn     |
| H         | X | X   | X   | L      |
| L         | H | X   | L   | L      |
| L         | H | X   | H   | H      |
| L         | L | L   | X   | L      |
| L         | L | H   | X   | H      |

H = High voltage level  
L = Low voltage level  
X = Don't care

74F158A

| INPUTS    |   |     |     | OUTPUT      |
|-----------|---|-----|-----|-------------|
| $\bar{E}$ | S | I0n | I1n | $\bar{Y}_n$ |
| H         | X | X   | X   | H           |
| L         | L | L   | X   | H           |
| L         | L | H   | X   | L           |
| L         | H | X   | L   | H           |
| L         | H | X   | H   | L           |

H = High voltage level  
L = Low voltage level  
X = Don't care

## Data selectors/multiplexers

## 74F157A, 74F158A

**ABSOLUTE MAXIMUM RATINGS**

(Operation beyond the limit set forth in this table may impair the useful life of the device.  
Unless otherwise noted these limits are over the operating free-air temperature range.)

| SYMBOL    | PARAMETER                                      |                               | RATING           | UNIT |
|-----------|------------------------------------------------|-------------------------------|------------------|------|
| $V_{CC}$  | Supply voltage                                 |                               | −0.5 to +7.0     | V    |
| $V_{IN}$  | Input voltage                                  |                               | −0.5 to +7.0     | V    |
| $I_{IN}$  | Input current                                  |                               | −30 to +5        | mA   |
| $V_{OUT}$ | Voltage applied to output in High output state |                               | −0.5 to $V_{CC}$ | V    |
| $I_{OUT}$ | Current applied to output in Low output state  |                               | 40               | mA   |
| $T_{amb}$ | Operating free-air temperature range           | Commercial Range              | 0 to +70         | °C   |
|           |                                                | Industrial Range 74F157A only | −40 to +85       |      |
| $T_{stg}$ | Storage temperature range                      |                               | −65 to +150      | °C   |

**RECOMMENDED OPERATING CONDITIONS**

| SYMBOL    | PARAMETER                            |                               | LIMITS |     |     | UNIT |
|-----------|--------------------------------------|-------------------------------|--------|-----|-----|------|
|           |                                      |                               | MIN    | NOM | MAX |      |
| $V_{CC}$  | Supply voltage                       |                               | 4.5    | 5.0 | 5.5 | V    |
| $V_{IH}$  | High-level input voltage             |                               | 2.0    |     |     | V    |
| $V_{IL}$  | Low-level input voltage              |                               |        |     | 0.8 | V    |
| $I_{IK}$  | Input clamp current                  |                               |        |     | −18 | mA   |
| $I_{OH}$  | High-level output current            |                               |        |     | −1  | mA   |
| $I_{OL}$  | Low-level output current             |                               |        |     | 20  | mA   |
| $T_{amb}$ | Operating free air temperature range | Commercial Range              | 0      |     | 70  | °C   |
|           |                                      | Industrial Range 74F157A only | −40    |     | 85  |      |

**DC ELECTRICAL CHARACTERISTICS**

(Over recommended operating free-air temperature range unless otherwise noted.)

| SYMBOL   | PARAMETER                                 | TEST CONDITIONS <sup>1</sup>               | LIMITS                |                  |      | UNIT |
|----------|-------------------------------------------|--------------------------------------------|-----------------------|------------------|------|------|
|          |                                           |                                            | MIN                   | TYP <sup>2</sup> | MAX  |      |
| $V_{OH}$ | High-level output voltage                 | $V_{CC} = \text{MIN}, V_{IL} = \text{MAX}$ | $\pm 10\%V_{CC}$      | 2.5              |      | V    |
|          |                                           | $V_{IH} = \text{MIN}, I_{OH} = \text{MAX}$ | $\pm 5\%V_{CC}$       | 2.7              | 3.4  |      |
| $V_{OL}$ | Low-level output voltage                  | $V_{CC} = \text{MIN}, V_{IL} = \text{MAX}$ | $\pm 10\%V_{CC}$      |                  | 0.30 | V    |
|          |                                           | $V_{IH} = \text{MIN}, I_{OL} = \text{MAX}$ | $\pm 5\%V_{CC}$       |                  | 0.30 |      |
| $V_{IK}$ | Input clamp voltage                       | $V_{CC} = \text{MIN}, I_I = I_{IK}$        |                       | −0.73            | −1.2 | V    |
| $I_I$    | Input current at maximum input voltage    | $V_{CC} = \text{MAX}, V_I = 7.0\text{V}$   |                       |                  | 100  | μA   |
| $I_{IH}$ | High-level input current                  | $V_{CC} = \text{MAX}, V_I = 2.7\text{V}$   |                       |                  | 20   | μA   |
| $I_{IL}$ | Low-level input current                   | $V_{CC} = \text{MAX}, V_I = 0.5\text{V}$   |                       |                  | −0.6 | mA   |
| $I_{OS}$ | Short-circuit output current <sup>3</sup> | $V_{CC} = \text{MAX}$                      | −60                   |                  | −150 | mA   |
| $I_{CC}$ | Supply current (total) <sup>4</sup>       | 74F157A                                    | $V_{CC} = \text{MAX}$ | 15.0             | 23.0 | mA   |
|          |                                           | 74F158A                                    |                       | 14.0             | 19.0 | mA   |

**NOTES:**

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at  $V_{CC} = 5\text{V}$ ,  $T_{amb} = 25^\circ\text{C}$ .
- Not more than one output should be shorted at a time. For testing  $I_{OS}$ , the use of high-speed test apparatus and/or sample-and-hold techniques are preferable in order to minimize internal heating and more accurately reflect operational values. Otherwise, prolonged shorting of a High output may raise the chip temperature well above normal and thereby cause invalid readings in other parameter tests. In any sequence of parameter tests,  $I_{OS}$  tests should be performed last.
- $I_{CC}$  is measured with 4.5V applied to all inputs and all outputs open.

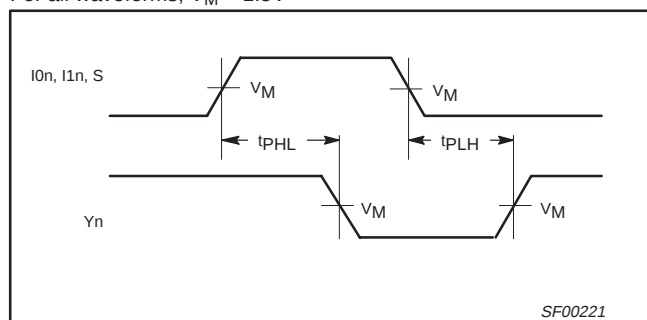
## Data selectors/multiplexers

## 74F157A, 74F158A

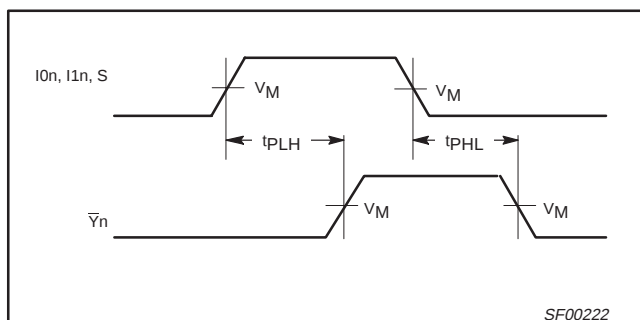
## AC ELECTRICAL CHARACTERISTICS FOR 74F157A AND 74F158A

| SYMBOL                 | PARAMETER                           |         | TEST<br>CONDITION | LIMITS                                                                            |            |             |                                                                                                                   |             |                                                                                                                     |             |    | UNIT |
|------------------------|-------------------------------------|---------|-------------------|-----------------------------------------------------------------------------------|------------|-------------|-------------------------------------------------------------------------------------------------------------------|-------------|---------------------------------------------------------------------------------------------------------------------|-------------|----|------|
|                        |                                     |         |                   | $V_{CC} = +5.0V$<br>$T_{amb} = +25^{\circ}C$<br>$C_L = 50pF$<br>$R_L = 500\Omega$ |            |             | $V_{CC} = +5.0V \pm 10\%$<br>$T_{amb} = 0^{\circ}C \text{ to } +70^{\circ}C$<br>$C_L = 50pF$<br>$R_L = 500\Omega$ |             | $V_{CC} = +5.0V \pm 10\%$<br>$T_{amb} = -40^{\circ}C \text{ to } +85^{\circ}C$<br>$C_L = 50pF$<br>$R_L = 500\Omega$ |             |    |      |
|                        |                                     |         |                   | MIN                                                                               | TYP        | MAX         | MIN                                                                                                               | MAX         | MIN                                                                                                                 | MAX         |    |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>I0n, I1n to Yn | 74F157A | Waveform 1        | 3.5<br>2.5                                                                        | 4.5<br>3.5 | 6.5<br>5.0  | 3.0<br>1.5                                                                                                        | 7.0<br>6.0  | 3.0<br>1.5                                                                                                          | 7.5<br>6.5  | ns |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>E to Yn        |         | Waveform 3        | 6.0<br>4.0                                                                        | 7.5<br>5.0 | 9.0<br>6.5  | 5.5<br>4.0                                                                                                        | 10.5<br>7.0 | 5.5<br>4.0                                                                                                          | 11.0<br>7.5 | ns |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>S to Yn        |         | Waveform 1        | 5.5<br>4.5                                                                        | 7.5<br>6.0 | 10.0<br>7.5 | 5.0<br>4.0                                                                                                        | 11.0<br>8.5 | 5.0<br>4.0                                                                                                          | 11.5<br>9.0 | ns |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>I0n, I1n to Yn | 74F158A | Waveform 2        | 3.0<br>1.5                                                                        | 4.0<br>2.5 | 6.0<br>4.0  | 2.5<br>1.0                                                                                                        | 7.0<br>4.5  | —                                                                                                                   | —           | ns |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>E to Yn        |         | Waveform 4        | 4.5<br>5.0                                                                        | 5.5<br>6.0 | 7.0<br>7.5  | 4.0<br>5.0                                                                                                        | 7.5<br>8.0  | —                                                                                                                   | —           | ns |      |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation delay<br>S to Yn        |         | Waveform 2        | 4.5<br>4.0                                                                        | 6.5<br>5.5 | 8.5<br>7.5  | 4.0<br>3.5                                                                                                        | 9.5<br>8.0  | —                                                                                                                   | —           | ns |      |

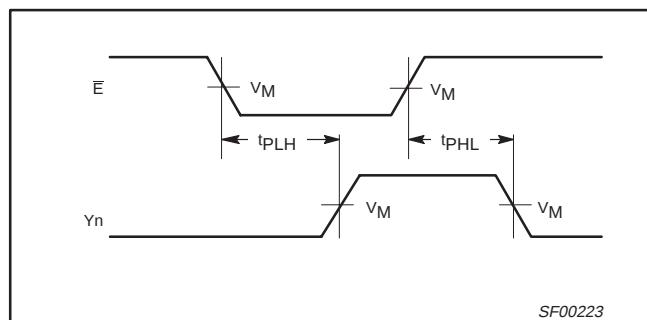
## AC WAVEFORMS

For all waveforms,  $V_M = 1.5V$ 

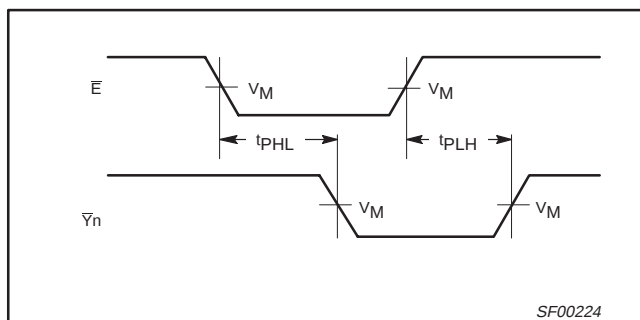
Waveform 1. Propagation Delay for Data and Select to Output



Waveform 2. Propagation Delay for Data and Select to Output



Waveform 3. Propagation Delay for Enable to Output

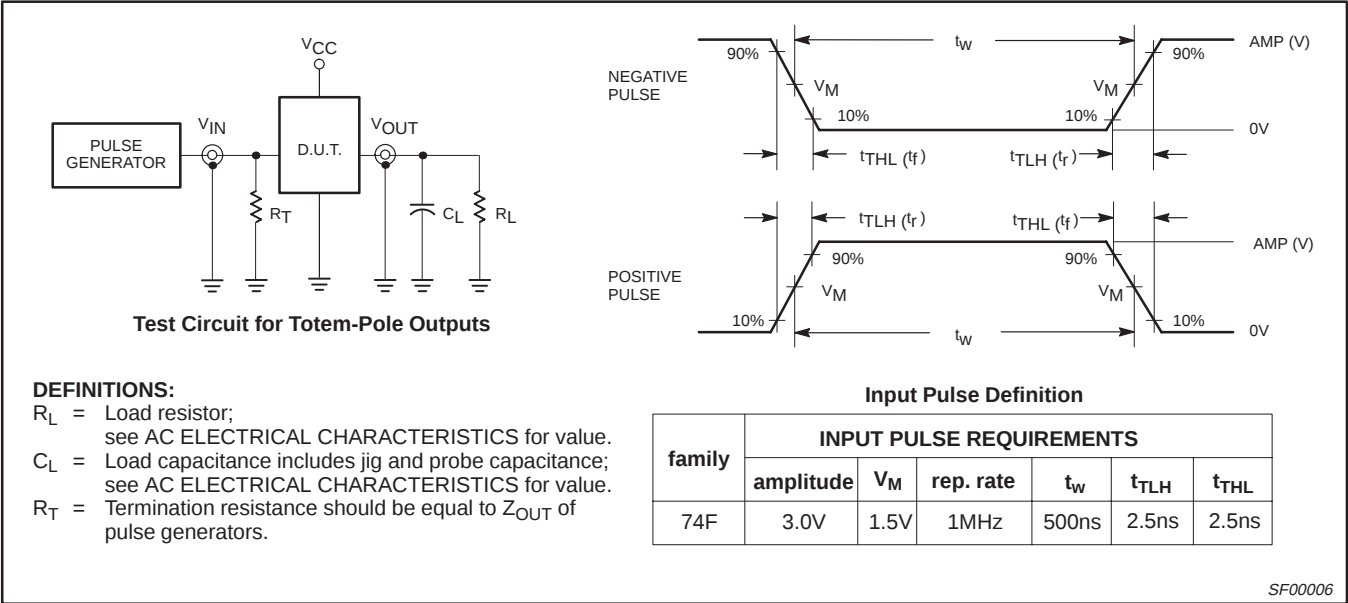


Waveform 4. Propagation Delay for Enable to Output

Data selectors/multiplexers

74F157A, 74F158A

TEST CIRCUIT AND WAVEFORMS

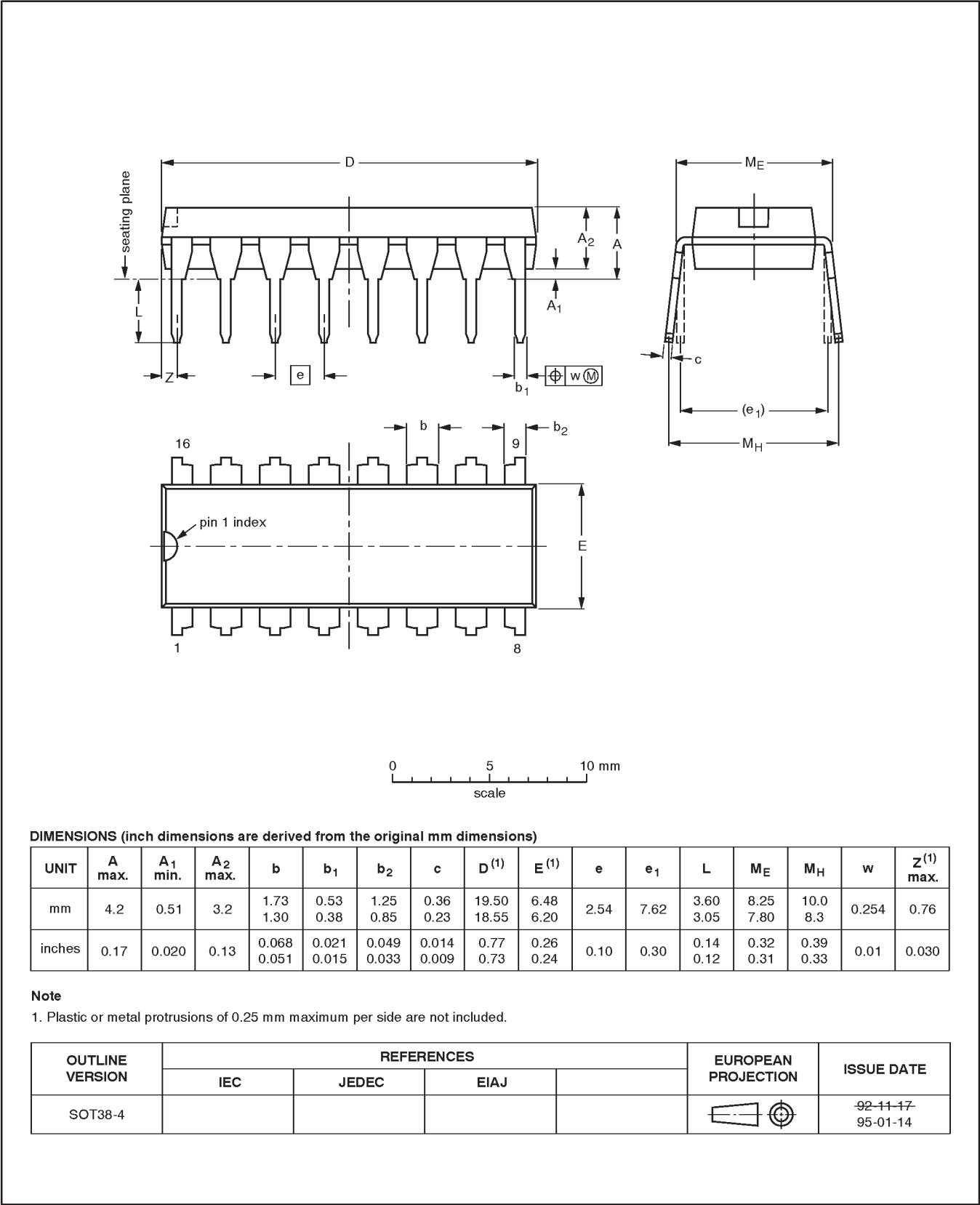


Data selectors/multiplexers

74F157A, 74F158A

DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4

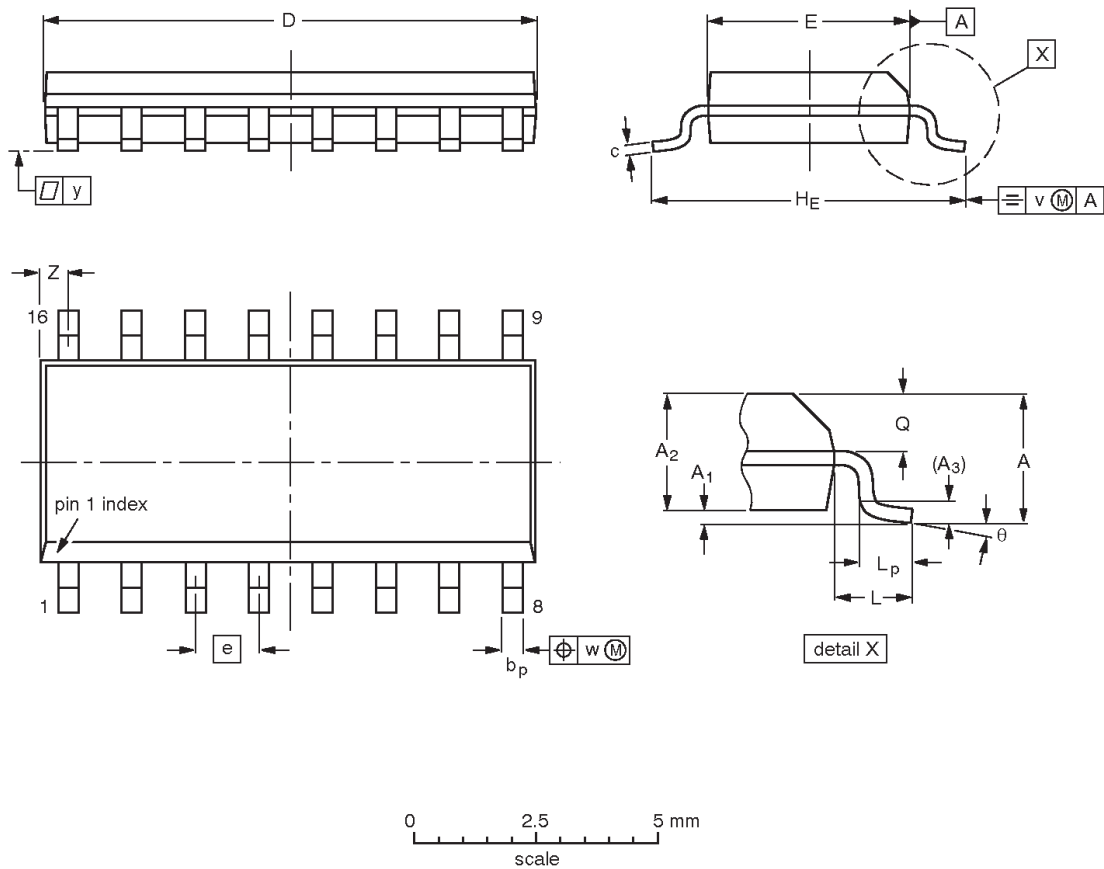


Data selectors/multiplexers

74F157A, 74F158A

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

| UNIT   | A<br>max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c                | D <sup>(1)</sup> | E <sup>(1)</sup> | e     | H <sub>E</sub> | L     | L <sub>p</sub> | Q              | v    | w    | y     | Z <sup>(1)</sup> | θ        |
|--------|-----------|----------------|----------------|----------------|----------------|------------------|------------------|------------------|-------|----------------|-------|----------------|----------------|------|------|-------|------------------|----------|
| mm     | 1.75      | 0.25<br>0.10   | 1.45<br>1.25   | 0.25           | 0.49<br>0.36   | 0.25<br>0.19     | 10.0<br>9.8      | 4.0<br>3.8       | 1.27  | 6.2<br>5.8     | 1.05  | 1.0<br>0.4     | 0.7<br>0.6     | 0.25 | 0.25 | 0.1   | 0.7<br>0.3       | 8°<br>0° |
| inches | 0.069     | 0.010<br>0.004 | 0.057<br>0.049 | 0.01           | 0.019<br>0.014 | 0.0100<br>0.0075 | 0.39<br>0.38     | 0.16<br>0.15     | 0.050 | 0.244<br>0.228 | 0.041 | 0.039<br>0.016 | 0.028<br>0.020 | 0.01 | 0.01 | 0.004 | 0.028<br>0.012   |          |

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

| OUTLINE<br>VERSION | REFERENCES |          |      |  | EUROPEAN<br>PROJECTION | ISSUE DATE           |
|--------------------|------------|----------|------|--|------------------------|----------------------|
|                    | IEC        | JEDEC    | EIAJ |  |                        |                      |
| SOT109-1           | 076E07S    | MS-012AC |      |  |                        | 95-01-23<br>97-05-22 |

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Data selectors/multiplexers

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74F157A, 74F158A

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**NOTES**

## Data selectors/multiplexers

74F157A, 74F158A

## Data sheet status

| Data sheet status         | Product status | Definition [1]                                                                                                                                                                                                                                             |
|---------------------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Objective specification   | Development    | This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.                                                                                                          |
| Preliminary specification | Qualification  | This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product. |
| Product specification     | Production     | This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.                                                       |

[1] Please consult the most recently issued datasheet before initiating or completing a design.

## Definitions

**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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