

AN3969K

Electronic volume IC for HiFi VCR

Overview

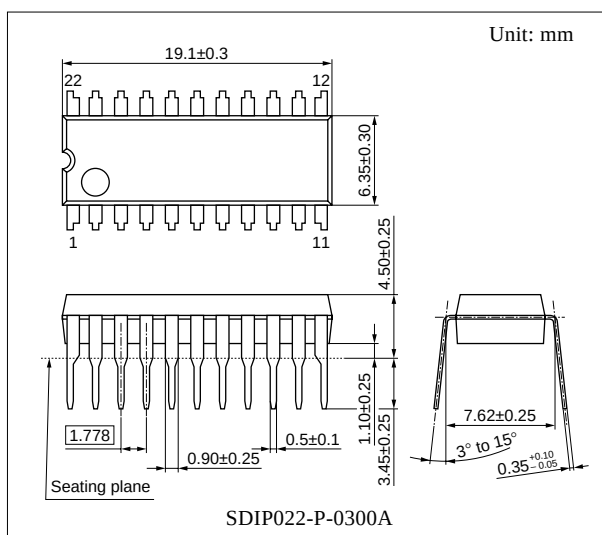
The AN3969K is a stereo electronic volume IC which has been developed for a HiFi video, enabling to switch four inputs of a single channel and to input from microphone.

Features

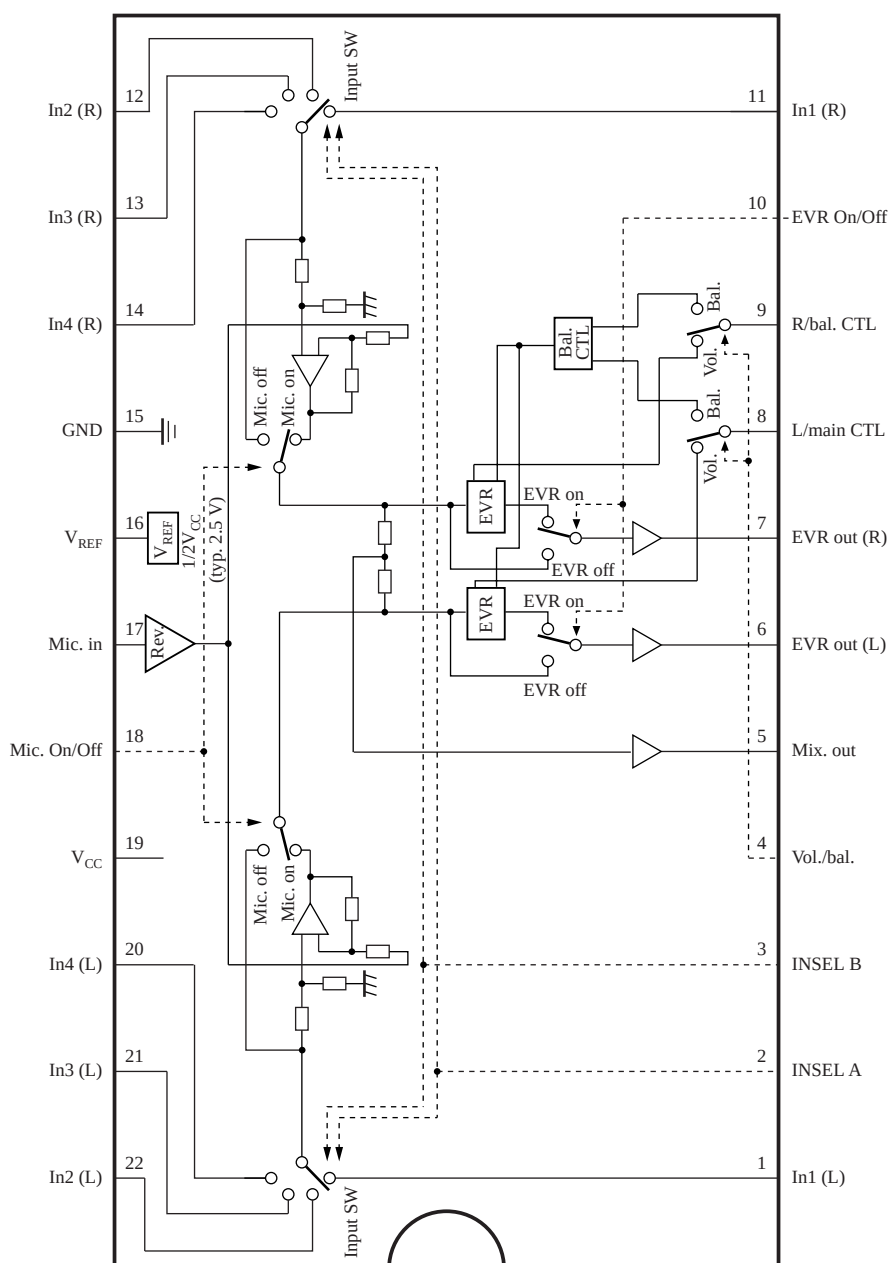
- Both L/R-independent volume control and balance control are available
- Switching four inputs with a single channel is possible
- Electronic volume off function
- Mic. mixing function

Applications

- HiFi VCR



■ Block Diagram



■ Pin Descriptions

Pin No.	Description	Pin No.	Description
1	In1 (L-ch.) input pin	11	In1 (R-ch.) input pin
2	Input selection control (CTL) A pin	12	In2 (R-ch.) input pin
3	Input selection control (CTL) B pin	13	In3 (R-ch.) input pin
4	EVR individual/balance mode CTL pin	14	In4 (R-ch.) input pin
5	Mix. output pin	15	GND pin
6	EVR (L-ch.) output pin	16	V_{REF} (1/2 V_{CC}) pin
7	EVR (R-ch.) output pin	17	Mic. input pin
8	Main volume adjustment pin for individual mode L-ch./balance mode	18	Mic. On/Off CTL pin
9	Balance volume adjustment pin for individual mode R-ch./balance mode	19	V_{CC} (5 V) pin
		20	In4 (L-ch.) input pin
10	EVR On/Off CTL pin	21	In3 (L-ch.) input pin
		22	In2 (L-ch.) input pin

■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V_{CC}	6.0	V
Power dissipation *1, *2	P_D	186	mW
Operating ambient temperature *1	T_{opr}	-20 to +75	°C
Storage temperature *1	T_{stg}	-55 to +150	°C

Note) *1: Except for the power dissipation, operating ambient temperature and storage temperature, all ratings are for $T_a = 25^\circ\text{C}$.

*2: The power dissipation shown is the value for $T_a = 75^\circ\text{C}$.

■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V

■ Electrical Characteristics at $V_{CC} = 5\text{ V}$, pin 8 = 3 V, pin 9 = 3 V, $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Circuit current 1	I_{VOL}	EVR individual CTL mode with no signal	14	18	23	mA
Circuit current 2	I_{BAL}	EVR balance mode with no signal	19	25	31	mA
EVR out level L	VEO_L	Typical input	-23	-20	-17	dBs
EVR out level R	VEO_R	Typical input	-23	-20	-17	dBs
EVR out distortion ratio L	TEO_L	Typical input	—	—	0.2	%
EVR out distortion ratio R	TEO_R	Typical input	—	—	0.2	%

Note) 1. Unless otherwise specified, set a mode to In1 input selection, EVR on, EVR individual CTL, mic. off and input a typical input that is In1 input, $V_{IN} = -20\text{ dBs}$, $f_{IN} = 1\text{ kHz}$ and L/R simultaneous input.

2. Unless otherwise specified, measure EVR out L for L-ch. parameter and EVR out R for R-ch. parameter.

■ Electrical Characteristics at $V_{CC} = 5\text{ V}$, pin 8 = 3 V, pin 9 = 3 V, $T_a = 25^\circ\text{C}$ (continued)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
EVR out max. output L	$VEOM_L$	Output level at pin 8 = 5 V and distortion ratio = 1%	1	—	—	dBs
EVR out max. output R	$VEOM_R$	Output level at pin 9 = 5 V and distortion ratio = 1%	1	—	—	dBs
EVR out S/N L	NEO_L	Ratio to VEO_L to In1, $R_G = 18\text{ k}\Omega$, JIS A and $R_L = 10\text{ k}\Omega$	70	—	—	dB
EVR out S/N R	NEO_R	Ratio to VEO_R to In1, $R_G = 18\text{ k}\Omega$, JIS A and $R_L = 10\text{ k}\Omega$	70	—	—	dB
EVR max. level L	$VMAX_L$	Ratio to VEO_L with typical input and pin 8 = 5 V	8	10	12	dB
EVR max. level R	$VMAX_R$	Ratio to VEO_R with typical input and pin 9 = 5 V	8	10	12	dB
EVR attenuation level L	$VMIN_L$	Ratio to output with $V_{IN} = -10\text{ dBs}$, pin 8 = 0 V and pin 8 = 3 V	—	—	-65	dB
EVR attenuation level R	$VMIN_R$	Ratio to output with $V_{IN} = -10\text{ dBs}$, pin 9 = 0 V and pin 9 = 3 V	—	—	-65	dB
EVR balance characteristics 1-L	$VBAL_{1L}$	EVR balance mode, pin 8 = 5 V, pin 9 = 2.5 V	-12	-10	-8	dBs
EVR balance characteristics 1-R	$VBAL_{1R}$	EVR balance mode, pin 8 = 5 V, pin 9 = 2.5 V	-12	-10	-8	dBs
EVR balance characteristics 2-L	$VBAL_{2L}$	EVR balance mode, pin 8 = 5 V, pin 9 = 1.25 V, ratio to $VBAL_{1L}$	-12	-10	-7	dB
EVR balance characteristics 2-R	$VBAL_{2R}$	EVR balance mode, pin 8 = 5 V, pin 9 = 3.75 V, ratio to $VBAL_{1R}$	-12	-10	-7	dB
EVR balance characteristics 3-L	$VBAL_{3L}$	EVR balance mode, pin 8 = 5 V, pin 9 = 0 V, ratio to $VBAL_{1L}$	—	—	-50	dB
EVR balance characteristics 3-R	$VBAL_{3R}$	EVR balance mode, pin 8 = 5 V, pin 9 = 5 V, ratio to $VBAL_{1R}$	—	—	-50	dB
Mix. out level	$VMIX$	Mix. out measurement in a typical input	-22	-20	-18	dBs
Mix. out distortion ratio	$TMIX$	Mix. out measurement in a typical input	—	—	0.2	%
Mic. mix. level	$VMIC$	Mix. out measurement with mic. on mode, mic in = -32 dBs, 1 kHz	-22	-20	-18	dBs
Mic. mix. muting ratio	$VMUTE$	Mic. off mode, mic. _{IN} = -32 dBs, 1 kHz, mix. out measurement, ratio to $VMIC$	—	—	-70	dB
Crosstalk 1 between channels (R→L)	$CCH1_{RL}$	In1R = -10 dBs, L-ch. measurement, DIN audio, ratio to VEO_L is -10 dB.	—	—	-75	dB
Crosstalk 2 between channels (L→R)	$CCH1_{LR}$	In1L = -10 dBs, R-ch. measurement, DIN audio, ratio to VEO_R is -10 dB.	—	—	-75	dB
Crosstalk between inputs	$CIN1$	In2, In3, In4 = -10 dBs, mix. measurement, DIN audio, ratio to $VMIX$ is -10 dB.	—	—	-60	dB

Note) 1. Unless otherwise specified, set a mode to In1 input selection, EVR on, EVR individual CTL, mic. off and input a typical input that is In1 input, $V_{IN} = -20\text{ dBs}$, $f_{IN} = 1\text{ kHz}$ and L/R simultaneous input.

2. Unless otherwise specified, measure EVR out L for L-ch. parameter and EVR out R for R-ch. parameter.

■ Electrical Characteristics at $V_{CC} = 5\text{ V}$, pin 8 = 3 V, pin 9 = 3 V, $T_a = 25^\circ\text{C}$ (continued)

• Design reference data

Note) The characteristics listed below are theoretical values based on the IC design and are not guaranteed.

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input selection CTL A (low) *	DINA _L	Pin 2 input	0	—	1.5	V
Input selection CTL A (high) *	DINA _H	Pin 2 input	3.5	—	5	V
Input selection CTL B (low) *	DINB _L	Pin 3 input	0	—	1.5	V
Input selection CTL B (high) *	DINB _H	Pin 3 input	3.5	—	5	V
EVR individual CTL mode	DVOLL	Pin 4 input (low)	0	—	1.5	V
EVR balance mode	DBALH	Pin 4 input (high)	3.5	—	5	V
EVR on mode	DEONL	Pin 10 input (low)	0	—	1.5	V
EVR off mode	DEOFFH	Pin 10 input (high)	3.5	—	5	V
Mic. on mode	DMONL	Pin 18 input (low)	0	—	1.5	V
Mic. off mode	DMOFFH	Pin 18 input (high)	3.5	—	5	V
EVR out L/R difference	VEOLR	Typical input, pin 8, pin 9 = 3 V, ratio of EVR out L to R	—	0	—	dB
EVR off level L	VEOFFL	EVR off mode, typical input, EVR out L measurement	—	-20	—	dBs
EVR off level R	VEOFFR	EVR off mode, typical input, EVR out R measurement	—	-20	—	dBs

Note) 1. Typical input = (In1 input, $V_{IN} = -20\text{ dBs}$, $f_{IN} = 1\text{ kHz}$, L/R simultaneous input)

2. *: In1 input selection mode: pin 2 = low, pin 3 = low, In2 input selection mode: pin 2 = high, pin 3 = low

In3 input selection mode: pin 2 = low, pin 3 = high, In4 input selection mode: pin 2 = high, pin 3 = high

■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	Pin voltage (typ) (V)
1		In1(L-ch.) input: Audio signal Typical = -20 dBs (1 kHz)	2.5

■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	Pin voltage (typ) (V)
2 3		Pin 2: input selection CTL A; Pin 3: input selection CTL B	—
4		Vol./bal. CTL	—
5 6 7		Pin 5: mix. input; Pin 6: EVR (L-ch.) output; Pin 7: EVR (R-ch.) output	2.5
8		L-ch. CTL (individual mode) Main CTL (balance mode)	—

■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	Pin voltage (typ) (V)
9		R-ch. CTL (individual mode) Balance CTL (balance mode)	—
10		EVR On/Off CTL	—
11 12 13 14	Refer to pin 1.	Pin 11: In1 (R-ch.) input; Pin 12: In2 (R-ch.) input; Pin 13: In3 (R-ch.) input; Pin 14: In4 (R-ch.) input	Refer to pin 1.
15	—	GND	0
16		V_{REF}	2.5
17		Mic. input	2.5

■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	Pin voltage (typ) (V)
18		Mic. On/Off CTL	—
19	—	V_{CC}	5
20	Refer to pin 1.	Pin 20: In4 (L-ch.) input;	Refer to pin 1.
21		Pin 21: In3 (L-ch.) input;	
22		Pin 22: In2 (L-ch.) input	

Application Circuit Example

