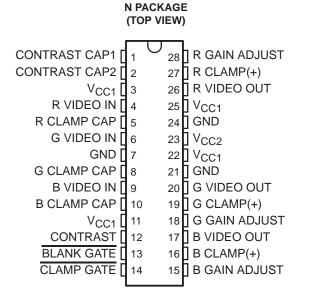
### TLS1215 WIDE-BAND VIDEO PREAMPLIFIER SYSTEM WITH BLANKING

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- Wide Bandwidth . . . Typ 100 MHz at −3 dB
- 0-V to 4-V Digital Level-Contrast Control Voltage Range
- 0-V to 4-V Digital Level-Gain Adjust Control Voltage Range
- Individual Gain Adjust for Video Amplifiers
- Output-Stage Blanking
- Fewer Peripheral Components Required Than for Competitive Systems

### description

The TLS1215 is a wide-band video preamplifier system intended for high-resolution RGB (red-green-blue) color monitors with blanking control features. Each video amplifier (R, G, and B) contains a gain set for adjusting maximum system gain. The TLS1215 provides digital



level-operated contrast, brightness, and gain adjustment. All the control inputs offer high input impedance and an operation range from 0 V to 4 V for easy interface to the serial digital buses. The TLS1215 also contains a blanking circuit, which clamps the video output voltage during blanking period to as low as 0.2 V above ground. The device operates from a 12-V supply. The TLS1215 is characterized for operation from 0°C to 70°C.



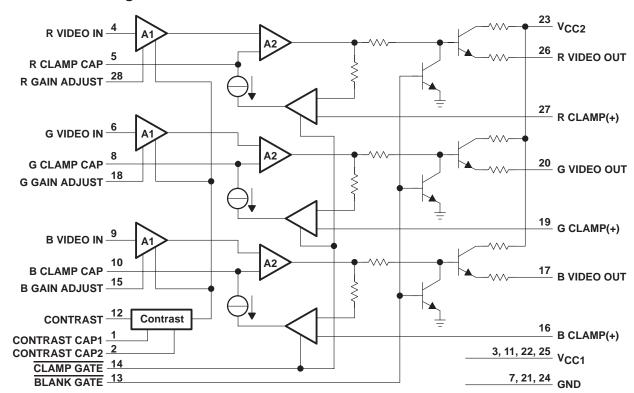
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### functional block diagram



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### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC</sub>	13.5 V
Input voltage range, V <sub>I</sub> (see Note 1)	0 V to V <sub>CC</sub>
Video output current (per channel)	28 mA
Total power dissipation at (or below) 25°C free-air temperature (see Note 2)	2.1 W
Operating virtual junction temperature, T <sub>J</sub>	150°C
Operating free-air temperature range, T <sub>A</sub>	0°C to 70°C
Storage temperature range, T <sub>stq</sub>	65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All V<sub>CC</sub> pins must be externally wired together to prevent internal damage during V<sub>CC</sub> power-on/off cycles.

### recommended operating conditions

		MIN	NOM	MAX	UNIT
Supply voltage, V <sub>CC1</sub> and V <sub>CC2</sub>			12	13.2	V
High-level input voltage, CLAMP GATE, VIH	Clamp comparators off	2.4		5	V
Low-level input voltage, CLAMP GATE, V <sub>IL</sub>	Clamp comparators on	0		0.8	V
High-level input voltage, BLANK GATE, VIH	Blanking circuit inactive	2.4		5	V
Low-level input voltage, BLANK GATE, VIL	Blanking circuit active	0		0.8	V
Operating free-air temperature, T <sub>A</sub>				70	°C

# electrical characteristics at 25°C operating free-air temperature range, $\overline{\text{CLAMP GATE}} = 0 \text{ V}$ , $\overline{\text{BLANK GATE}} = 4 \text{ V}$ , $\overline{\text{CLAMP(+)}} = 2 \text{ V}$ , $\overline{\text{CONTRAST}} = R$ , G, B GAIN ADJUST = 4 V, $\overline{\text{VCC1}} = \overline{\text{VCC2}} = 12 \text{ V}$ (see Figure 2) (unless otherwise noted)

	PARAMETER	ALT SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Icc	Supply current		VCC1 + VCC2	78	90	100	mA
V <sub>ref</sub>	Video input reference voltage		Measure R, G, B VIDEO IN	2.1	2.3	2.6	V
Ц	Contrast, R, G, B gain adjust input current	lį	Measure CONTRAST and B, G, R GAIN ADJUST		-1	-2.5	μΑ
I <sub>IL</sub>	Clamp gate low input current		CLAMP GATE = 0 V		-1	-2.5	μΑ
liH	Clamp gate high input current		CLAMP GATE = 5 V		0.03	1	μΑ
	Clamp capacitor charge current	IK(chg)	R, G, B CLAMP CAP = 0 V		-850		μΑ
	Clamp capacitor discharge current	lK(dschg)	R, G, B CLAMP CAP = 5 V		+850		μΑ
VOL	Low-level output voltage		R, G, B CLAMP CAP = 0 V		0.3		V
VOH	High-level output voltage		R, G, B CLAMP CAP = 5 V		7.8		V
VO(blanked)	Blanked output voltage		Blanking circuit active		0.2		V
V <sub>O(diff)</sub>	Output voltage difference	VOdiff	Between any two channels		±0.5	±50	mV

<sup>2.</sup> For operation above 25°C free-air temperature, derate linearly to 1.5 W at the rate of 13 mW/°C.

## TLS1215 WIDE-BAND VIDEO PREAMPLIFIER SYSTEM WITH BLANKING

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## operating characteristics at 25°C free-air temperature range, $\overline{\text{CLAMP GATE}}$ = 0 V, BLANK GATE = 4 V, CLAMP(+) = 4 V, CONTRAST = R, G, B GAIN ADJUST = 4 V, f<sub>I</sub> = 10 kHz (unless otherwise noted)

	PARAMETER	ALT SYMBOL	TEST CONDITIONS	MIN TYP MAX	UNIT
A <sub>V(max)</sub>	Maximum voltage amplification	AVMAX	CONTRAST = 4 V, V <sub>I(PP)</sub> = 700 mV	7.8	V/V
AV(mid)	Mid-range voltage amplification	A <sub>VMID</sub>	CONTRAST = 2 V, V <sub>I</sub> (PP) = 700 mV	2	V/V
	Contrast voltage for mini- mum amplification	VCONTRASTLOW	V <sub>I(PP)</sub> = 1 V, See Note 3	1	V
	Amplification match at AV(max)	AVMAX(DIFF)	CONTRAST = 4 V, See Note 4	±0.2	dB
	Amplification match at AV(mid)	AVMID(DIFF)	CONTRAST = 2 V, See Note 3	±0.2	dB
	Amplification match at AV(low)	AVLOW(DIFF)	CONTRAST = V <sub>CONTRASTLOW</sub> , See Notes 3 and 4	±0.2	dB
THD	Total harmonic distortion	THD	CONTRAST = 1 V, V <sub>I(PP)</sub> = 1 V	1.0%	
BW	Amplifier bandwidth	BW(-3 dB)	CONTRAST = 4 V, See Notes 5 and 7	100	MHz
Crosstalk attenuation		CONTRAST = 4 V, f = 10 kHz, See Note 6	80	dB	
	a <sub>X</sub>	CONTRAST = 4 V, f = 10 MHz, See Notes 6 or 7	40	dB	
	Tr, video Tf, video	V <sub>O</sub> (PP) = 4 V, CONTRAST = 4 V, Clamp(+) = 2 V, See Notes 5 and 7	4		
Pulse test		Tr, blank Tf, blank	CONTRAST = 4 V , Clamp(+) = 2 V, See Notes 5 and 7	7	ns

NOTES: 3. Determine  $V_{\mbox{CONTRASTLOW}}$  for  $-40~\mbox{dB}$  attenuation of output. Reference to  $A_{\mbox{V}}$  maximum.

- 4. Measure gain difference between any two amplifiers,  $V_{I(PP)} = 1 \text{ V}$ .
- 5. Adjust input frequency from 10 kHz (A<sub>V</sub> maximum ref level) to the -3 dB corner frequency (f-3 dB).  $V_{I(PP)} = 700$  mV.
- 6.  $V_{I(PP)} = 700 \text{ mV}$  at f = 10 kHz to any amplifier. Measure output levels of the other two undriven amplifiers relative to driven amplifier.
- 7. A special text fixture without a socket and a double-sided full-ground-plane PC board are required.

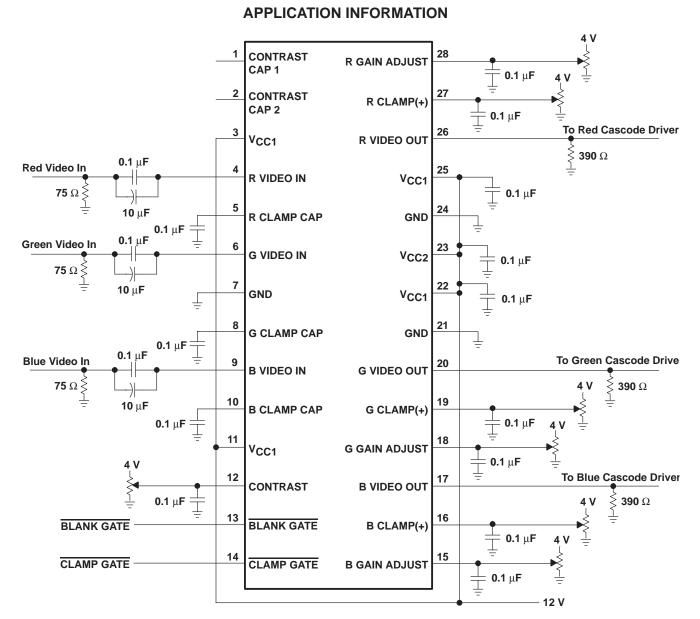
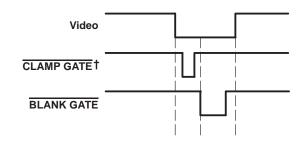


Figure 1. Test/Application Circuit



† Minimum CLAMP GATE pulse: 300 ns

Figure 2. Test/Application Circuit Timing Diagram

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