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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **HA17431G Series**

# Adjustable Precision Shunt Regulators

REJ03D0805-0100 Rev.1.00 Mar 10, 2006

### **Description**

The HA17431G series is temperature-compensated adjustable precision shunt regulators. The products have improved features such as wide operating cathode voltage range and precision than the previous products.

Output voltage can be set to any value in the range from the reference voltage (Vref) to 40 V by two external resistors. There are two types of reference voltage accuracy sources such as  $\pm 1.0\%$  standard version and  $\pm 0.5\%$  A version with higher precision. As for the packages, small surface-mounted types such as MPAK, MPAK-5, and UPAK are available. Therefore, the HA17431G series is suitable for various applications that require high precision and miniaturization.

#### **Features**

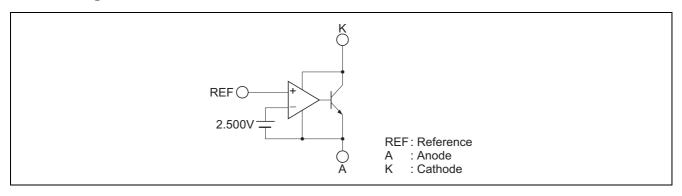
• High-precision reference voltage :  $2.500 \text{ V} \pm 1.0\%$  (Ta =  $25^{\circ}$ C, Standard version)

 $: 2.500 \text{ V} \pm 0.5\% \text{ (Ta} = 25^{\circ}\text{C, A version)}$ 

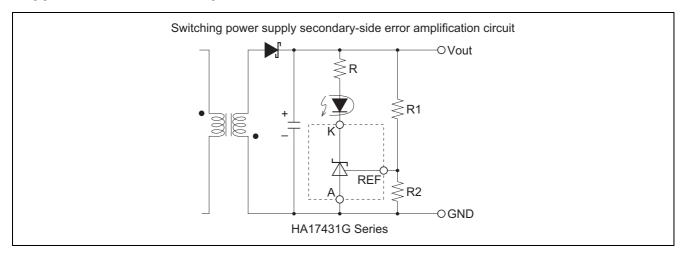
Maximum cathode voltage : 40 V
 Continuous cathode current : 100 mA

K-REF pin reversing type : HA17432G (UPAK)
 Operating temperature range : -40°C to +85°C

# **Block Diagram**



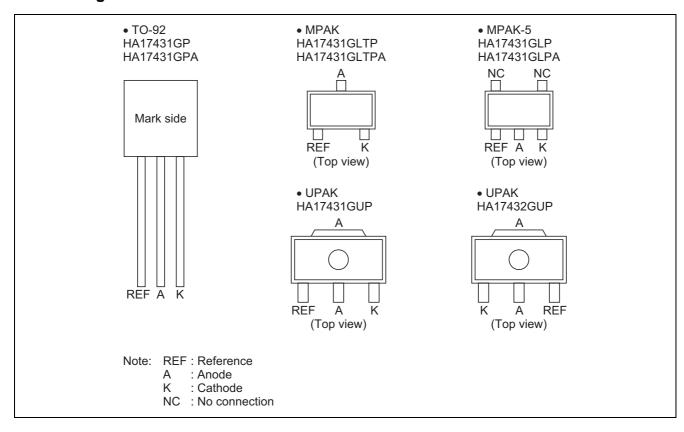
# **Application Circuit Example**



# **Ordering Information**

		Reference Volta	age (Ta = 25°C)		Operating
		Standard Version	A Version	Package Name	Temperature
Application	Type No.	2.500V ± 1.0%	$2.500V \pm 0.5\%$	(Package Code)	Range
Industrial	HA17431GP	0		TO-92	-40°C to +85°C
use	HA17431GPA		0	(PRSS0003DA-A)	
	HA17431GLTP	0		MPAK	
	HA17431GLTPA		0	(PLSP0003ZB-A)	
	HA17431GLP	0		MPAK-5	
	HA17431GLPA		0	(PLSP0005ZB-A)	
	HA17431GUP	0		UPAK	
	HA17432GUP	0		(PLZZ0004CA-A)	
	(K-REF pin reversing type)				

# **Pin Arrangement**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item		Symbol	Symbol Ratings		Notes	
Cathode voltage		V <sub>KA</sub>	40	V	1	
Continuous cathode	current	I <sub>K</sub>	-50 to +100	mA		
Reference input curre	ent	Iref	-0.05 to +10	mA		
Power dissipation TO-92		P <sub>T</sub>	500	mW	2	
	MPAK		150		3	
	MPAK-5		150		3	
	UPAK		800		4	
Operating temperature range		Topr	-40 to +85	°C		
Storage temperature		Tstg	-55 to +150	°C		

Notes: 1. Voltage values are with reference to the Anode pin.

- 2. Ta  $\leq$  25°C. If Ta > 25°C, derate by -4 mW/°C.
- 3. Ta  $\leq$  25°C. If Ta > 25°C, derate by -1.2 mW/°C.
- 4. 15 mm  $\times$  25 mm  $\times$  0.7mmt alumina ceramic board, Ta  $\leq$  25°C. If Ta > 25°C, derate by -6.4 mW/°C.

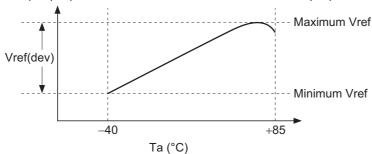
## **Electrical Characteristics**

(Ta = 25°C,  $I_K = 10$  mA, unless otherwise noted)

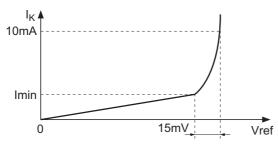
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	Notes
Reference voltage	Vref	2.487	2.500	2.513	V	V <sub>KA</sub> = Vref	Α
		2.475	2.500	2.525			Standard
Reference voltage	Vref(dev)	_	(14)	_	mV	V <sub>KA</sub> = Vref,	1, 2
temperature deviation						Ta = $-40$ °C to $+85$ °C	
Reference voltage	ΔVref/ΔTa	_	(±30)	_	ppm/°C	V <sub>KA</sub> = Vref,	1
temperature coefficient						0°C to 50°C gradient	
Reference voltage regulation	$\Delta Vref/\Delta V_{KA}$	_	2.0	3.7	mV/V	V <sub>KA</sub> = Vref to 10 V	
		_	2.0	3.7		V <sub>KA</sub> = 10 V to 40 V	
Reference input current	Iref	_	2	6	μΑ	R1 = 10 k $\Omega$ , R <sub>2</sub> = $\infty$	
Reference current	Iref(dev)	_	(0.9)	_	μΑ	$R1 = 10 \text{ k}\Omega, R_2 = \infty,$	1
temperature deviation						Ta = -40°C to +85°C	
Minimum cathode current	Imin	_	0.4	1.0	mA	V <sub>KA</sub> = Vref	3
Off state cathode current	loff	_	0.001	1.0	μΑ	V <sub>KA</sub> = 40 V, Vref = 0 V	
Dynamic impedance	Z <sub>KA</sub>	_	0.2	0.5	Ω	V <sub>KA</sub> = Vref,	
						$I_K = 1 \text{ mA to } 100 \text{ mA}$	

Notes: 1. Reference values for design.

2. Vref(dev) = (Vref maximum value at Ta = -40°C to +85°C) - (Vref minimum value at Ta = -40°C to +85°C)

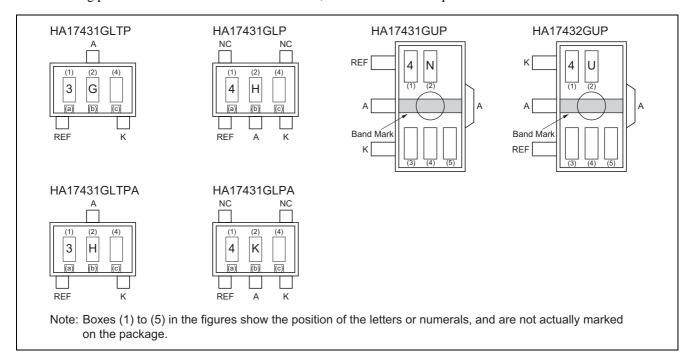


3. Definition of minimum cathode current. Imin is the cathode current value at which  $Vref = Vref_{(IK=10mA)} - 15 \text{ mV}$ .



# **Marking Patterns**

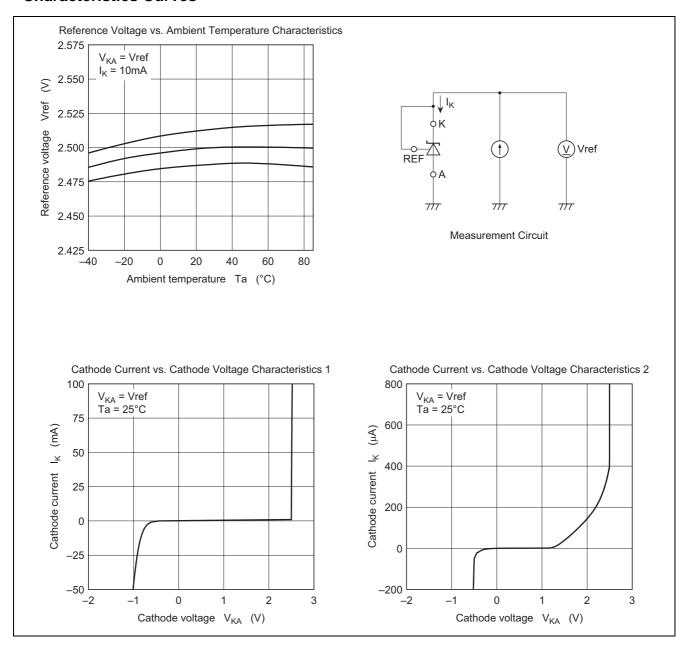
The marking patterns shown below are used on MPAK, MPAK-5 and UPAK products.

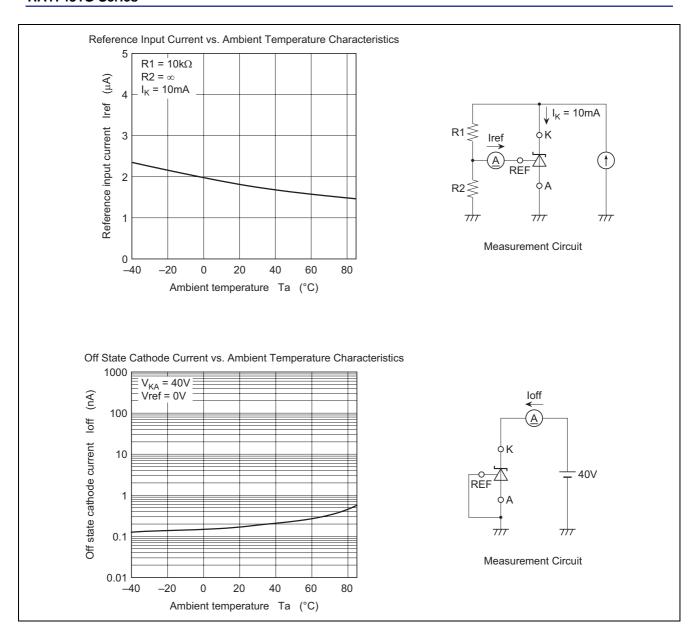


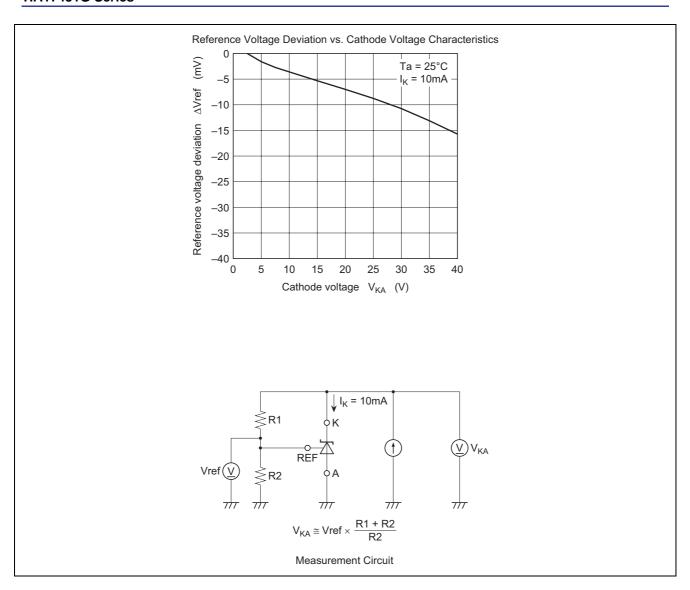
## **Markings**

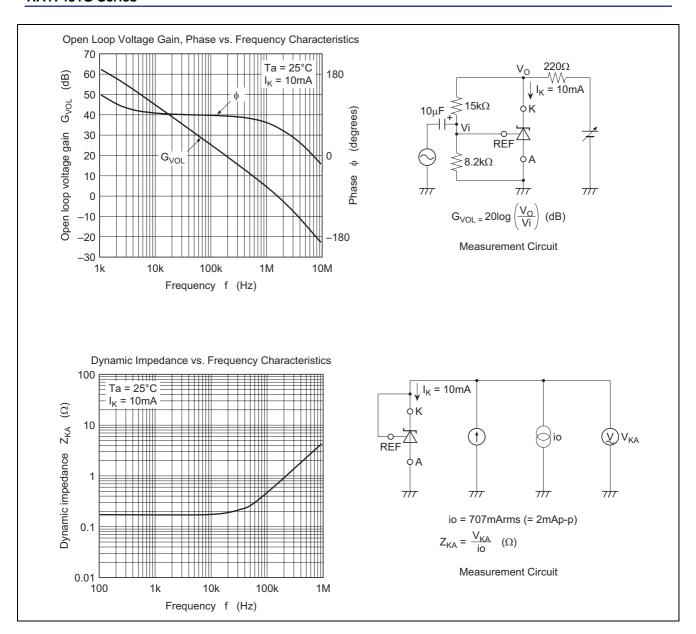
Position	Type of Marking	Meaning												
(1), (2)	Characters	Type No. co	de	HA1	74310	SLTP:	3G		Н	A1743	1GLT	PA: 3H		
				HA1	74310	SLP: 4	Н		H	A1743	1GLP	4: 4K		
				HA1	74310	SUP: 4	-N							
				HA1	74320	SUP: 4	·U							
(3)		Production :	ear c	ode (T	he las	t digit	of the	yea	r)					
		Notes: 1.	For UF	PAK pı	oduct	s (HA′	17431	GUF	P, HA	17432	GUP)			
(a), (b), (c)	Bar mark	Production	ear c	ode										
		Production Year	2006	2007	200	3 200	9 20	10 2	2011	2012	2013			
		(a)	Bar	Bar	Bar	Non	e No	ne N	lone	None	Bar			
		(b)	None	Bar	Bar	Non	e No	ne	Bar	Bar	None			
		(c)	Bar	None	Bar	Non	е Ва	ar N	lone	Bar	None			
		Notes: 2. Repeated every 8 years from 2014 on.												
		3.	For MI	PAK p	roduct	s (HA	17431	GLT	P, H/	\1743 <sup>-</sup>	1GLTF	PA)		
			For MI	PAK-5	produ	icts (H	A174	31GI	LP, H	A1743	31GLP	۹)		
(4)	Characters	Production i	month	code										
		Production Month	Jan.	Feb.	Mar.	Apr.	May	Jun	ı. Ju	I. Aug	g. Sep	o. Oct.	Nov.	Dec.
		Code	Α	В	С	D	Е	F	G	Н	J	K	L	М
(5)		Management code												
		Notes: 4.	For UF	PAK pı	oduct	s (HA	7431	GUF	P, HA	17432	GUP)			

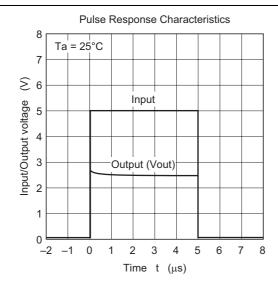
## **Characteristics Curves**

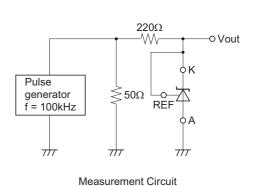




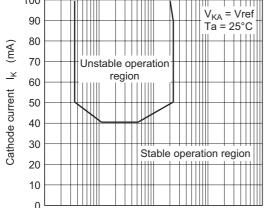






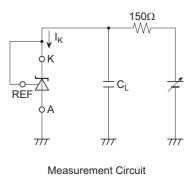


Stable Operation Region 100 90



0.1

Load capcitance  $C_L$  ( $\mu F$ )



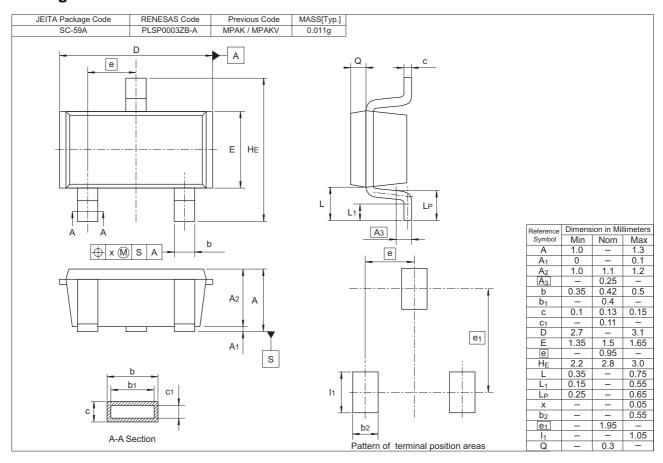
Note: In the unstable operation region, there is a possibility that the device oscillates. Please change to the setting with an enough margin in consideration of the difference when you use it.

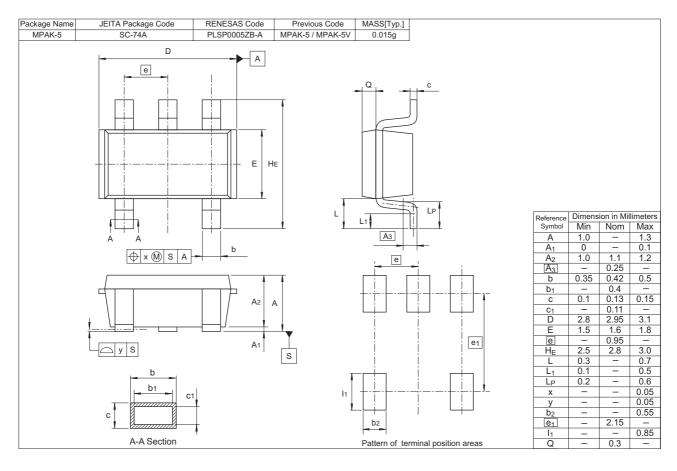
10

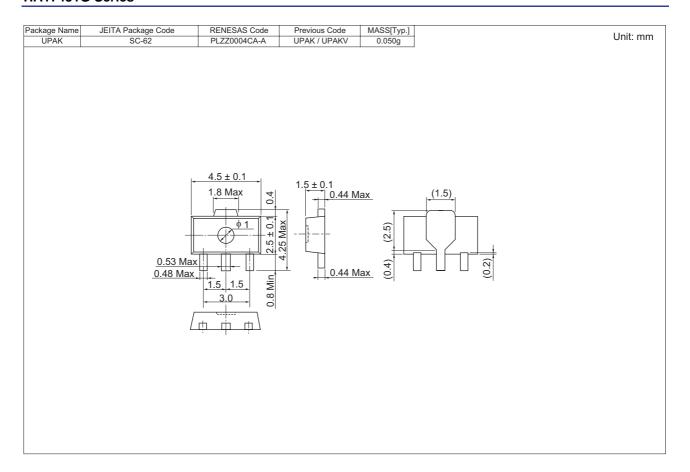
0.001

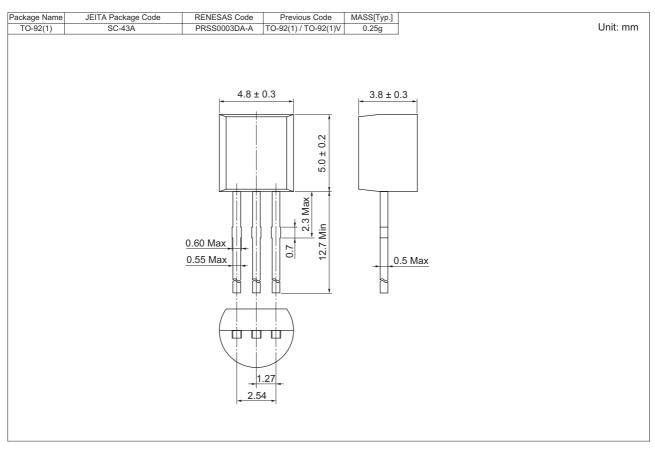
0.01

## **Package Dimensions**









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