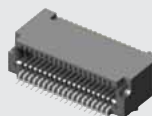
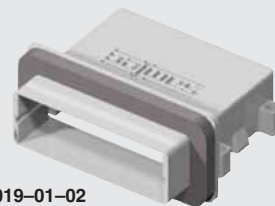


(0.80 mm) .0315"

ERI8-019-S-D-RA



ERC-019-01-02



ERI8, ERC SERIES

# HIGH SPEED I/O SYSTEM

ERI8 Mates with:  
EPLSP

## SPECIFICATIONS

For complete specifications and recommended PCB layouts see [www.samtec.com?ERI8](http://www.samtec.com?ERI8)

**Insulator Material:**

Black LCP

**Terminal Material:**

BeCu

**Plating:**

Au or Sn over

50μ" (1.27 μm) Ni

**Current Rating:**

1.7 A per pin

(1 pin powered per row)

**Operating Temp Range:**

-55°C to +125°C

**Voltage Rating:**

Testing Now!

**RoHS Compliant:** Yes

**Lead-Free Solderable:** Yes

**SMT Lead Coplanarity:**

(0.10 mm) .004" max



ERI8

NO. OF  
POSITIONSPLATING  
OPTION

D

RA

OTHER  
OPTION

-019

= 8 Pairs, 5 Power,  
2 Clock, 19 positions

-031

= 16 Pairs, 5 Power,  
2 Clock, 31 positions

-S

= 30μ" (0.76 μm) Gold on contact,  
Matte Tin on tail

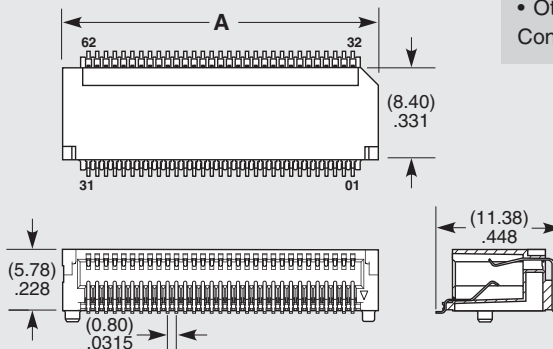
-TR

= Tape & Reel  
Packaging

**ALSO AVAILABLE**  
(MOQ Required)

- Other platings  
Contact Samtec.

| NO. OF<br>POSITIONS | A               |
|---------------------|-----------------|
| -019                | (19.08)<br>.751 |
| -031                | (28.68)<br>1.13 |



### Note:

Active equalization  
reference design for  
Eye Speed® I/O system  
see [www.samtec.com?EPLSP](http://www.samtec.com?EPLSP)

**Note:** Some lengths, styles  
and options are non-standard,  
non-returnable.

## SPECIFICATIONS

For complete specifications and recommended PCB layouts see [www.samtec.com?ERC](http://www.samtec.com?ERC)

TYPE

NO. OF  
POSITIONS

01

OPTION

ERC  
= Cage-019, -031  
= Number of Positions-01  
= Without Gasket-02  
= With Gasket

| NO. OF<br>POSITIONS | A                |
|---------------------|------------------|
| -019                | (30.55)<br>1.203 |
| -031                | (40.15)<br>1.58  |

