

## EPD2000G

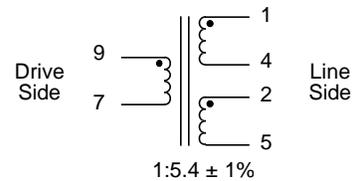
- For use with Globespan HDSL chipset •
- Designed to meet supplementary Insulation Requirements of IEC 950 for 250 VAC Working Voltage
  - 2500 Vrms Isolation •
- Operating temperature range -40°C to +85°C •

### Electrical Parameters @ 25° C

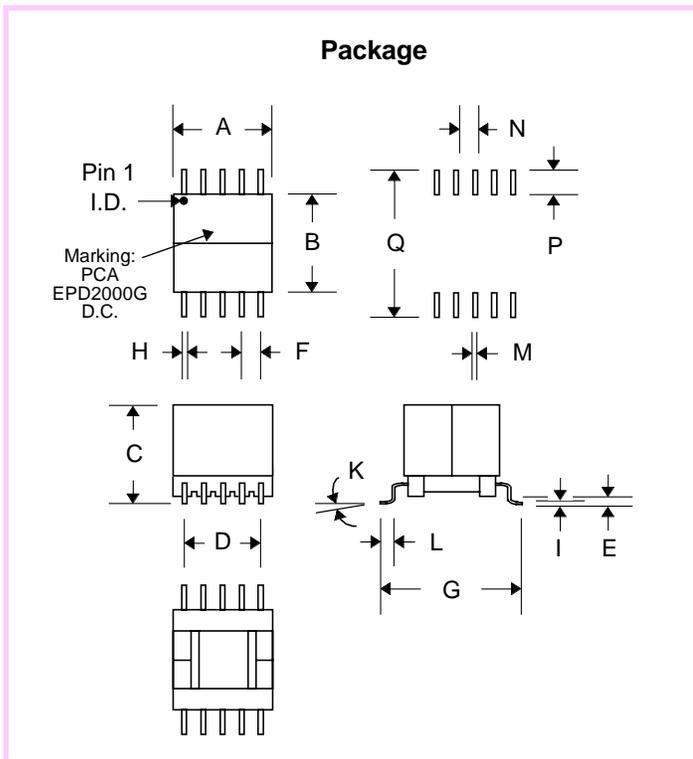
OCL (mH ± 6%)	Leakage Inductance (μH Max.)	Interwinding Capacitance (pF Max.)	Insertion Loss (dB Max.)	Frequency Response (± dB Max.)	THD (dB Max.)	Longitudinal Balance (dB Min.)	DCR (Ω ± 10%)	
							Drive Side	Line Side
@ 10 KHz 0.1 Vrms	@ 100 KHz 0.1 Vrms	@ 100 KHz, 0.1 Vrms	@100 KHz	17-600 KHz (Ref. to 100 KHz)	@ 40 KHz, 12 V p-p	20 Hz- 250 KHz		
3.0	12	50	-0.5	0.5	-90	-60	0.21	2.2
Pins 1-5 with 4,2 Shorted	Pins 1-5 with 4,2 & 9-7 Shorted	Pins 9-7 to 1-5 w/ 2, 4 Shorted					Pins 9-7	Pins 1-5w/ 4,2 shorted

• Line Side Impedance : 100 Ω •

### Schematic



### Package



### Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.490	.530	.512	12.45	13.46	13.00
B	.506	.516	.510	12.85	13.11	12.95
C	.460	.480	.470	11.68	12.19	11.94
D	.389	.399	.394	9.88	10.13	10.00
E	.005	.015	.010	.127	.381	.254
F	.093	.103	.098	2.36	2.62	2.49
G	.665	.675	.670	16.89	17.15	17.02
H	.026	.030	.028	.660	.762	.711
I	.010	.014	.012	.254	.356	.305
K	0°	8°	---	0°	8°	---
L	.029	.049	.039	.737	1.25	.991
M	---	---	.051	---	---	1.29
N	---	---	.098	---	---	.249
P	---	---	.100	---	---	2.54
Q	---	---	.700	---	---	17.78