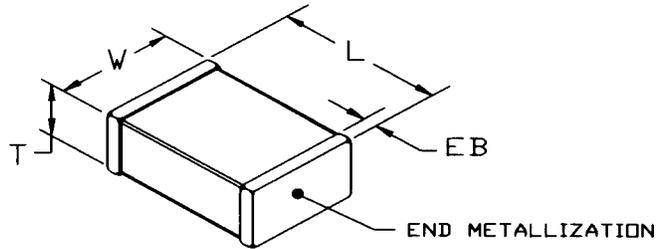


ROHS COMPLIANT



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	NPO
RATED VOLTAGE	50 VDC
CAPACITANCE RANGE (EIA)	8200 Pf (822)
CAPACITANCE TOLERANCE	J ($\pm 5\%$)
TEST PARAMETERS	1 MHz ± 50 Hz @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	+15% TO -25% @ 50 VDC (-55°C TO +125°C)
DISSIPATION FACTOR	< 2.5% @ 1kHz, 25°C
INSULATION RESISTANCE	> 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	> 2.5 x WVDC, 50 mA max

MECHANICAL CHARACTERISTICS

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.050 \pm .010	.040 \pm .010	.040 MAX	.010 \pm .005

TERMINATION DESCRIPTION:

TERMINATION: P - PALLADIUM SILVER

MARKING DESCRIPTION:

MARK: 4 - NO MARKING

PACKAGING DESCRIPTION:

PACKAGING: T - PUNCHED PAPER TAPE AND REEL

						15191 BLEDSOE ST SYLMAR, CA 91342	
				TITLE			
				MULTILAYER CHIP CAPACITOR			
REV	DESCRIPTION	APPROVED	DATE	PART NO.			DWG SCALE
				500R11B822JP4T			NONE
DRN BY	DATE	CHKD BY	DATE	DWG. NO.			SHEET
C. ENGLAND	10/08/08			500R11B822JP4T			RE V 1 OF 1