

AN7700/AN7700F Series

3-pin Low Power Loss Voltage Regulator (1.2A Type)

Overview

The AN7700/AN7700F series is a stabilized constant voltage power supply with a low input/output voltage (typ. 0.5V). It is suitable for the low-voltage equipment using batteries, and consumer/industrial equipments with great fluctuation of the supply voltage.

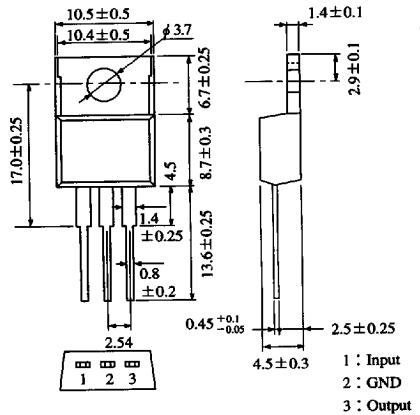
A wide range of output voltage is available from 3V through 10V, 12V, 15V, 18V, 20V, and 24V.

Features

- Minimum input/output voltage difference : 0.5V (typ)
- Built-in overcurrent limiting circuit (ASO protective circuit)
- Built-in overheat protective circuit
- Built-in rush current preventive circuit at saturation voltage rise time
- Built-in input short-circuit protective circuit

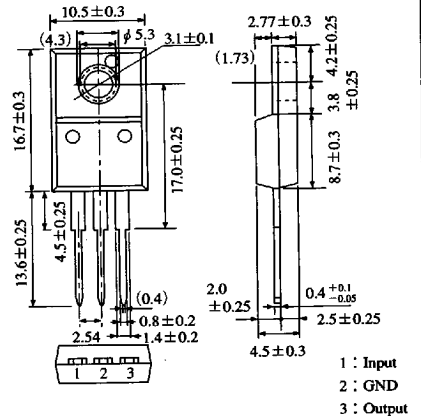
AN7700 Series

Unit : mm



AN7700F Series

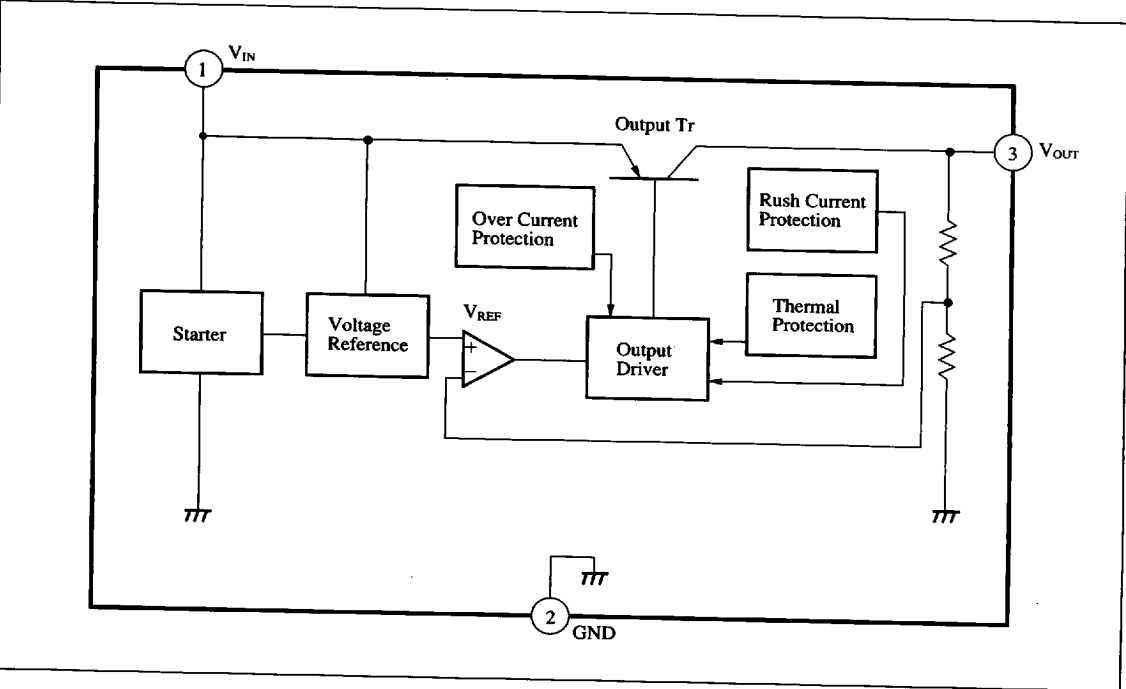
Unit : mm



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■ Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Rating	Unit
Supply voltage		VI	30	V
Supply current		II	2.4 *1	A
Power dissipation	AN7700 Series	Pd	15	W
	AN7700F Series		10.25	
Operating ambient temperature		Topr	-30 to +85	°C
Storage temperature		Tsg	-55 to +150	°C

*1 No current larger than this one flows because of the protective circuit inside the IC.

■ Recommended Operating Conditions (Ta=25°C, Io=500mA)

Part No.	Output voltage (VI)	Operating supply voltage range (VI)	Unit
AN7703/F	3	4 to 14	V
AN7704/F	4	5 to 15	V
AN7705/F	5	6 to 16	V
AN7706/F	6	7 to 17	V
AN7707/F	7	8 to 18	V
AN7708/F	8	9 to 19	V
AN7709/F	9	10 to 20	V
AN7710/F	10	11 to 21	V
AN7712/F	12	13 to 23	V
AN7715/F	15	16.5 to 26.5	V
AN7718/F	18	19.5 to 29.5	V
AN7720/F	20	21.5 to 29.5	V
AN7724/F	24	25.5 to 29.5	V

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■ Electrical Characteristics (Ta=25°C)

• AN7703/F (3V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=4V, I_{out}=500mA$	2.91	3	3.09	V
Input stability	REG_{IN}	$V_{in}=4 \text{ to } 14V, I_{out}=500mA$	—	3	30	mV
Load stability	REG_{LOA}	$V_{in}=4V, I_{out}=0 \text{ to } 1200mA$	—	15	60	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=4 \text{ to } 14V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=4V, I_{out}=0 \text{ to } 1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=4V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=2.7V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=3.5V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=3.5V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=4V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=13V$	1	1.5	2	A
Peak output current (3) Note 1)	$I_{O(peak)3}$	$V_{in}=18V$	0.5	1	1.5	A
Ripple rejection ratio	RR	$V_{in}=4 \text{ to } 6V, I_{out}=100mA, f=120Hz$	54	74	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_J(TH)$	$V_{in}=4V$	—	150	—	°C
Output voltage temperature coefficient Note 2)	α	$V_{in}=4V, T_J=25 \text{ to } 125^\circ C$	—	-40	—	ppm/°C

Note 1) Since it is the parameter of abnormal operation (over-current), it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7704/F (4V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=5V, I_{out}=500mA$	3.88	4	4.12	V
Input stability	REG_{IN}	$V_{in}=5 \text{ to } 15V, I_{out}=500mA$	—	4	40	mV
Load stability	REG_{LOA}	$V_{in}=5V, I_{out}=0 \text{ to } 1200mA$	—	20	80	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=5 \text{ to } 15V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=5V, I_{out}=0 \text{ to } 1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=5V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=3.6V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=3.6V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=3.6V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=5V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=14V$	1	1.5	2	A
Peak output current (3) Note 1)	$I_{O(peak)3}$	$V_{in}=19V$	0.5	1	1.5	A
Ripple rejection ratio	RR	$V_{in}=5 \text{ to } 7V, I_{out}=100mA, f=120Hz$	52	72	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_J(TH)$	$V_{in}=5V$	—	150	—	°C
Output voltage temperature coefficient Note 2)	α	$V_{in}=5V, T_J=25 \text{ to } 125^\circ C$	—	-40	—	ppm/°C

Note 1) Since it is the parameter of abnormal operation (over-current), it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7705/F (5V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V _O	V _{in} =6V, I _{out} =500mA	4.85	5	5.15	V
Input stability	REG _{IN}	V _{in} =6 to 16V, I _{out} =500mA	—	5	50	mV
Load stability	REG _{LOA}	V _{in} =6V, I _{out} =0 to 1200mA	—	25	100	mV
Input bias current stable	ΔI _{bias} (IN)	V _{in} =6 to 16V, I _{out} =500mA	—	1	10	mA
Bias current fluctuation under load	ΔI _{bias} (LOA)	V _{in} =6V, I _{out} =0 to 1200mA	—	10	50	mA
Bias current under no load	I _{bias}	V _{in} =6V, I _{out} =0mA	—	2.6	5	mA
Bias current before regulation start	I _{rush}	V _{in} =4.5V, I _{out} =0mA	—	3	5	mA
Min. input/output voltage difference (1)	V _{DIF(min)1}	V _{in} =4.5V, I _{out} =500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	V _{DIF(min)2}	V _{in} =4.5V, I _{out} =1200mA	—	0.5	1	V
Peak output current (1)	I _{O(peak)1}	V _{in} =6V	1.2	1.8	2.4	A
Peak output current (2) Note 1)	I _{O(peak)2}	V _{in} =15V	1	1.5	2	A
Peak output current (3) Note 1)	I _{O(peak)3}	V _{in} =20V	0.5	1	1.5	A
Ripple rejection ratio	RR	V _{in} =6 to 8V, I _{out} =100mA, f=120Hz	50	70	—	dB
Output short-circuit current Note 2)	I _{O(short)}	V _{in} =30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	T _{J(TH)}	V _{in} =6V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	V _{in} =6V, T _J =25 to 125℃	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds P_{D (max)} , it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7706/F (6V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V _O	V _{in} =7V, I _{out} =500mA	5.82	6	6.18	V
Input stability	REG _{IN}	V _{in} =7 to 17V, I _{out} =500mA	—	6	60	mV
Load stability	REG _{LOA}	V _{in} =7V, I _{out} =0 to 1200mA	—	30	120	mV
Input bias current stable	ΔI _{bias} (IN)	V _{in} =7 to 17V, I _{out} =500mA	—	1	10	mA
Bias current fluctuation under load	ΔI _{bias} (LOA)	V _{in} =7V, I _{out} =0 to 1200mA	—	10	50	mA
Bias current under no load	I _{bias}	V _{in} =7V, I _{out} =0mA	—	2.6	5	mA
Bias current before regulation start	I _{rush}	V _{in} =5.4V, I _{out} =0mA	—	3	5	mA
Min. input/output voltage difference (1)	V _{DIF(min)1}	V _{in} =5.4V, I _{out} =500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	V _{DIF(min)2}	V _{in} =5.4V, I _{out} =1200mA	—	0.5	1	V
Peak output current (1)	I _{O(peak)1}	V _{in} =7V	1.2	1.8	2.4	A
Peak output current (2) Note 1)	I _{O(peak)2}	V _{in} =16V	1	1.5	2	A
Peak output current (3) Note 1)	I _{O(peak)3}	V _{in} =21V	0.5	1	1.5	A
Ripple rejection ratio	RR	V _{in} =7 to 9V, I _{out} =100mA, f=120Hz	48	68	—	dB
Output short-circuit current Note 2)	I _{O(short)}	V _{in} =30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	T _{J(TH)}	V _{in} =7V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	V _{in} =7V, T _J =25 to 125℃	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds P_{D (max)} , it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7707/F (7V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=8V, I_{out}=500mA$	6.79	7	7.21	V
Input stability	REG_{IN}	$V_{in}=8\text{ to }18V, I_{out}=500mA$	—	7	70	mV
Load stability	REG_{LOA}	$V_{in}=8V, I_{out}=0\text{ to }1200mA$	—	35	140	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=8\text{ to }18V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=8V, I_{out}=0\text{ to }1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=8V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=6.3V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=6.3V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=6.3V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=8V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=17V$	1	1.5	2	A
Peak output current (3) Note 1)	$I_{O(peak)3}$	$V_{in}=22V$	0.5	1	1.5	A
Ripple rejection ratio	RR	$V_{in}=8\text{ to }10V, I_{out}=100mA, f=120Hz$	47	67	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_{j(TH)}$	$V_{in}=8V$	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	$V_{in}=8V, T_j=25\text{ to }125℃$	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7708/F (8V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=9V, I_{out}=500mA$	7.76	8	8.24	V
Input stability	REG_{IN}	$V_{in}=9\text{ to }19V, I_{out}=500mA$	—	8	80	mV
Load stability	REG_{LOA}	$V_{in}=9V, I_{out}=0\text{ to }1200mA$	—	40	160	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=9\text{ to }19V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=9V, I_{out}=0\text{ to }1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=9V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=7.2V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=7.2V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=7.2V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=9V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=18V$	1	1.5	2	A
Peak output current (3) Note 1)	$I_{O(peak)3}$	$V_{in}=23V$	0.5	1	1.5	A
Ripple rejection ratio	RR	$V_{in}=9\text{ to }11V, I_{out}=100mA, f=120Hz$	46	66	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_{j(TH)}$	$V_{in}=9V$	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	$V_{in}=9V, T_j=25\text{ to }125℃$	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7709/F (9V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	VO	Vin=10V, Iout=500mA	8.73	9	9.27	V
Input stability	REGIN	Vin=10 to 20V, Iout=500mA	—	9	90	mV
Load stability	REGLOA	Vin=10V, Iout=0 to 1200mA	—	45	180	mV
Input bias current stable	ΔIbias(IN)	Vin=10 to 20V, Iout=500mA	—	1	10	mA
Bias current fluctuation under load	ΔIbias(LOA)	Vin=10V, Iout=0 to 1200mA	—	10	50	mA
Bias current under no load	Ibias	Vin=10V, Iout=0mA	—	2.6	5	mA
Bias current before regulation start	Irush	Vin=8.1V, Iout=0mA	—	3	5	mA
Min. input/output voltage difference (1)	VDIF(min)1	Vin=8.1V, Iout=500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	VDIF(min)2	Vin=8.1V, Iout=1200mA	—	0.5	1	V
Peak output current (1)	IO(peak)1	Vin=10V	1.2	1.8	2.4	A
Peak output current (2) Note 1)	IO(peak)2	Vin=19V	1	1.5	2	A
Peak output current (3) Note 1)	IO(peak)3	Vin=24V	0.5	1	1.5	A
Ripple rejection ratio	RR	Vin=10 to 12V, Iout=100mA, f=120Hz	45	65	—	dB
Output short-circuit current Note 2)	IO(short)	Vin=30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	Tj(TH)	Vin=10V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	Vin=10V, Tj=25 to 125℃	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds PD (max) , it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7710/F (10V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	VO	Vin=11V, Iout=500mA	9.70	10	10.3	V
Input stability	REGIN	Vin=11 to 21V, Iout=500mA	—	10	100	mV
Load stability	REGLOA	Vin=11V, Iout=0 to 1200mA	—	50	200	mV
Input bias current stable	ΔIbias(IN)	Vin=11 to 21V, Iout=500mA	—	1	10	mA
Bias current fluctuation under load	ΔIbias(LOA)	Vin=11V, Iout=0 to 1200mA	—	10	50	mA
Bias current under no load	Ibias	Vin=11V, Iout=0mA	—	2.6	5	mA
Bias current before regulation start	Irush	Vin=9V, Iout=0mA	—	3	5	mA
Min. input/output voltage difference (1)	VDIF(min)1	Vin=9V, Iout=500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	VDIF(min)2	Vin=9V, Iout=1200mA	—	0.5	1	V
Peak output current (1)	IO(peak)1	Vin=11V	1.2	1.8	2.4	A
Peak output current (2) Note 1)	IO(peak)2	Vin=20V	1	1.5	2	A
Peak output current (3) Note 1)	IO(peak)3	Vin=25V	0.5	1	1.5	A
Ripple rejection ratio	RR	Vin=11 to 13V, Iout=100mA, f=120Hz	44	64	—	dB
Output short-circuit current Note 2)	IO(short)	Vin=30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	Tj(TH)	Vin=11V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	Vin=11V, Tj=25 to 125℃	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds PD (max) , it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7712/F (12V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=13V, I_{out}=500mA$	11.64	12	12.36	V
Input stability	REG_{IN}	$V_{in}=13 \text{ to } 23V, I_{out}=500mA$	—	12	120	mV
Load stability	REG_{LOA}	$V_{in}=13, I_{out}=0 \text{ to } 1200mA$	—	60	240	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=13 \text{ to } 23V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=13V, I_{out}=0 \text{ to } 1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=13V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=10.8V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=10.8V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=10.8V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=13V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=22V$	1	1.5	2	A
Peak output current (3) Note 1)	$I_{O(peak)3}$	$V_{in}=27V$	0.5	1	1.5	A
Ripple rejection ratio	RR	$V_{in}=13 \text{ to } 15V, I_{out}=100mA, f=120Hz$	42	62	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_{j(TH)}$	$V_{in}=13V$	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	$V_{in}=13V, T_j=25 \text{ to } 125℃$	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7715/F (15V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V_O	$V_{in}=16.5V, I_{out}=500mA$	14.55	15	15.45	V
Input stability	REG_{IN}	$V_{in}=16.5 \text{ to } 26.5V, I_{out}=500mA$	—	15	150	mV
Load stability	REG_{LOA}	$V_{in}=16.5V, I_{out}=0 \text{ to } 1200mA$	—	75	300	mV
Input bias current stable	$\Delta I_{bias(IN)}$	$V_{in}=16.5 \text{ to } 26.5V, I_{out}=500mA$	—	1	10	mA
Bias current fluctuation under load	$\Delta I_{bias(LOA)}$	$V_{in}=16.5V, I_{out}=0 \text{ to } 1200mA$	—	10	50	mA
Bias current under no load	I_{bias}	$V_{in}=16.5V, I_{out}=0mA$	—	2.6	5	mA
Bias current before regulation start	I_{rush}	$V_{in}=13.5V, I_{out}=0mA$	—	3	5	mA
Min. input/output voltage difference (1)	$V_{DIF(min)1}$	$V_{in}=13.5V, I_{out}=500mA$	—	0.4	0.6	V
Min. input/output voltage difference (2)	$V_{DIF(min)2}$	$V_{in}=13.5V, I_{out}=1200mA$	—	0.5	1	V
Peak output current (1)	$I_{O(peak)1}$	$V_{in}=16.5V$	1.2	1.8	2.4	A
Peak output current (2) Note 1)	$I_{O(peak)2}$	$V_{in}=25V$	1	1.5	2	A
Ripple rejection ratio	RR	$V_{in}=16.5 \text{ to } 18.5V, I_{out}=100mA, f=120Hz$	40	60	—	dB
Output short-circuit current Note 2)	$I_{O(short)}$	$V_{in}=30V, \text{Load short-circuit}$	—	10	—	mA
Thermal protection operating temperature Note 2)	$T_{j(TH)}$	$V_{in}=16.5V$	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	$V_{in}=16.5V, T_j=25 \text{ to } 125℃$	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds $P_{D(max)}$, it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7718/F (18V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	Vo	Vin=19.5V, Iout=500mA	17.46	18	18.54	V
Input stability	REGIN	Vin=19.5 to 29.5V, Iout=500mA	—	18	180	mV
Load stability	REGLOA	Vin=19.5V, Iout=0 to 1200mA	—	90	360	mV
Input bias current stable	ΔIbias(IN)	Vin=19.5 to 29.5V, Iout=500mA	—	1	10	mA
Bias current fluctuation under load	ΔIbias(LOA)	Vin=19.5V, Iout=0 to 1200mA	—	10	50	mA
Bias current under no load	Ibias	Vin=19.5V, Iout=0mA	—	2.6	5	mA
Bias current before regulation start	Irush	Vin=16.2V, Iout=0mA	—	3	5	mA
Min. input/output voltage difference (1)	VDIF(min)1	Vin=16.2V, Iout=500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	VDIF(min)2	Vin=16.2V, Iout=1200mA	—	0.5	1	V
Peak output current (1)	IO(peak)1	Vin=19.5V	1.2	1.8	2.4	A
Peak output current (2) Note 1)	IO(peak)2	Vin=28V	1	1.5	2	A
Ripple rejection ratio	RR	Vin=19.5 to 21.5V, Iout=100mA, f=120Hz	39	59	—	dB
Output short-circuit current Note 2)	IO(short)	Vin=30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	Tj(TH)	Vin=19.5V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	Vin=19.5V, Tj=25 to 125℃	—	-40	—	ppm/℃

Note 1) Since it is the parameter of abnormal operation (over-current) , it exceeds P_{D (max)} , it should be usually set according to the derating curve.

Note 2) These values are design reference values, not guaranteed values.

• AN7720/F (20V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	Vo	Vin=21.5V, Iout=500mA	19.4	20	20.6	V
Input stability	REGIN	Vin=21.5 to 29.5V, Iout=500mA	—	16	160	mV
Load stability	REGLOA	Vin=21.5V, Iout=0 to 1200mA	—	100	400	mV
Input bias current stable	ΔIbias(IN)	Vin=21.5 to 29.5V, Iout=500mA	—	1	10	mA
Bias current fluctuation under load	ΔIbias(LOA)	Vin=21.5V, Iout=0 to 1200mA	—	10	50	mA
Bias current under no load	Ibias	Vin=21.5V, Iout=0mA	—	2.6	5	mA
Bias current before regulation start	Irush	Vin=18V, Iout=0mA	—	3	5	mA
Min. input/output voltage difference (1)	VDIF(min)1	Vin=18V, Iout=500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	VDIF(min)2	Vin=18V, Iout=1200mA	—	0.5	1	V
Peak output current (1)	IO(peak)	Vin=21.5V	1.2	1.8	2.4	A
Ripple rejection ratio	RR	Vin=21.5 to 23.5V, Iout=100mA, f=120Hz	38	58	—	dB
Output short-circuit current Note 2)	IO(short)	Vin=30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature Note 2)	Tj(TH)	Vin=21.5V	—	150	—	℃
Output voltage temperature coefficient Note 2)	α	Vin=21.5V, Tj=25 to 125℃	—	-40	—	ppm/℃

* These values are design reference values, not guaranteed values.

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■ Electrical Characteristics (Ta=25℃)

• AN7724/F (24V Type)

Parameter	Symbol	Condition	min	typ	max	Unit
Output voltage	V _O	V _{in} = 25.5V, I _{out} = 500mA	23.28	24	24.72	V
Input stability	REG _{IN}	V _{in} = 25.5 to 29.5V, I _{out} = 500mA	—	9.6	96	mV
Load stability	REG _{LOA}	V _{in} = 25.5V, I _{out} = 0 to 1200mA	—	120	480	mV
Input bias current stable	ΔI _{bias} (IN)	V _{in} = 25.5 to 29.5V, I _{out} = 500mA	—	1	10	mA
Bias current fluctuation under load	ΔI _{bias} (LOA)	V _{in} = 25.5V, I _{out} = 0 to 1200mA	—	10	50	mA
Bias current under no load	I _{bias}	V _{in} = 25.5V, I _{out} = 0mA	—	2.6	5	mA
Bias current before regulation start	I _{rush}	V _{in} = 21.6V, I _{out} = 0mA	—	3	5	mA
Min. input/output voltage difference (1)	V _{DIF} (min)1	V _{in} = 21.6V, I _{out} = 500mA	—	0.4	0.6	V
Min. input/output voltage difference (2)	V _{DIF} (min)2	V _{in} = 21.6V, I _{out} = 1200mA	—	0.5	1	V
Peak output current (1)	I _O (peak)	V _{in} = 25.5V	1.2	1.8	2.4	A
Ripple rejection ratio	RR	V _{in} = 25.5 to 27.5V, I _{out} = 100mA, f = 120Hz	36	56	—	dB
Output short-circuit current ^{Note 2)}	I _O (short)	V _{in} = 30V, Load short-circuit	—	10	—	mA
Thermal protection operating temperature ^{Note 2)}	T _j (TH)	V _{in} = 25.5V	—	150	—	℃
Output voltage temperature coefficient ^{Note 2)}	α	V _{in} = 25.5V, T _j = 25 to 125℃	—	—40	—	ppm/℃

* These values are design reference values, not guaranteed values.

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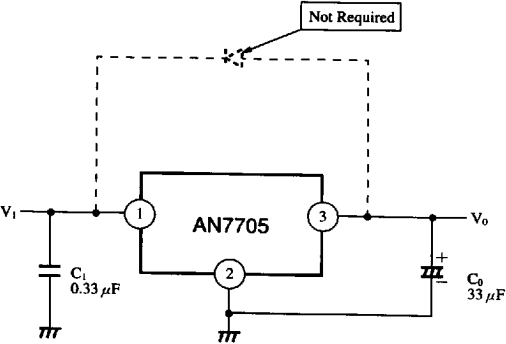
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■ Precautions on Use

1. Input Short-Circuit Protection Circuit

For the conventional Matsushita 3-pin regulators (such as of the AN7800 series), when DC input Pin ③ is short-circuited with GND ② in the normal operation condition, the potential of output Pin ③ becomes higher than that of DC input pin and the electric charges which is charged in output capacitor C_o flows in the input side, resulting in the breakage of elements.

In the above case, the common silicone diode is connected as shown in the right figure (the dotted line). However, for the AN77XX/F series, since the protection circuit, which protects the elements from the discharging current, is incorporated in the internal circuit, the protection diode is not required.

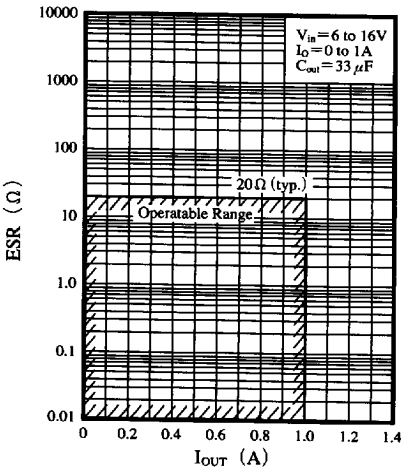


2. Capacitor for External Compensation

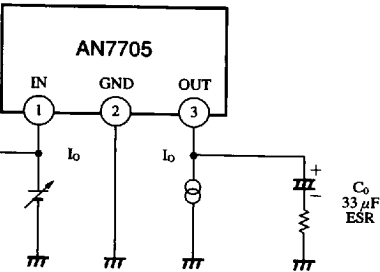
In order to secure the safety, the capacitor of 33 μ F is required in the output side and it should be added as near as possible to output Pin ③ and GND ②. When it is used under low temperature, oscillation may occur due to the decrease of the aluminum electrolytic capacitor and increase of ESR.

For the AN77XX/F, it is recommended that the tantalum capacitor or aluminum electrolytic capacitor whose serial-connected resistance equivalent with that of output capacitor C_o has temperature characteristics within the recommended range specified in the right.

Output Capacitor ESR (Equivalent Serial Connected Resistor) Under $T_a = 25^\circ\text{C}$



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3. Others

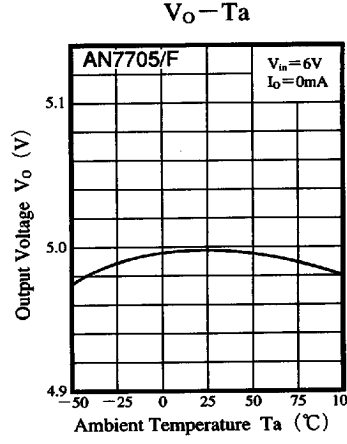
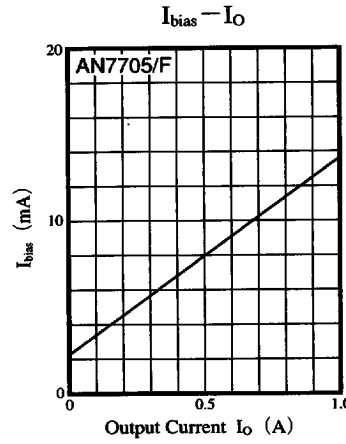
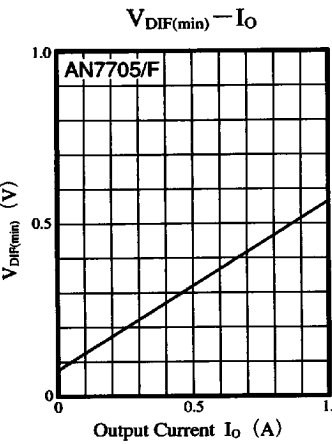
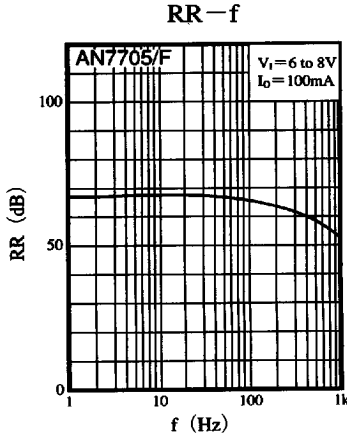
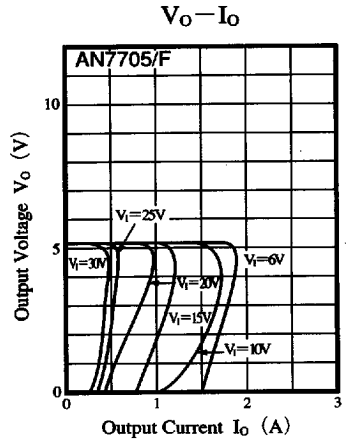
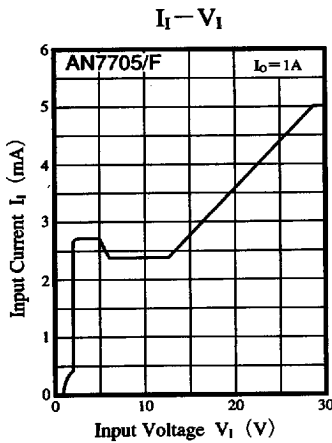
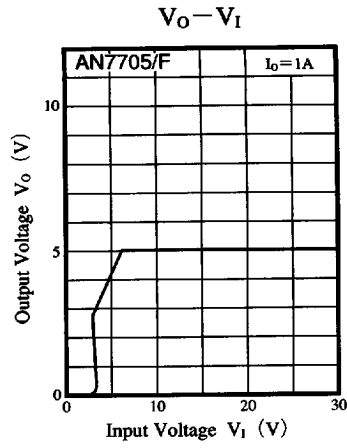
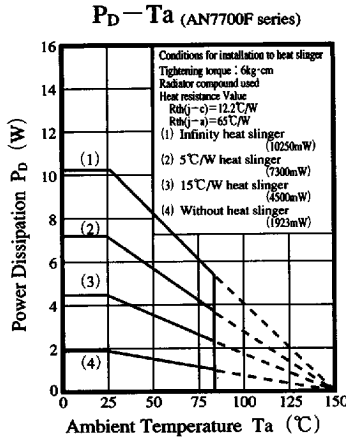
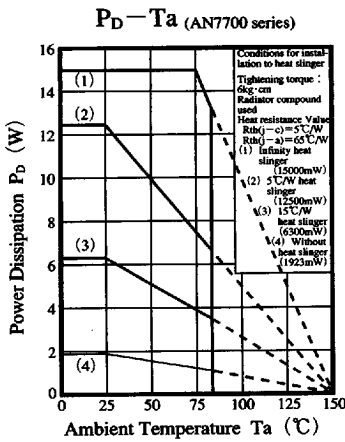
Precautions for the input voltage exceeding the operating supply voltage

- a) When V_{CC} of 30V is applied, the overvoltage protection function is activated and the output may be shut down (3 V type to 10 V type).
- b) When it is used under $I_{OUT} \leq 2 \text{ mA}$, the output voltage may rise over the maximum value within the operating supply voltage range (12 V type to 24 V type).

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■ Characteristics Curve



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