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ADC1213D065WO demo board; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

Status: Active New

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ADC1213D065WO ADC1213D065WO demo board; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

Our ADC demostration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets



Features

- Single POWER supply (on board regulators)
- · On board demodulator + LO synthesizer
- · SMA connector for clock and analog inputs signal
- On board Data clock generator

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
ADC1213D065WO-DB	Active New	BD0	BOARD	0	С	N	Check Inventory

Documents

There are no public documents available for this product. Click here to request documentation.

Related Products

PRODUCTID	TITLE
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