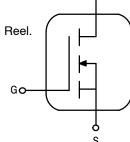
The RF MOSFET Line

RF Power Field Effect Transistors

N-Channel Enhancement-Mode Lateral MOSFETs

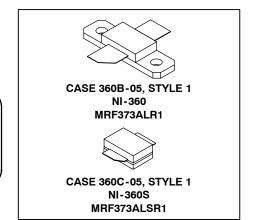
Designed for broadband commercial and industrial applications with frequencies from 470 to 860 MHz. The high gain and broadband performance of these devices make them ideal for large-signal, common source amplifier applications in 28/32 volt transmitter equipment.

- Typical CW Performance at 860 MHz, 32 Volts, Narrowband Fixture Output Power — 75 Watts Power Gain — 18.2 dB Efficiency — 60%
- 100% Tested for Load Mismatch Stress at All Phase Angles with 10:1 VSWR @ 32 Vdc, 860 MHz, 75 Watts CW
- Integrated ESD Protection
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters
- In Tape and Reel. R1 = 500 units per 32 mm, 13 inch Reel.
- Low Gold Plating Thickness on Leads.
 L Suffix Indicates 40μ" Nominal.



MRF373ALR1 MRF373ALSR1

470 - 860 MHz, 75 W, 32 V LATERAL N-CHANNEL BROADBAND RF POWER MOSFETS



MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	70	Vdc
Gate-Source Voltage		V _{GS}	- 0.5, +15	Vdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	MRF373ALR1 MRF373ALSR1	P _D	197 1.12 278 1.59	Watts W/°C Watts W/°C
Storage Temperature Range		T _{stg}	- 65 to +150	°C
Operating Junction Temperature		TJ	200	°C

THERMAL CHARACTERISTICS

Characteristic		Symbol	Max	Unit
Thermal Resistance, Junction to Case	MRF373ALR1	$R_{ heta JC}$	0.89	°C/W
	MRF373ALSR1		0.63	

ESD PROTECTION CHARACTERISTICS

Test Conditions		Class	
Human Body Model		1 (Minimum)	
Machine Model	MRF373ALR1 MRF373ALSR1	M2 (Minimum) M1 (Minimum)	

NOTE - <u>CAUTION</u> - MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.

REV 4





ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•	•		•	
Drain-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D =1 μA)	V _{(BR)DSS}	70	_	_	Vdc
Zero Gate Voltage Drain Current (V _{DS} = 32 Vdc, V _{GS} = 0 Vdc)	I _{DSS}	_	_	1	μAdc
Gate-Source Leakage Current (V _{GS} = 5 Vdc, V _{DS} = 0 Vdc)	I _{GSS}	_	_	1	μAdc
ON CHARACTERISTICS	•	•		•	
Gate Threshold Voltage (V_{DS} = 10 V, I_{D} = 200 μ A)	V _{GS(th)}	2	2.9	4	Vdc
Gate Quiescent Voltage (V _{DS} = 32 V, I _D = 100 mA)	V _{GS(Q)}	2.5	3.3	4.5	Vdc
Drain-Source On-Voltage (V _{GS} = 10 V, I _D = 3 A)	V _{DS(on)}	_	0.41	0.45	Vdc
DYNAMIC CHARACTERISTICS	•	•		•	
Input Capacitance (V _{DS} = 32 V, V _{GS} = 0, f = 1 MHz)	C _{iss}	_	98.5	_	pF
Output Capacitance (V _{DS} = 32 V, V _{GS} = 0, f = 1 MHz)	C _{oss}	_	49	_	pF
Reverse Transfer Capacitance (V _{DS} = 32 V, V _{GS} = 0, f = 1 MHz)	C _{rss}	_	2	_	pF
FUNCTIONAL CHARACTERISTICS (50 ohm system)	1	•		•	
Common Source Power Gain (V _{DD} = 32 V, P _{out} = 75 W CW, I _{DQ} = 200 mA, f = 860 MHz)	G _{ps}	16.5	18.2		dB
Drain Efficiency $(V_{DD} = 32 \text{ V}, P_{out} = 75 \text{ W CW}, I_{DQ} = 200 \text{ mA}, f = 860 \text{ MHz})$	η	56	60	_	%
Load Mismatch $(V_{DD}=32~V,~P_{out}=75~W~CW,~I_{DQ}=200~mA,~f=860~MHz,~Load~VSWR~at~10:1~at~All~Phase~Angles)$	Ψ	No Degradation in Output Power			

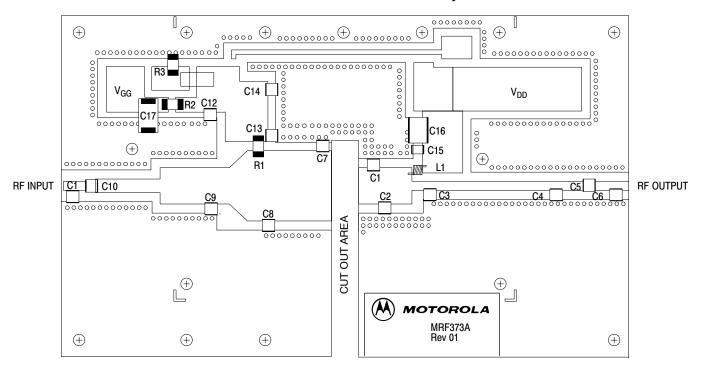


Figure 1. MRF373ALR1/ALSR1 Narrowband Test Circuit Component Layout

Table 1. MRF373ALR1/ALSR1 Narrowband Test Circuit Component Layout Designations and Values

Designation	Description	
C1, C2	18 pF Chip Capacitors, B Case, ATC	
C3	12 pF Chip Capacitor, B Case, ATC	
C4	1.8 pF Chip Capacitor, B Case, ATC	
C5, C10	51 pF Chip Capacitors, B Case, ATC	
C6	0.3 pF Chip Capacitor, B Case, ATC (Used only on the MRF373AS)	
C7	15 pF Chip Capacitor, B Case, ATC	
C8	10 pF Chip Capacitor, B Case, ATC	
C9	2.7 pF Chip Capacitor, B Case, ATC	
C11	0.5 pF Chip Capacitor, B Case, ATC	
C12	1000 pF Chip Capacitor, B Case, ATC	
C13	39 pF Chip Capacitor, B Case, ATC	
C14, C15	470 pF Chip Capacitors, B Case, ATC	
C16	2.2 μF, 100 V Chip Capacitor, Vishay #VJ3640Y225KXBAT	
C17	10 μF, 35 V Tantalum Capacitor, Kemet #T491D106K35AS	
L1A	12 nH, Coilcraft #A04T	
R1, R2	390 Ω , 1/2 Ω Chip Resistors, Vishay Dale (2010)	
R3	1 k Ω , 1/2 Ω Chip Resistor, Vishay Dale (2010)	
PCB	Arlon GX-0300-55, 30 mils, $\varepsilon_{\rm r}$ = 2.55	

TYPICAL CHARACTERISTICS

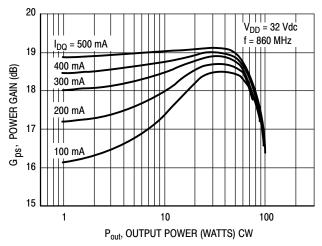


Figure 2. Power Gain versus Output Power

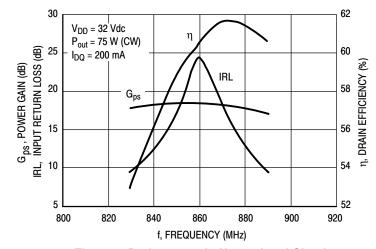


Figure 3. Performance in Narrowband Circuit

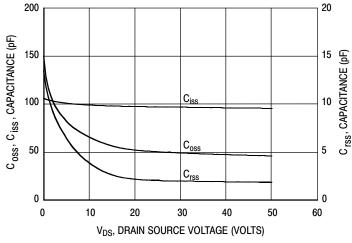
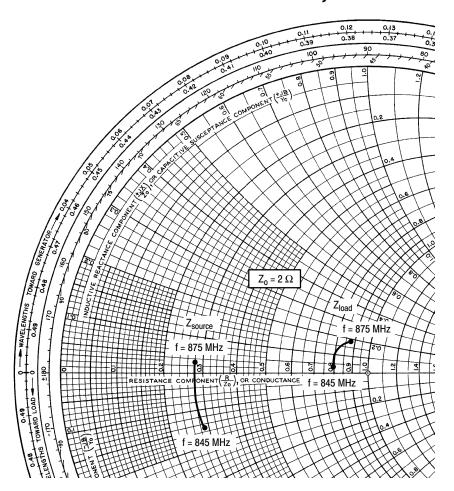


Figure 4. Capacitance versus Voltage

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 V_{DD} = 32 V, I_{DQ} = 200 mA, P_{out} = 75 W CW

f MHz	$\mathbf{Z_{source}}_{\Omega}$	$oldsymbol{Z_{load}}{\Omega}$
845	0.58 - j0.29	1.60 + j0.07
860	0.56 - j0.11	1.65 + j0.22
875	0.56 + j0.06	1.79 + j0.38

Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

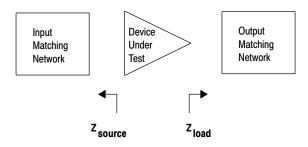
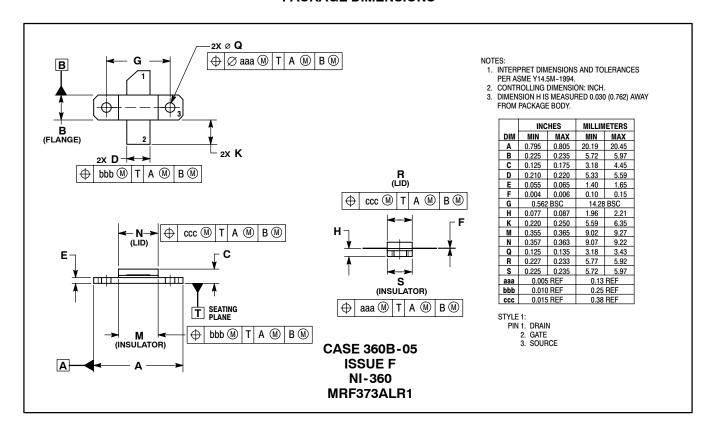
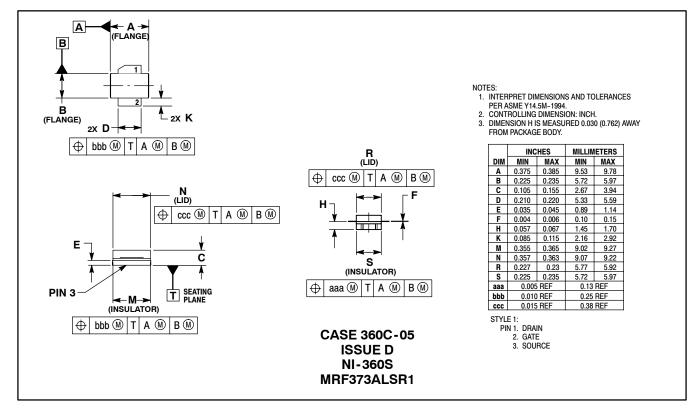


Figure 5. Series Equivalent Input and Output Impedance

PACKAGE DIMENSIONS





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