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ADC1113D125WO demo board; compliant with Lattice, Altera, Xilinx FPGAboards through specific connectors

Status: **NRND**

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ADC1113D125WO  
ADC1113D125WO demo board; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

Our ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets



Features

- Single POWER supply (on board regulators)
- On board demodulator +LO synthesizer
- SMA connector for clock and analog inputs signal
- On board Data clock generator

Product Options

| Orderable Part ID | Part Status | Pkg. Code           | Pkg. Type | Lead Count (#) | Temp. Grade | Pb (Lead) Free | Sample & Buy                       |
|-------------------|-------------|---------------------|-----------|----------------|-------------|----------------|------------------------------------|
| ADC1113D125WO-DB  | NRND        | <a href="#">BD0</a> | BOARD     | 0              | I           | No             | <a href="#">Check Availability</a> |

Documents

There are no public documents available for this product. [Click here to request documentation.](#)

Related Products

| Product ID                    | Title                                       |
|-------------------------------|---------------------------------------------|
| <a href="#">ADC1113D125HN</a> | Dual 11 bits ADC; 125 Msps; serial JESD204A |

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