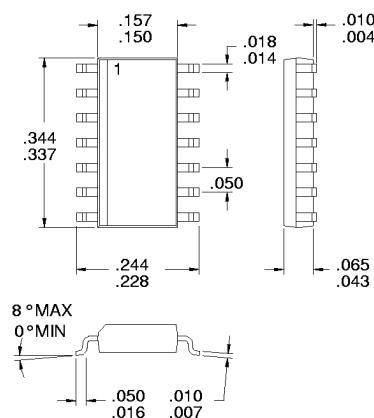


Typical Applications

- Analog Communication Systems
- Digital Communication Systems
- Single Sideband Modulation
- Commercial and Consumer Systems
- Portable Battery Powered Equipment
- Image-Reject Upconverter

Product Description

The RF2812 is a monolithic integrated universal modulation system capable of generating modulated AM, PM, or compound carriers in the VHF frequency range. The IC contains all of the required components to implement the modulation function including differential amplifiers for the baseband inputs, a 90°-hybrid phase splitter, limiting LO amplifiers, two balanced mixers, a combining amplifier, and an output RF amplifier which will drive a 200Ω to 400Ω load. Component matching, which can only be accomplished with monolithic construction, is used to full advantage to obtain excellent amplitude balance and high phase accuracy.



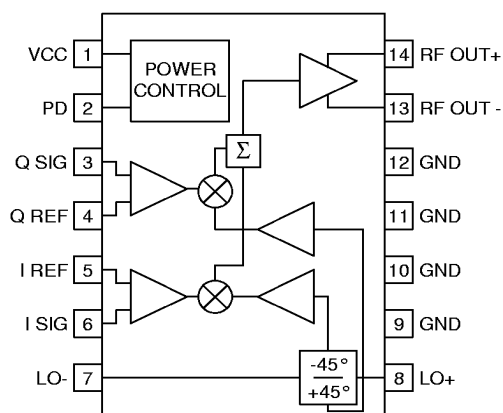
Optimum Technology Matching® Applied

- ☒ Si BJT ☐ GaAs HBT ☐ GaAs MESFET
☐ Si Bi-CMOS

Package Style: SOP-14

Features

- 3V to 6V Power Supply
- Integrated Quadrature Network
- Excellent Amplitude & Phase Balance
- Digitally Controlled Power Down Mode
- 50MHz Modulation Bandwidth
- 50MHz to 250MHz Operation



Functional Block Diagram

Ordering Information

RF2812	VHF Quadrature Modulator
RF2812 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc.
7625 Thorndike Road
Greensboro, NC 27409, USA

Tel (336) 664 1233
Fax (336) 664 0454
<http://www.rfmd.com>

Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +7.0	V _{DC}
Power Down Voltage	V _{CC} +0.4	V _{DC}
Input LO Level	+10	dBm
Input Modulation Level	2	V _{PP}
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



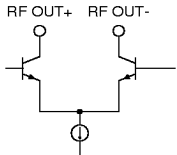
Caution! ESD sensitive device.

RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Carrier Input					T=25 °C, V _{CC} =5.0V
Frequency Range		50-250		MHz	
Power Level		-20 to +6		dBm	
Input Impedance		200		Ω	At 150MHz
I/Q Amplitude Error		0.1		dB	
Modulation Input					
Frequency Range		DC-50		MHz	
Reference Voltage		1.8 to 3.0		V	
Maximum Modulation		1.5		V _{P-P}	I and Q signals
Quadrature Phase Error		±2		°	
DC Offset		3		mV	I _{sig} - I _{ref} and Q _{sig} - Q _{ref}
RF Output					LO power=-10dBm, LO freq=200MHz, SSB, V _{MOD} =480mV _{PP}
Output Power	-6	-3		dBm	1 dB compression
Output Impedance	Balanced Open Collector			Ω	
Output Load		200		Ω	Balanced
Output Noise Floor		<-135		dBm/Hz	
Output IP3		+10		dBm	
Sideband Suppression	20	30		dB	
Carrier Suppression	18	35		dB	
Power Down					
Turn On/Off Time		<100		ns	
PD Input Resistance		>50		kΩ	
PD "OFF" Voltage	1.0	1.2		V	
PD "ON" Voltage		V _{CC}		V	
Power Supply					
Voltage		5		V	Specifications
		2.7 to 6.0		V	Operating limits
Current		18	25	mA	Operating
		3	10	μA	Power Down

Pin	Function	Description	Interface Schematic
1	VCC	Power supply pin. An external 2.2nF bypass capacitor is recommended if no other bypass capacitor is nearby.	
2	PD	Power Down control. When this pin is "low", all circuits are shut off. A "low" is typically 1.2V or less at room temperature. When this pin is "high", all circuits are operating normally. A "high" is V _{CC} . If PD is below V _{CC} output power and performance will be degraded. Operating in this region is not recommended.	
3	Q SIG	Baseband input to the Q mixer. This pin is DC coupled. Maximum output power is obtained when the input signal has a peak to peak amplitude of 1.5V. The recommended DC level for this pin is 2.5V when the power supply is 5V, and 1.8V when the power supply is 3V. If the power supply voltage is 3V, the maximum modulation signal should not exceed 0.6V _{P-P} . The peak minimum voltage on this pin (VREF - peak modulation amplitude) should never drop below 1.3V. The peak maximum voltage on this pin (VREF + peak modulation amplitude) should never exceed 4.0V. The input impedance is very high for frequencies up to 10MHz. Above 10MHz this input looks capacitive. The SIG and REF inputs are inputs of a differential amplifier. Therefore the REF and SIG inputs are interchangeable. If swapping the Q SIG and Q REF pins, the I SIG and I REF also need to be swapped to maintain the correct phase. It is also possible to drive the SIG and REF inputs in a balanced mode. This will increase the gain.	
4	Q REF	Reference voltage for the Q mixer. This voltage should be the same as the DC voltage supplied to the Q SIG pin.	See pin 3.
5	I REF	Reference voltage for the I mixer. This voltage should be the same as the DC voltage supplied to the I SIG pin.	Same as pin 4.
6	I SIG	Base band input to the I mixer. This pin is DC coupled. Maximum output power is obtained when the input signal has a peak to peak amplitude of 1.5V. The recommended DC level for this pin is 2.5V when the power supply is 5V, and 1.8V when the power supply is 3V. If the power supply voltage is 3V, the maximum modulation signal should not exceed 0.6V _{P-P} . The peak minimum voltage on this pin (VREF - peak modulation amplitude) should never drop below 1.3V. The peak maximum voltage on this pin (VREF + peak modulation amplitude) should never exceed 4.0V. The input impedance is very high for frequencies up to 10MHz. Above 10MHz this input looks capacitive.	Same as pin 3.
7	LO-	Balanced LO input. When the LO is driven single ended this pin should be connected to a 2.2nF capacitor to ground. This pin has an internal pull-up resistor to V _{CC} , and an external DC blocking capacitor is recommended.	
8	LO+	Balanced LO input. This pin has an internal pull-up resistor to V _{CC} , and an external DC blocking capacitor is recommended. When the LO is driven single ended an external 56 ohm may be connected to provide a match.	Same as pin 7.
9	GND	Ground connection. This pin should be connected directly to the ground plane.	
10	GND	Same as pin 9.	
11	GND	Same as pin 9.	
12	GND	Same as pin 9.	

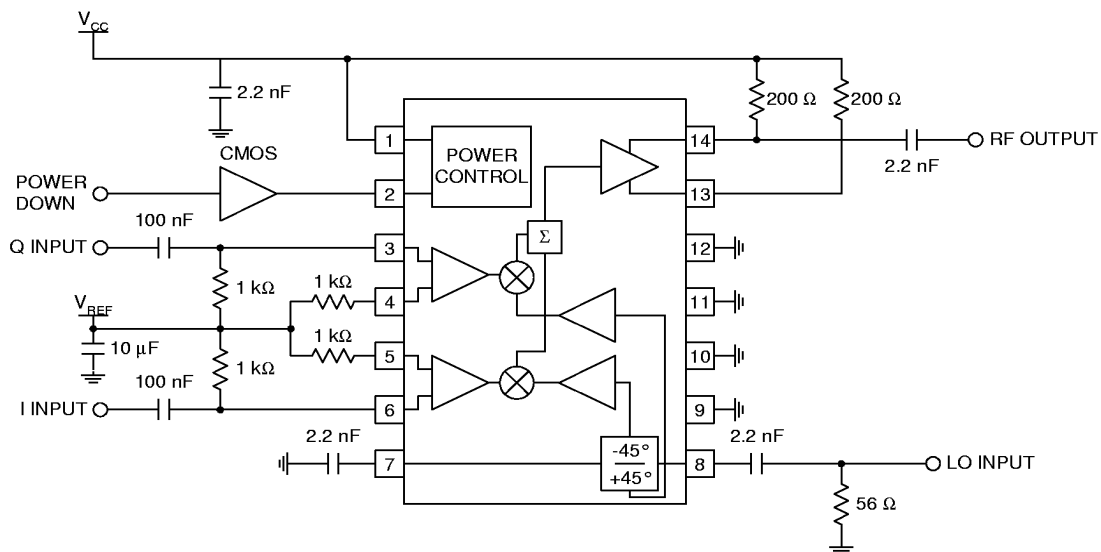
RF2812

13	RF OUT-	Balanced RF output. This pin is an open collector output. An external pull-up resistor is needed for the circuit to operate. The maximum resistor value is 200Ω; this will give the maximum output voltage. If a more linear operation is required the resistor value can be lowered to reduce output swing. The output impedance will be approximately equal to the resistor value. Even when a single ended output is used the pull-up resistor should be connected.	
14	RF OUT+	Balanced RF output. This pin is an open collector output. An external pull-up resistor is needed for the circuit to operate. The maximum resistor value is 200Ω; this will give the maximum output voltage. If a more linear operation is required the resistor value can be lowered to reduce output swing. The output impedance will be approximately equal to the resistor value.	See pin 13.

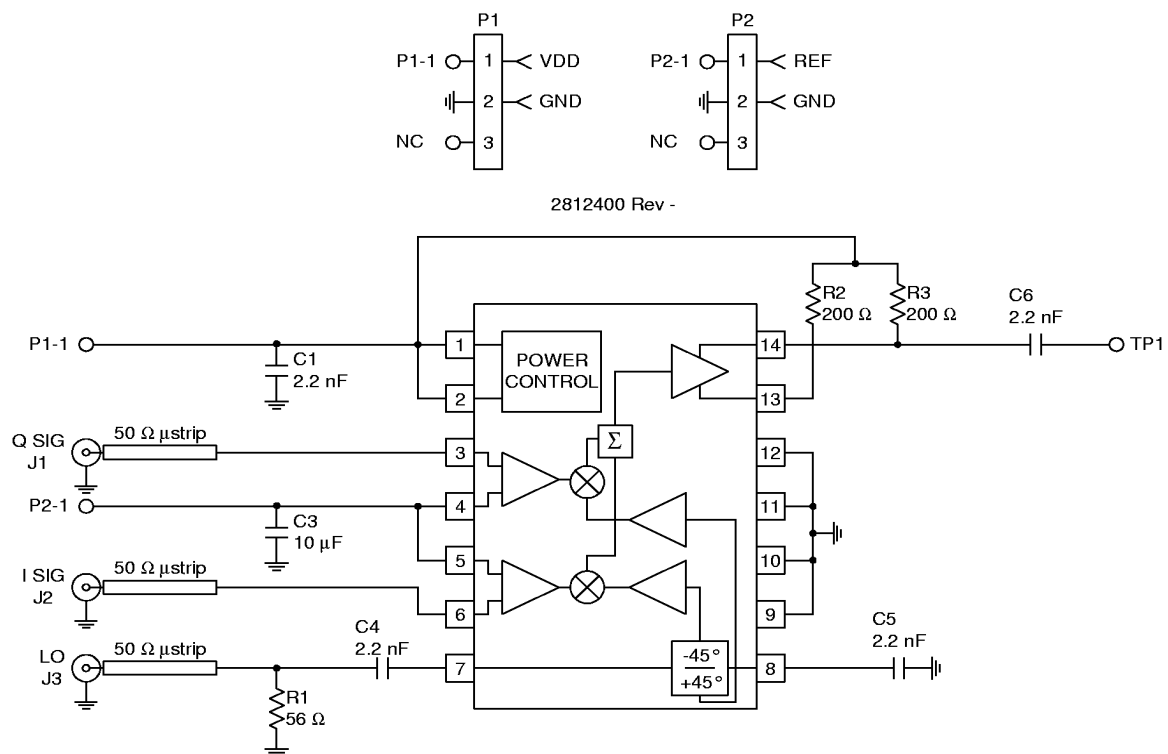
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MODULATORS AND
UPCONVERTERS

Application Schematic



Evaluation Board Schematic



Evaluation Board Layout

