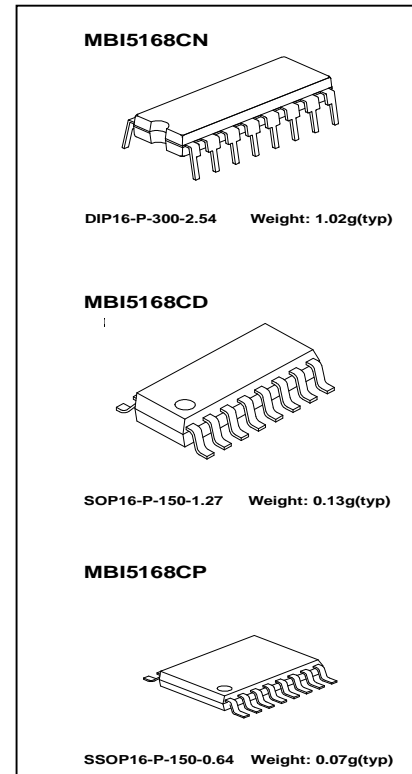


**8-bit Constant Current LED Sink Driver****Features**

- | 8 constant-current output channels
- | Constant output current invariant to load voltage change
- | Excellent output current accuracy:
between channels: $< \pm 4\%$ (max.), and
between ICs: $< \pm 6\%$ (max.)
- | Output current adjusted through an external resistor
- | Constant output current range: 5 -120 mA
- | Fast response of output current, /OE (min.): 400 ns
- | 25MHz clock frequency
- | Schmitt trigger input
- | 5V supply voltage



Current Accuracy		Conditions
Between Channels	Between ICs	
$< \pm 4\%$	$< \pm 6\%$	$I_{OUT} = 10 \text{ mA to } 60 \text{ mA}, V_{DS} = 0.6V$
$< \pm 6\%$	$< \pm 12\%$	$I_{OUT} = 60 \text{ mA to } 100 \text{ mA}, V_{DS} = 0.8V$

Product Description

MBI5168 is designed for LED displays. As an enhancement of its predecessor, MBI5001, MBI5168 exploits PrecisionDrive™ technology to enhance its output characteristics. MBI5168 contains a serial buffer and data latches which convert serial input data into parallel output format. At MBI5168 output stage, eight regulated current ports are designed to provide uniform and constant current sinks for driving LEDs within a large range of V_f variations.

MBI5168 provides users with great flexibility and device performance while using MBI5168 in their system design for LED display applications, e.g. LED panels. Users may adjust the output current from 5 mA to 120 mA through an external resistor, R_{ext} , which gives users flexibility in controlling the light intensity of LEDs. MBI5168 guarantees to endure maximum 17V at the output port. The high clock frequency, 25 MHz, also satisfies the system requirements of high volume data transmission.

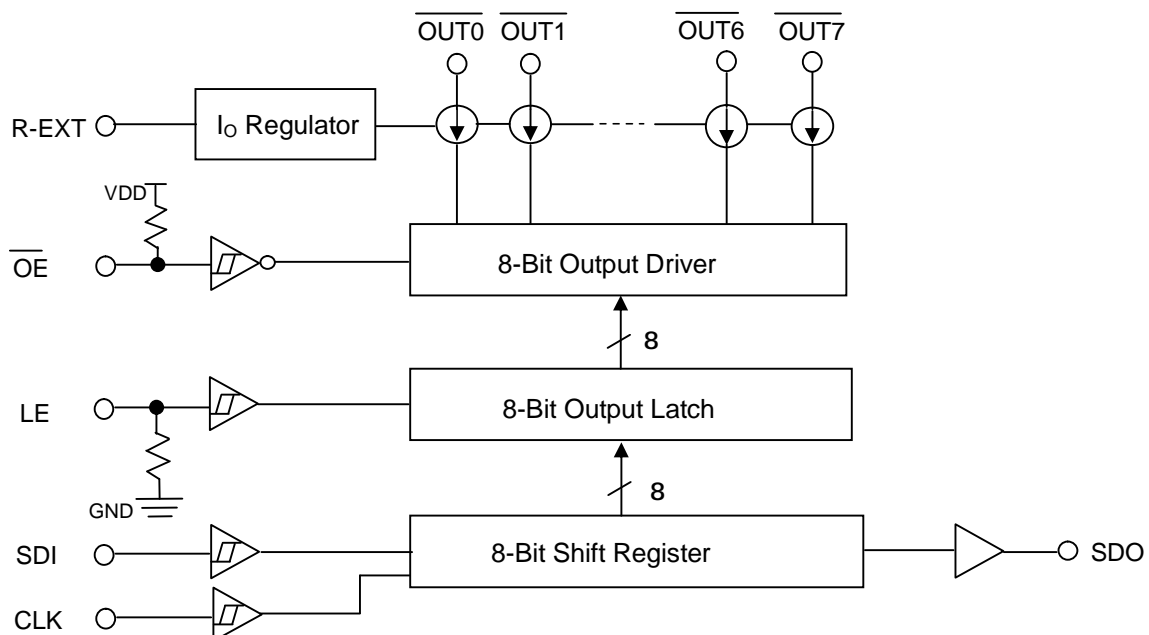
Terminal Description

PIN NO.	PIN NAME	FUNCTION
1	GND	Ground terminal for control logic and current sink
2	SDI	Serial-data input to the shift register
3	CLK	Clock input terminal for data shift on rising edge
4	LE	Data strobe input terminal Serial data is transferred to the respective latch when LE is high. The data is latched when LE goes low.
5-12	$\overline{\text{OUT0}} \sim \overline{\text{OUT7}}$	Constant current output terminals
13	$\overline{\text{OE}}$	Output enable terminal When (active) low, the output drivers are enabled; when high, all output drivers are turned OFF (blanked).
14	SDO	Serial-data output to the following SDI of next driver IC
15	R-EXT	Input terminal used to connect an external resistor for setting up output current for all output channels
16	VDD	5V supply voltage terminal

Pin Description

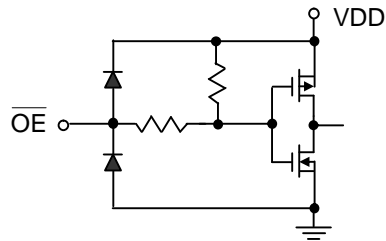
GND	1	16	VDD
SDI	2	15	R-EXT
CLK	3	14	SDO
LE	4	13	$\overline{\text{OE}}$
$\overline{\text{OUT0}}$	5	12	$\overline{\text{OUT7}}$
$\overline{\text{OUT1}}$	6	11	$\overline{\text{OUT6}}$
$\overline{\text{OUT2}}$	7	10	$\overline{\text{OUT5}}$
$\overline{\text{OUT3}}$	8	9	$\overline{\text{OUT4}}$

Block Diagram

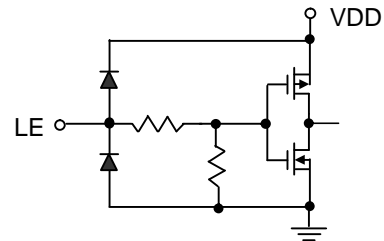


Equivalent Circuits of Inputs and Outputs

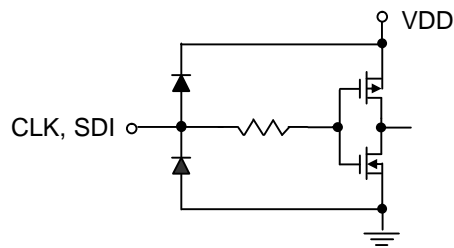
$\overline{\text{OE}}$ terminal



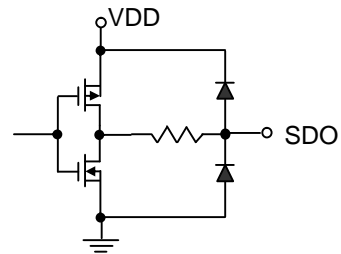
LE terminal



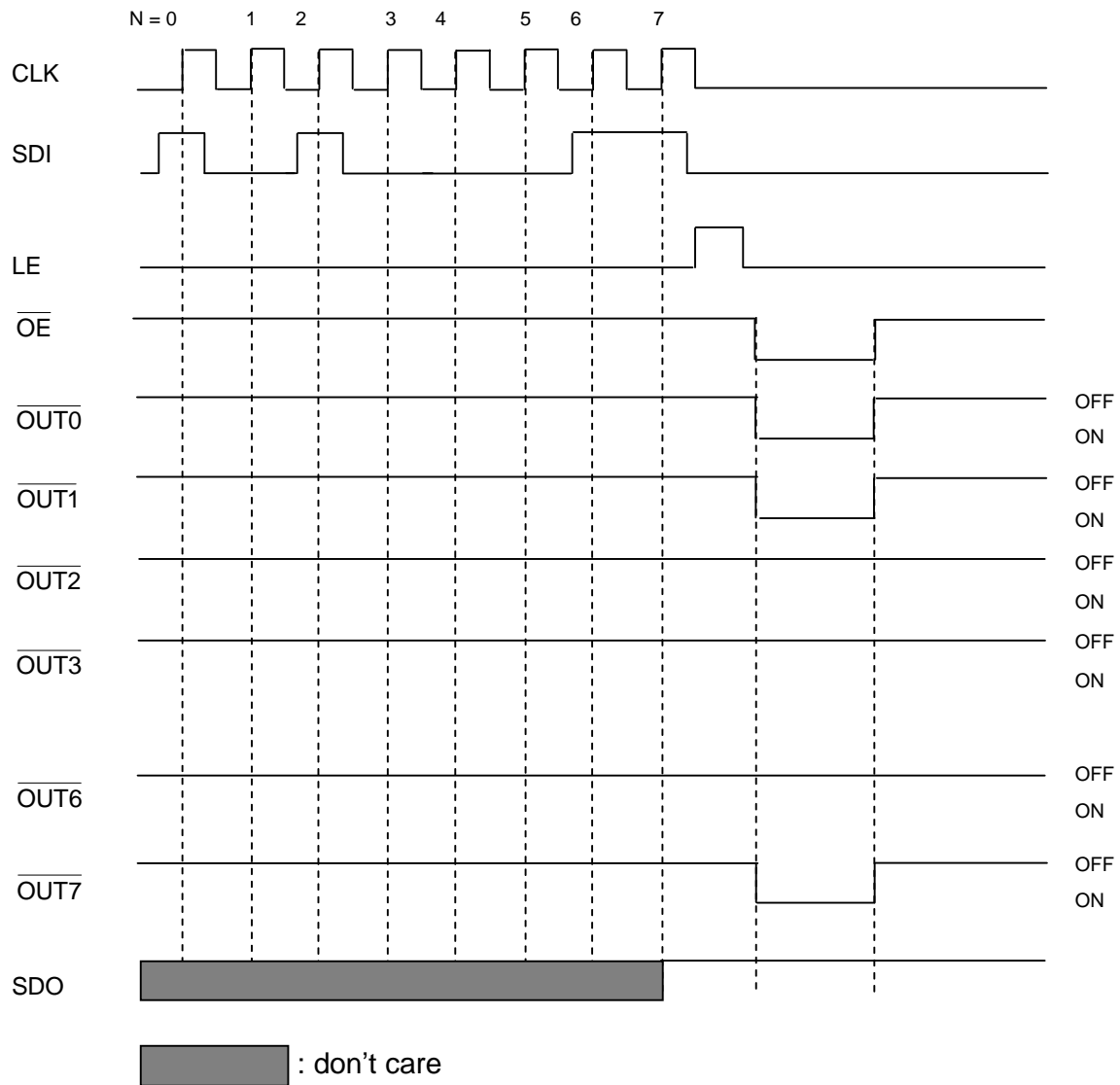
CLK, SDI terminal








SDO terminal



Timing Diagram



Truth Table

CLK	LE	$\overline{\text{OE}}$	SDI	$\overline{\text{OUT0}} \dots \overline{\text{OUT5}} \dots \overline{\text{OUT7}}$	SDO
	H	L	D_n	$\overline{D_n} \dots \overline{D_{n-5}} \dots \overline{D_{n-7}}$	D_{n-7}
	L	L	D_{n+1}	No Change	D_{n-6}
	H	L	D_{n+2}	$\overline{D_{n+2}} \dots \overline{D_{n-3}} \dots \overline{D_{n-5}}$	D_{n-5}
	X	L	D_{n+3}	$\overline{D_{n+2}} \dots \overline{D_{n-3}} \dots \overline{D_{n-5}}$	D_{n-5}
	X	H	D_{n+3}	Off	D_{n-5}

Maximum Ratings

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V_{DD}	0~7.0	V
Input Voltage		V_{IN}	-0.4~ $V_{DD} + 0.4$	V
Output Current		I_{OUT}	+120	mA
Output Voltage		V_{DS}	-0.5~+20.0	V
Clock Frequency		F_{CLK}	25	MHz
GND Terminal Current		I_{GND}	960	mA
Power Dissipation (On PCB, $T_a=25^\circ\text{C}$)	CN – type	P_D	1.64	W
	CD – type		1.06	
	CP – type		0.88	
Thermal Resistance (On PCB, $T_a=25^\circ\text{C}$)	CN – type	$R_{th(j-a)}$	76	$^\circ\text{C/W}$
	CD – type		117	
	CP – type		141	
Operating Temperature		T_{opr}	-40~+85	$^\circ\text{C}$
Storage Temperature		T_{stg}	-55~+150	$^\circ\text{C}$

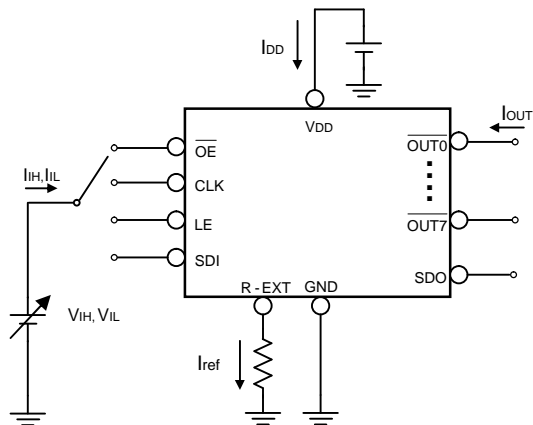
Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
Output Voltage	V_{DS}	$\overline{OUT0} \sim \overline{OUT7}$	-	-	17.0	V
Output Current	I_{OUT}	DC Test Circuit	5	-	120	mA
	I_{OH}	SDO	-	-	-1.0	mA
	I_{OL}	SDO	-	-	1.0	mA
Input Voltage	V_{IH}	CLK, \overline{OE} , LE and SDI	$0.8V_{DD}$	-	$V_{DD}+0.3$	V
	V_{IL}	CLK, \overline{OE} , LE and SDI	-0.3	-	$0.3V_{DD}$	V
LE Pulse Width	$t_{w(L)}$	$V_{DD}=4.5\sim 5.5V$	40	-	-	ns
CLK Pulse Width	$t_{w(CLK)}$		20	-	-	ns
\overline{OE} Pulse Width	$t_{w(OE)}$		400	-	-	ns
Setup Time for SDI	$t_{su(D)}$		5	-	-	ns
Hold Time for SDI	$t_{h(D)}$		10	-	-	ns
Setup Time for LE	$t_{su(L)}$		15	-	-	ns
Hold Time for LE	$t_{h(L)}$		15	-	-	ns
Clock Frequency	F_{CLK}	Cascade Operation	-	-	25.0	MHz
Power Dissipation	P_D	$T_a=85^{\circ}C$	-	-	0.85	W
			-	-	0.55	
			-	-	0.46	

Electrical Characteristics

CHARACTERISTIC		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Input Voltage	“H” level	V _{IH}	Ta = -40~85°C		0.8V _{DD}	-	V _{DD}	V
	“L” level	V _{IL}	Ta = -40~85°C		GND	-	0.3V _{DD}	V
Output Leakage Current		I _{OH}	V _{OH} =17.0V		-	-	0.5	μA
Output Voltage	SDO	V _{OL}	I _{OL} =+1.0mA		-	-	0.4	V
		V _{OH}	I _{OH} =-1.0mA		4.6	-	-	V
Output Current 1		I _{OUT1}	V _{DS} =0.6V	R _{ext} =744 Ω	-	25.0	-	mA
Current Skew		dI _{OUT1}	I _{OUT} =25mA V _{DS} =0.6V	R _{ext} =744 Ω	-	±1	±4	%
Output Current 2		I _{OUT2}	V _{DS} =0.6V	R _{ext} =372 Ω	-	50.0	-	mA
Current Skew		dI _{OUT2}	I _{OUT} =50mA V _{DS} =0.6V	R _{ext} =372 Ω	-	±1	±4	%
Output Current 3		I _{OUT3}	V _{DS} =0.8V	R _{ext} =186 Ω	-	100	-	mA
Current Skew		dI _{OUT3}	I _{OUT} =100mA V _{DS} =0.8V	R _{ext} =186 Ω	-	±1	±6	%
Output Current vs. Output Voltage Regulation		%/dV _{DS}	V _{DS} within 1.0V and 3.0V		-	±0.1	-	% / V
Output Current vs. Supply Voltage Regulation		%/dV _{DD}	V _{DD} within 4.5V and 5.5V		-	±1	-	% / V
Pull-up Resistor		R _{IN} (up)	OE		250	500	800	KΩ
Pull-down Resistor		R _{IN} (down)	LE		250	500	800	KΩ
Supply Current	“OFF”	I _{DD} (off) 1	R _{ext} =Open, OUT0 ~ OUT7 =Off		-	9	-	mA
		I _{DD} (off) 2	R _{ext} =744 Ω, OUT0 ~ OUT7 =Off		-	10	-	
		I _{DD} (off) 3	R _{ext} =372 Ω, OUT0 ~ OUT7 =Off		-	11	-	
	“ON”	I _{DD} (on) 1	R _{ext} =744 Ω, OUT0 ~ OUT7 =On		-	10	-	
		I _{DD} (on) 2	R _{ext} =372 Ω, OUT0 ~ OUT7 =On		-	11	-	

Test Circuit for Electrical Characteristics

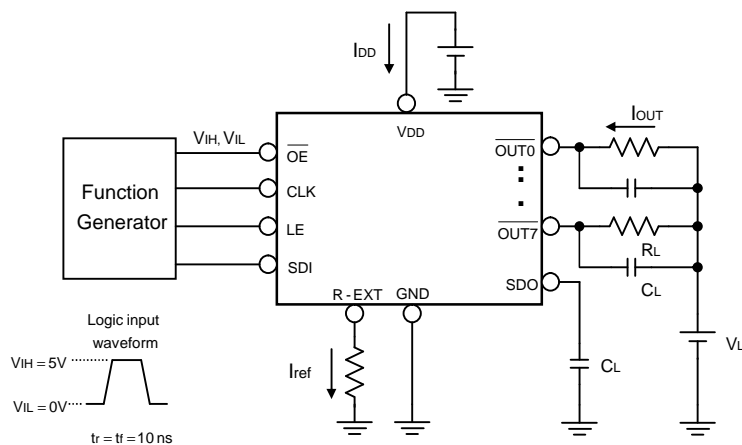


Switching Characteristics

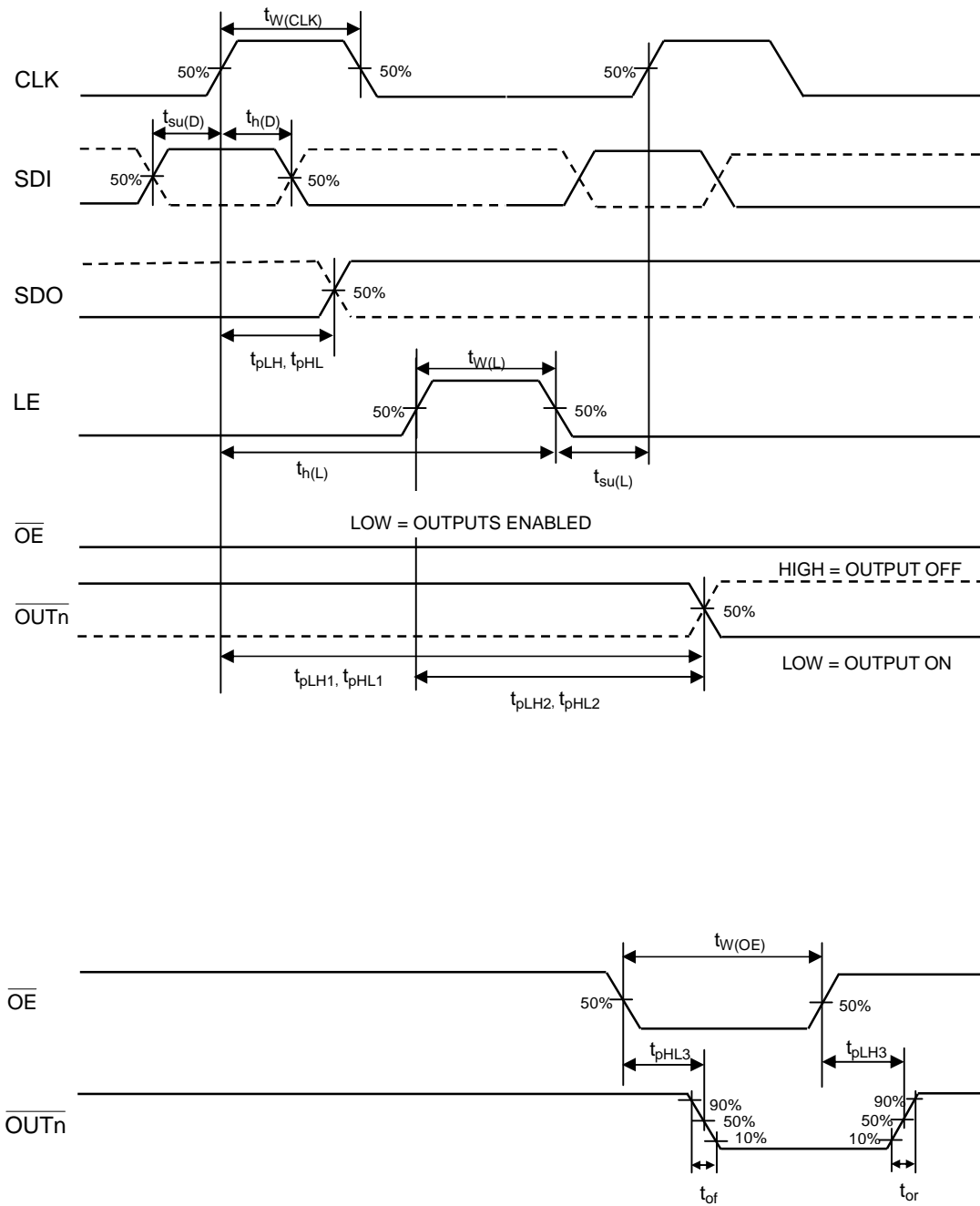
CHARACTERISTIC		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time ("L" to "H")	CLK - $\overline{\text{OUTn}}$	t_{pLH1}	$V_{\text{DD}}=5.0\text{ V}$ $V_{\text{DS}}=0.8\text{ V}$ $V_{\text{IH}}=V_{\text{DD}}$ $V_{\text{IL}}=\text{GND}$ $R_{\text{ext}}=366\ \Omega$ $V_{\text{L}}=4.0\text{ V}$ $R_{\text{L}}=52\ \Omega$ $C_{\text{L}}=10\text{ pF}$	-	50	100	ns
	LE - $\overline{\text{OUTn}}$	t_{pLH2}		-	50	100	ns
	$\overline{\text{OE}}$ - $\overline{\text{OUTn}}$	t_{pLH3}		-	20	100	ns
	CLK - SDO	t_{pLH}		15	20	-	ns
Propagation Delay Time ("H" to "L")	CLK - $\overline{\text{OUTn}}$	t_{pHL1}		-	100	150	ns
	LE - $\overline{\text{OUTn}}$	t_{pHL2}		-	100	150	ns
	$\overline{\text{OE}}$ - $\overline{\text{OUTn}}$	t_{pHL3}		-	50	150	ns
	CLK - SDO	t_{pHL}		15	20	-	ns
Pulse Width	CLK	$t_{\text{w}}(\text{CLK})$		20	-	-	ns
	LE	$t_{\text{w}}(\text{L})$		20	-	-	ns
	$\overline{\text{OE}}$	$t_{\text{w}}(\text{OE})$		400	-	-	ns
Hold Time for LE		$t_{\text{h}}(\text{L})$		5	-	-	ns
Setup Time for LE		$t_{\text{su}}(\text{L})$		5	-	-	ns
Maximum CLK Rise Time		t_{r}^{**}		-	-	500	ns
Maximum CLK Fall Time		t_{f}^{**}		-	-	500	ns
Output Rise Time of Iout		t_{or}		-	70	200	ns
Output Fall Time of Iout		t_{of}		-	40	120	ns

**If the devices are connected in cascade and t_{r} or t_{f} is large, it may be critical to achieve the timing required for data transfer between two cascaded devices.

Test Circuit for Switching Characteristics



Timing Waveform



Application Information

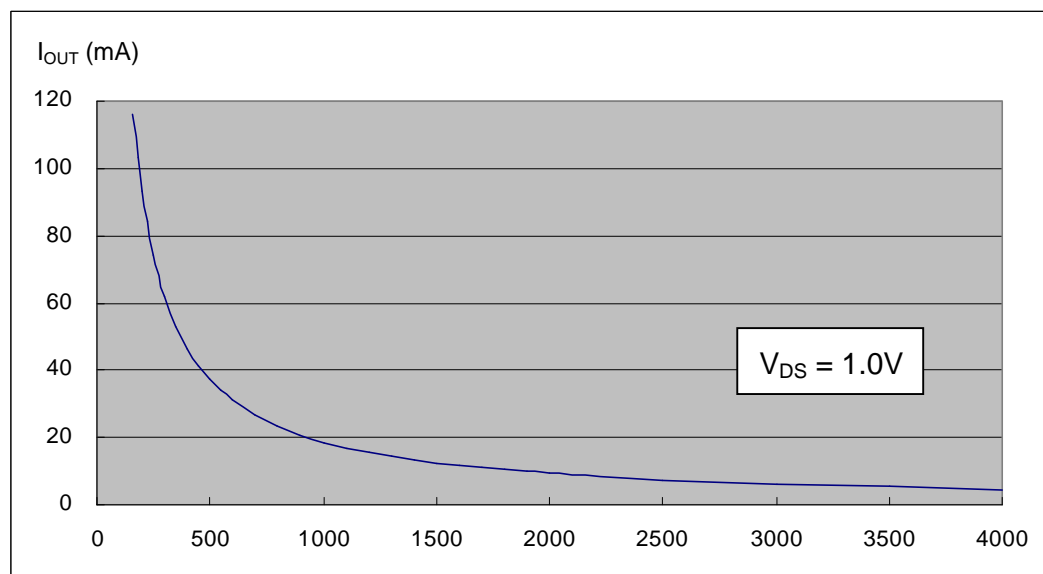
Constant Current

To design LED displays, MBI5168 provides nearly no variations in current from channel to channel and from IC to IC. This can be achieved by:

- 1) While I_{OUT} is $\leq 60\text{mA}$, the maximum current variation between channels is less than $\pm 4\%$, and that between ICs is less than $\pm 6\%$.
- 2) In addition, the current characteristic of output stage is flat and users can refer to the figure as shown below. The output current can be kept constant regardless of the variations of LED forward voltages (V_f). This performs as a perfection of load regulation.

Adjusting Output Current

The output current of each channel (I_{OUT}) is set by an external resistor, R_{ext} . The relationship between I_{OUT} and R_{ext} is shown in the following figure.



Resistance of the external resistor, R_{ext} , in Ω

Also, the output current in milliamps can be calculated from the equation:

I_{OUT} is $(620 / R_{ext}) \times 30$, approximately,

where R_{ext} , in Ω , is the resistance of the external resistor connected to R-EXT terminal.

The magnitude of current (as a function of R_{ext}) is around 50mA at 372 Ω and 25mA at 744 Ω .

Package Power Dissipation (P_D)

The maximum allowable package power dissipation is determined as $P_{D(max)} = (T_j - T_a) / R_{th(j-a)}$. When 16 output channels are turned on simultaneously, the actual package power dissipation is $P_{D(act)} = (I_{DD} \times V_{DD}) + (I_{OUT} \times \text{Duty} \times V_{DS} \times 8)$. Therefore, to keep $P_{D(act)} \leq P_{D(max)}$, the allowable maximum output current as a function of duty cycle is:

$$I_{OUT} = \{ [(T_j - T_a) / R_{th(j-a)}] - (I_{DD} \times V_{DD}) \} / V_{DS} / \text{Duty} / 8,$$

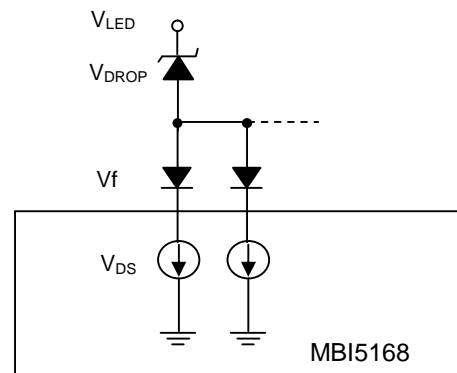
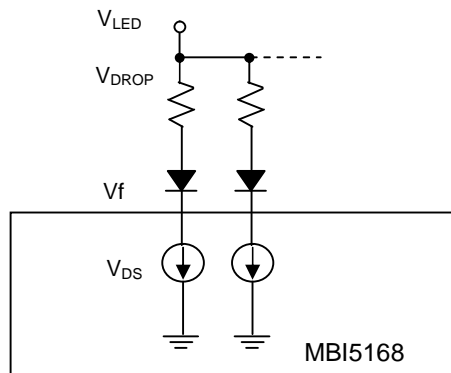
where $T_j = 150^\circ\text{C}$.

Load Supply Voltage (V_{LED})

MBI5168 are designed to operate with V_{DS} ranging from 0.4V to 1.0V considering the package power dissipating limits. V_{DS} may be higher enough to make $P_{D(act)} > P_{D(max)}$ when $V_{LED} = 5\text{V}$ and $V_{DS} = V_{LED} - V_f$, in which V_{LED} is the load supply voltage. In this case, it is recommended to use the lowest possible supply voltage or to set an external voltage reducer, V_{DROP} .

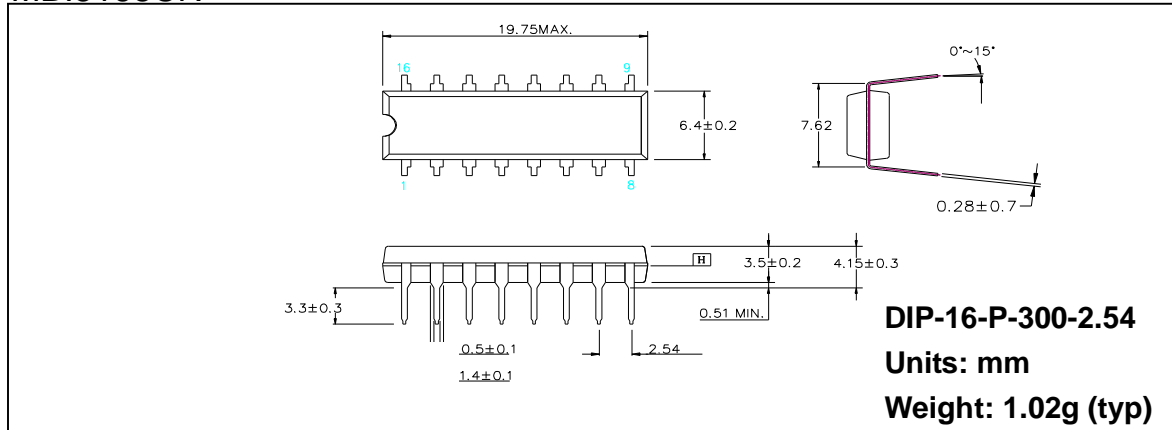
A voltage reducer lets $V_{DS} = (V_{LED} - V_f) - V_{DROP}$.

Resistors or Zener diode can be used in the applications as shown in the following figures.

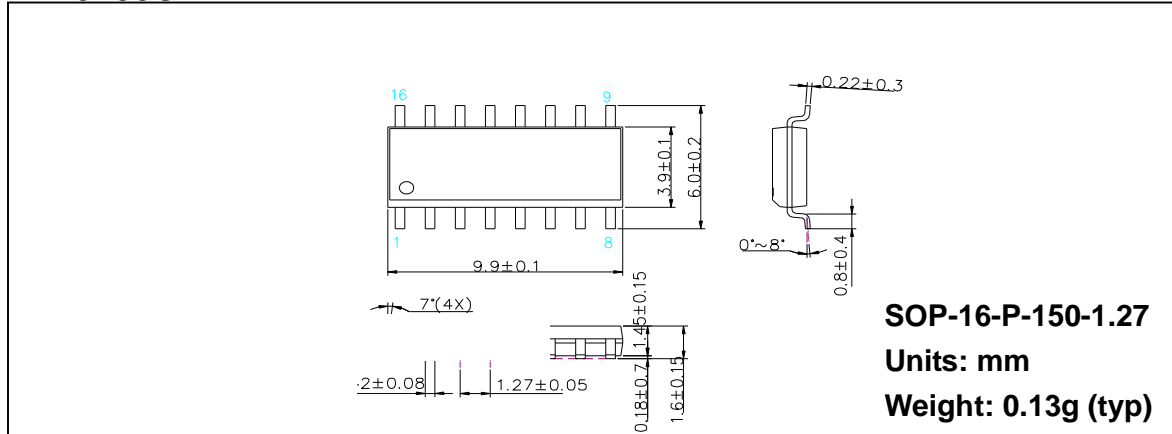


Outline Drawings

MBI5168CN



MBI5168CD



MBI5168CP

