



## DESCRIPTION

The NSL-5532 is a CdS photoconductive cell on a TO-5 ceramic substrate. The photocell is encapsulated with epoxy for moisture resistance.

## FEATURES

- Passive resistance output
- Ceramic package

## RELIABILITY

Contact Luna for recommendations on specific test conditions and procedures.

## APPLICATIONS

- Industrial

## ABSOLUTE MAXIMUM RATINGS

SYMBOL	MIN		MAX	UNITS	(TA)= 23°C UNLESS OTHERWISE NOTED
Voltage (peak AC or DC)	-	-	320	V	-
Power Dissipation @ 25°C <sup>1</sup>	-	-	125	mW	-
Operation Temperature	-60	to	+75	°C	-
Storage Temperature	-60	to	+75	°C	-
Soldering Temperature <sup>2</sup>	-	-	+260	°C	-

### NOTE:

1. Derate linearly to 0 at 75°C
2. >0.08" from case for <5 sec.
3. Cells light adapted at 30 to 50 Ftc for 16 hrs minimum prior to electrical tests

**OPTO-ELECTRICAL PARAMETERS**

$T_a = 23^{\circ}\text{C}$  UNLESS NOTED OTHERWISE

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Light Resistance	2ftc., $2854^{\circ}\text{K}^3$	110	165	220	$\text{K}\Omega$
	100 ftc., $2854^{\circ}\text{K}$	-	10	-	
Dark Resistance	5 sec after removal of test light	11	-	-	$\text{M}\Omega$
Spectral Peak	-	-	550	-	nm

**NOTE:**

1. Derate linearly to 0 at  $75^{\circ}\text{C}$
2.  $>0.08"$  from case for  $<5$  sec.
3. Cells light adapted at 30 to 50 ftc. for 16 hrs. minimum prior to electrical tests.