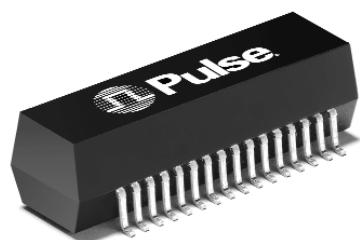


T3/DS3/E3/STS-1 TRANSFORMERS

Eight Transformers Designed for T3, DS3, E3 and STS-1 Interface



- Eight transformers in a surface mount package
- Models matched to leading transceiver ICs
- Return loss meets requirements of G.703
- Recognized by UL1950 (some parts pending approval)
- Isolation voltage: 1500 Vrms

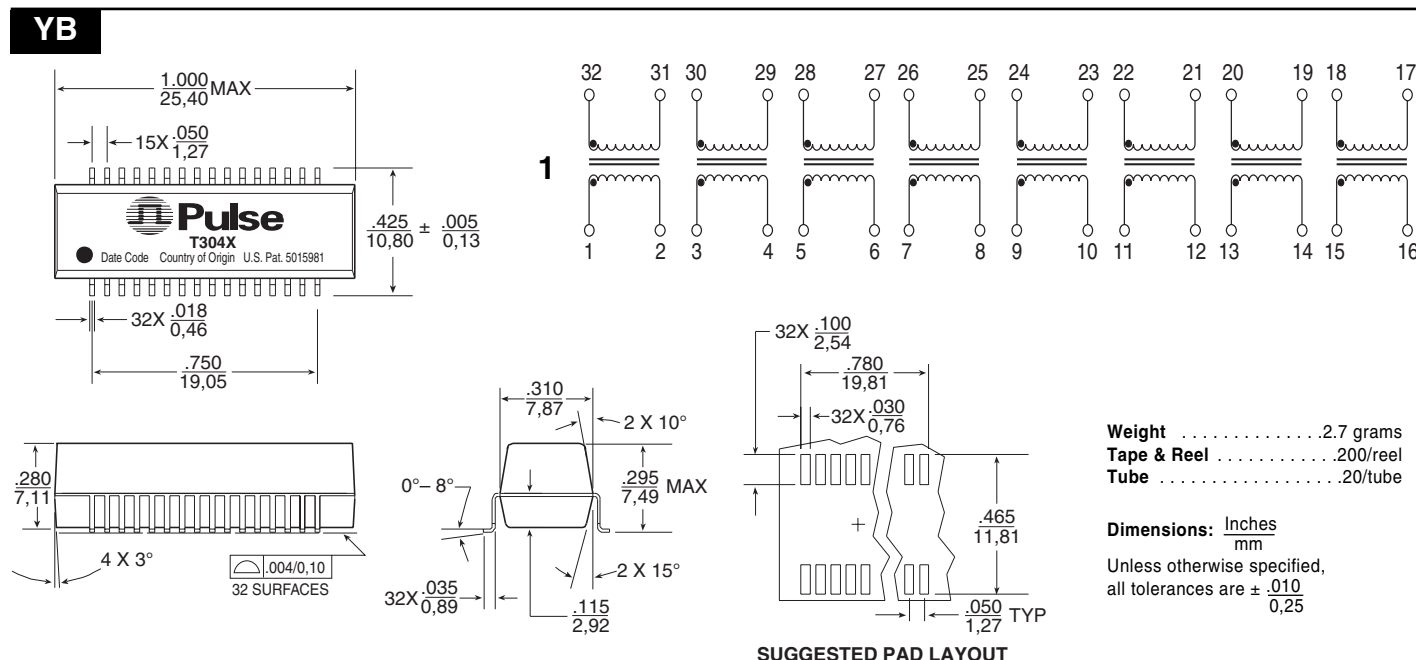
Electrical Specifications @ 25°C

Part Number	Turns Ratio (Pri:Sec ±2%)	OCL Primary @ 25°C (μH MIN)	LL (μH MAX)	C _{ww} (pF MAX)	Isolation Voltage (Vrms MIN)	Package/ Schematic	Primary Pins
T3048	1:1	40	.150	10	1500	YB/1	1-2, 3-4, 5-6, 7-8 9-10, 11-12, 13-14, 15-16
T3049	1:2	19	.150	10	1500	YB/1	1-2, 3-4, 5-6, 7-8 9-10, 11-12, 13-14, 15-16
TX3047*	1:1	100	.150	10	1500	YB/1	1-2, 3-4, 5-6, 7-8 9-10, 11-12, 13-14, 15-16
TX3045*	1:1 & 1:2	100 & 40	.120	10	1500	YB/1	1-2, 5-6, 9-10, 13-14 & 3-4, 7-8, 11-12, 15-16

*NOTE: Operating Temperature Range: -40°C to +85°C

Mechanical

Schematic



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