

TOSHIBA SCHOTTKY BARRIER RECTIFIER SCHOTTKY BARRIER TYPE

U2GWJ44

SWITCHING MODE POWER SUPPLY APPLICATION

- Low Forward Voltage : $V_{FM} = 0.55V$ (Max)
- Average Forward Current : $I_F (AV) = 2.0A$
- Repetitive Peak Reverse Voltage : $V_{RRM} = 40V$
- Surface Mounting Plastic Mold Package

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Repetitive Peak Reverse Voltage	V_{RRM}	40	V
Average Forward Current	$I_F (AV)$	2.0	A
Peak One Cycle Surge Forward Current (Non-Repetitive)	I_{FSM}	40 (50 Hz)	A
Junction Temperature	T_j	$-40 \sim 125$	$^\circ C$
Storage Temperature Range	T_{stg}	$-40 \sim 125$	$^\circ C$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Unit: mm

1. ANODE	
2. CATHODE	
JEDEC	—
JEITA	—
TOSHIBA	3-4D1A

Weight: 0.06 g (typ.)

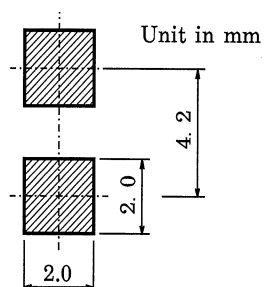
ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ C$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Peak Forward Voltage	V_{FM}	$I_{FM} = 2.0A$	—	—	0.55	V
Repetitive Peak Reverse Current	I_{RRM}	$V_{RRM} = 40V$	—	—	500	μA
Junction Capacitance	C_j	$V_R = 10V, f = 1.0MHz$	—	125	—	pF
Thermal Resistance (Junction to Ambient)	$r_{th (j-a)}$	On ceramic substrate	—	—	60	$^\circ C / W$
		On glass-epoxy substrate	—	—	145	

MARKING

Abbreviation Code	Part No.
2GW	U2GWJ44

STANDARD SOLDERING PAD



Handling Precaution

Schottky barrier diodes have reverse current characteristics compared to other diodes. There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. Please take forward and reverse loss into consideration during design.

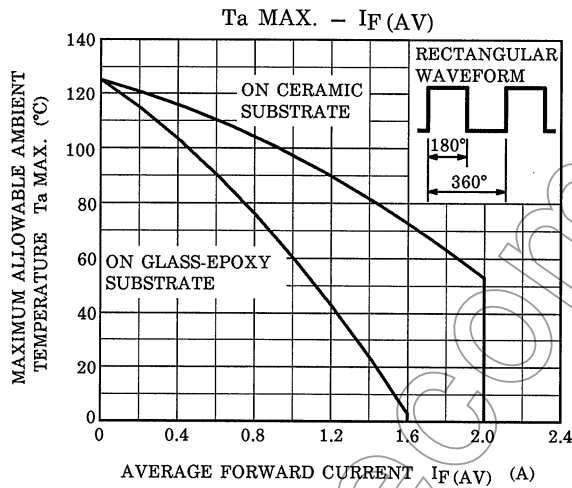
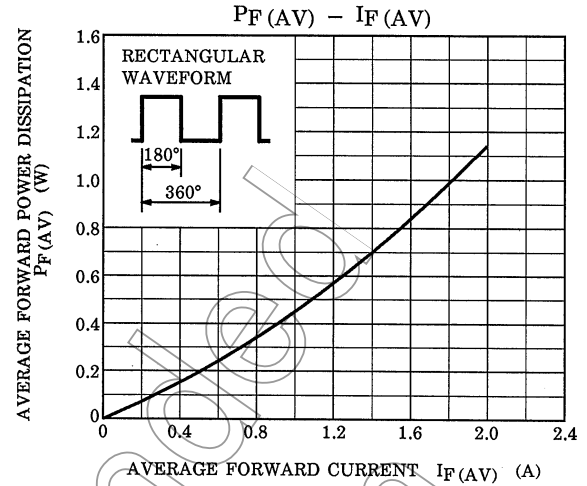
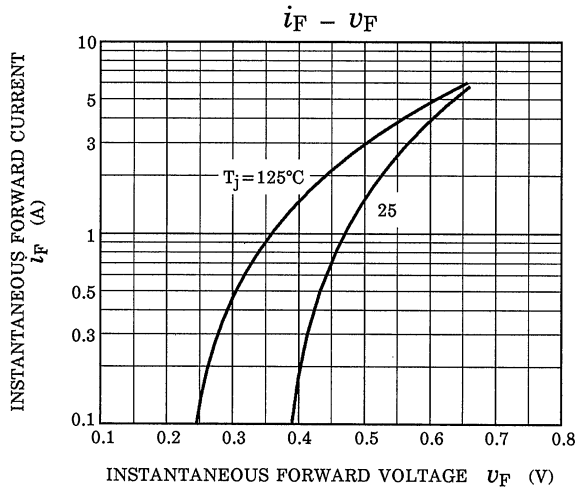
The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

- VRRM:** Use this rating with reference to the above. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.
- IF(AV):** We recommend that the worst case current be no greater than 80% of the absolute maximum rating of IF(AV) and T_j be below 100°C. When using this device, take the margin into consideration by using an allowable T_{max} -IF(AV) curve.
- IFSM:** This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.
- T_j :** Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at a T_j of below 100°C.

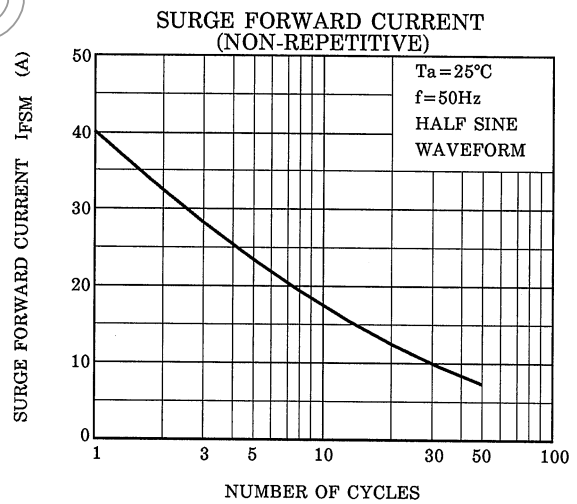
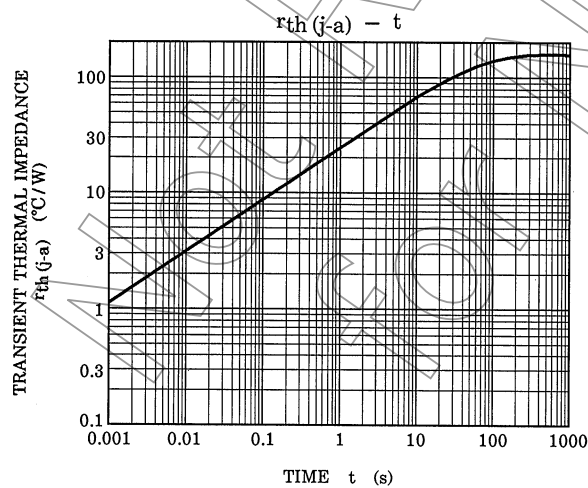
Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

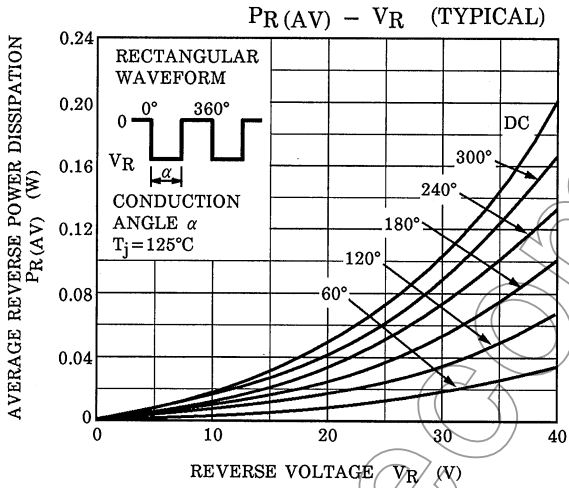
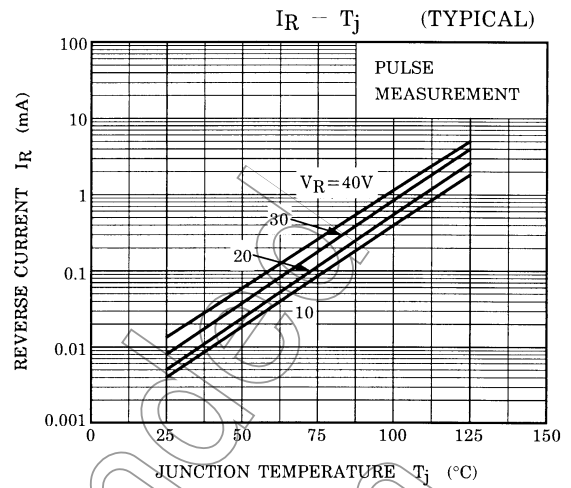
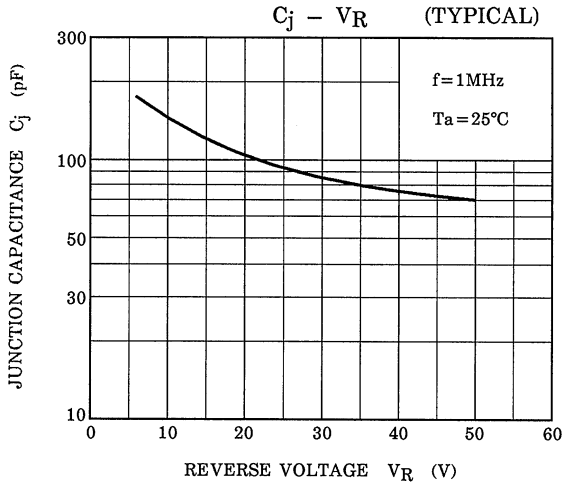
Please refer to the Rectifiers databook for further information.

Not Recommended for New Design



	ON CERAMIC SUBSTRATE	ON GLASS-EPOXY SUBSTRATE
Soldering land : a	2mm×2mm	6mm×6mm
Substrate size : b	50mm×50mm	50mm×50mm
Substrate thickness : c	0.64mm	1.6mm





RESTRICTIONS ON PRODUCT USE

20070701-EN

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