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ADC1410S065F1

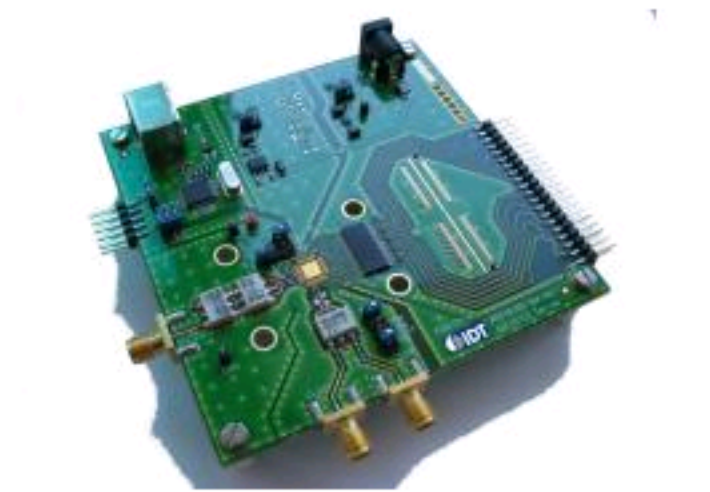
ADC1410S065F1 Demo Board; CMOS

version

IDTs ADC1410S065F1 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance

Features


- CMOS (buffered outputs)
- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals



Product Options

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Documents

	TITLE	TYPE▲	FORMAT	FILE SIZE	DATE
	<a href="#">Demo Board Software ADC_1_r04</a>	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

PRODUCT ID	TITLE
<a href="#">ADC1410S065HN</a>	Single 14-bit ADC 65 Msp/s CMOS or LVDS DDR digital outputs

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