Unit: mm

TOSHIBA Multi-Chip Transistor Silicon NPN & PNP Epitaxial Type

TPC6901

High-Speed Switching Applications MOS Gate Drive Applications

- NPN and PNP transistors are mounted on a compact and slim package.
- High DC current gain: NPN hFE = 400 to 1000

 $: PNP h_{FE} = 200 to 500$

• Low collector-emitter saturation voltage

: NPN V_{CE} (sat) = 0.17 V (max)

: PNP V_{CE} (sat) = 0.23 V (max)

• High-speed switching: NPN tf = 85 ns (typ.)

: PNP $t_f = 70 \text{ ns (typ.)}$

Maximum Ratings (Ta = 25°C)

Characteristics		Cumbal	Rating		Unit	
		Symbol	NPN	PNP	Offic	
Collector-base voltage		V _{CBO}	100	-50	V	
Collector-emitter voltage		V_{CEX}	80	-50	V	
Collector-emitter voltage		V_{CEO}	50	-50	٧	
Emitter-base voltage		V_{EBO}	7	-7	V	
Collector current	DC (Note 1)	Ic	1.0	0.7	Α	
	Pulse (Note 1)	I _{CP}	2.0	-2.0	Α	
Base current		lΒ	0.1	-0.1	Α	
Collector power dissipation (t=10 s) (Note 2)	Single-device operation	P _C (1)	500		mW	
Collector power dissipation (DC) (Note 2)	Single-device operation	P _C (2)	400			
	Single-device value at dual operation	P _C (3)	330		mW	
Thermal resistance, junction to ambient (t=10 s) (Note 2)	Single-device operation	R _{th (j-a)} (1)	250		°C/W	
Thermal resistance, junction to ambient (DC) (Note 2)	Single-device operation	R _{th (j-a)} (2)	312			
	Single-device value at dual operation	R _{th (j-a)} (3)	378		°C/W	
Junction temperature		Tj	150		°C	
Storage temperature range		T _{stg}	–55 to 150		°C	



1. Base 1 (NPN) 4. Collector 2 (PNP) 2. Emitter 2 (PNP) 6. Collector 1 (NPN)

JEDEC —

JEITA —

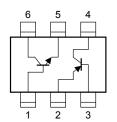
TOSHIBA 2-3T1A

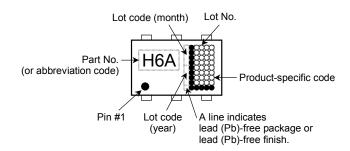
Weight: 0.011 g (typ.)

Note 2:Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Circuit Configuration

Marking





Electrical Characteristics (Ta = 25°C): NPN

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = 100 \text{ V}, I_E = 0$	_	_	100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = 7 \text{ V}, I_{C} = 0$	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	50	_	_	V
DC current gain		h _{FE} (1)	V _{CE} = 2 V, I _C = 0.1 A	400	_	1000	
		h _{FE} (2)	$V_{CE} = 2 \text{ V}, I_{C} = 0.3 \text{ A}$	200	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	0.17	V
Base-emitter saturation voltage		V _{BE (sat)}	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	1.10	V
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	5	_	pF
Switching time	Rise time	t _r	See Figure 1 circuit diagram.	_	35	_	
	Storage time	t _{stg}	$V_{CC} \approx 30 \text{ V}, R_L = 100 \Omega$	_	680	_	ns
	Fall time	t _f	$I_{B1} = -I_{B2} = 10 \text{ mA}$	_	85	_	

Electrical Characteristics (Ta = 25°C): PNP

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -7 \text{ V}, I_{C} = 0$	_	_	-100	nA
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-50	_	_	V
DC current gain		h _{FE} (1)	$V_{CE} = -2 \text{ V}, I_{C} = -0.1 \text{ A}$	200	_	500	
		h _{FE} (2)	$V_{CE} = -2 \text{ V}, I_{C} = -0.3 \text{ A}$	125	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	$I_C = -300 \text{ mA}, I_B = -10 \text{ mA}$	_	_	0.23	V
Base-emitter saturation voltage		V _{BE (sat)}	$I_C = -300 \text{ mA}, I_B = -10 \text{ mA}$	_	_	1.10	V
Collector output capacitance		C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	8	_	pF
Switching time	Rise time	t _r	See Figure 1 circuit diagram.	_	60	_	
	Storage time	t _{stg}	$V_{CC} \approx 30 \text{ V}, R_L = 100 \Omega$	_	280	_	ns
	Fall time	t _f	$I_{B1} = -I_{B2} = -10 \text{ mA}$	_	70	_	

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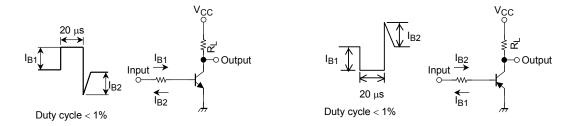
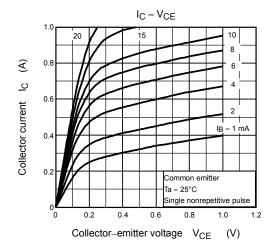
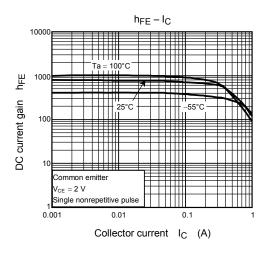


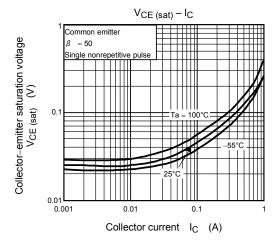
Figure 3 Switching Time Test Circuit & Figure 4 Switching Time Test Circuit & Timing Chart (NPN) Timing Chart (PNP)

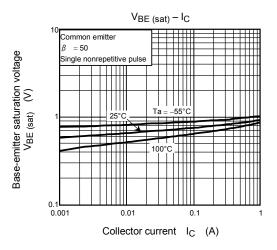
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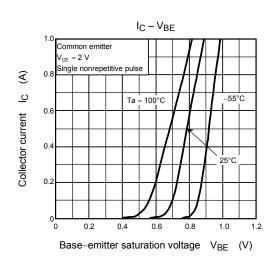
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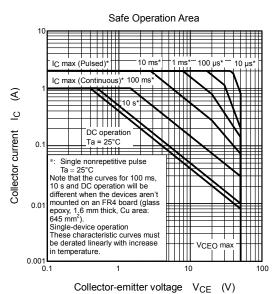




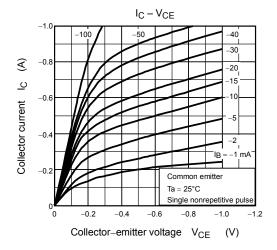


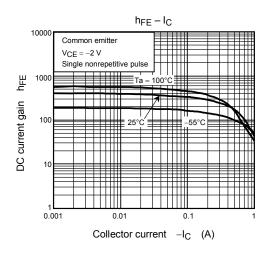


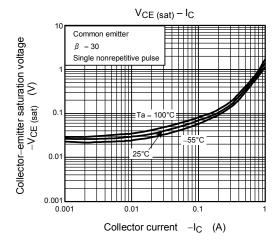


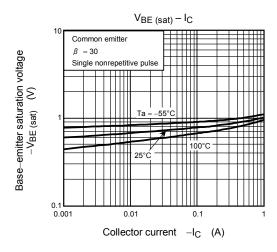


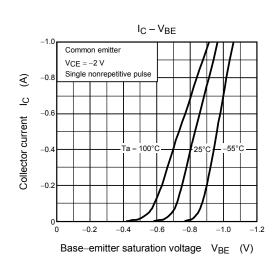
PNP

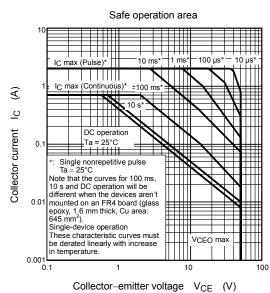






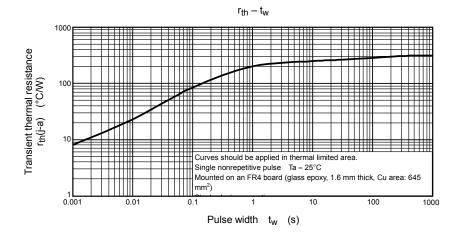




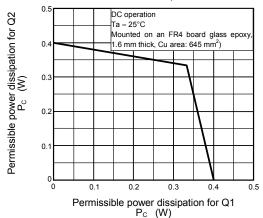


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Common



Permissible Power Dissipation for Simultaneous Operation



Collector power dissipation at the single-device

Collector power dissipation at the single-device value at dual operation is 0.33W.

Collector power dissipation at the dual operation is set to 0.66W.

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Handbook" etc..

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