

# PHOTOCOUPLER PS9122

### 1 Mbps OPEN COLLECTOR OUTPUT TYPE 5-PIN SOP (SO-5) HIGH-SPEED PHOTOCOUPLER

-NEPOC Series-

### **DESCRIPTION**

The PS9122 is an optical coupled high-speed, active low type isolator containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

The PS9122 is a high-speed digital output type photocoupler designed specifically for low circuit current.

The PS9122 is in 5-pin plastic SOP (Small Outline Package) and is suitable for high density application.

#### **FEATURES**

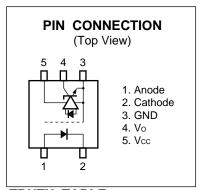
· Supply Voltage

N rank: Vcc = 3.3 VL rank: Vcc = 5 V

- Pulse width distortion ( | tphl tplh | = 200 ns MAX.)
- Small package (SO-5)
- High-speed (1 Mbps)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- · Open collector output
- Embossed tape product: PS9122-F3: 2 500 pcs/reel
- Pb-Free product
- ★ Safety standards
  - UL approved: File No. E72422
  - DIN EN60747-5-2 (VDE0884 Part2) approved No.40008902 (option)

### **APPLICATIONS**

- PoE (Power over Ethernet)
- Measurement equipment
- FA Network

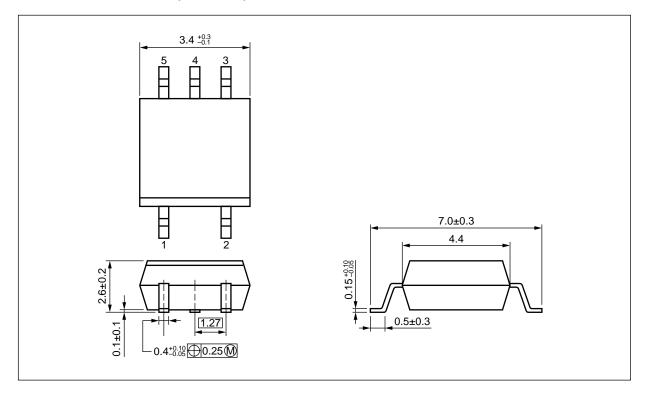


TRUTH TABLE

LED	Output
ON	L
OFF	Н

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

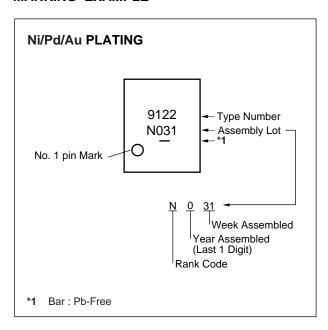
### PACKAGE DIMENSIONS (UNIT: mm)



### **\* PHOTOCOUPLER CONSTRUCTION**

Parameter	Unit (MIN.)
Air Distance	4.2 mm
Outer Creepage Distance	4.2 mm
Isolation Distance	0.2 mm

### **★ MARKING EXAMPLE**



### **\* ORDERING INFORMATION**

Part Number	Order Number	Rank	Solder Plating Specification	Packing Style	Safety Standards Approval	Application *1 Part Number
PS9122	PS9122-AX	N*2 L*3	Pb-Free (Ni/Pd/Au)	20 pcs (Tape 20 pcs cut)	Standard products (UL approved)	PS9122
PS9122-F3	PS9122-F3-AX	N*2 L*3		Embossed Tape 2 500 pcs/reel		
PS9122-V	PS9122-V-AX	N*2 L*3		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2 (VDE0884 Part2)	
PS9122-V-F3	PS9122-V-F3-AX	N*2 L*3		Embossed Tape 2 500 pcs/reel	approved (Option)	

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

<sup>\*2</sup> N rank: Vcc = 3.3 V

<sup>\*3</sup> L rank: Vcc = 5 V

### ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current*1	lF	25	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	Output Current	lo	20	mA
	Power Dissipation*2	Pc	40	mW
Isolation	Voltage <sup>*3</sup>	BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +100	°C
Storage -	Temperature	T <sub>stg</sub>	-55 to +125	°C

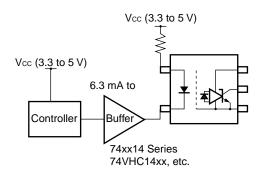
- ★ \*1 Reduced to 0.17 mA/°C at T<sub>A</sub> = 25°C or more.
- \*2 Applies to output pin Vo (collector pin). Reduced to 1.5 mW/°C at TA = 80°C or more.
  - \*3 AC voltage for 1 minute at  $T_A = 25$ °C, RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.

#### RECOMMENDED OPERATING CONDITIONS

Parameter		Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage		V <sub>F</sub> L	0		0.8	V
High Level Input Current		lғн	6.3	10	12.5	mA
Supply Voltage	N rank	Vcc	2.7	3.3	3.6	V
	L rank		4.5	5.0	5.5	
TTL (R <sub>L</sub> = 1 k $\Omega$ , loads)		N			3	
Pull-up Resistor		R∟	330		4 k	Ω

#### **★ DRIVER CIRCUIT**

It is recommended to use some buffer for low output current controller, especially in the case of low Vcc, otherwise to confirm that enough input current is supplied from controller.



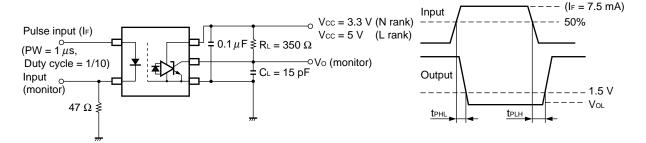
### ELECTRICAL CHARACTERISTICS 1: N rank ( $T_A = -40 \text{ to } +100^{\circ}\text{C}$ , unless otherwise specified)

	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25°C		1.6	1.8	V
	Reverse Current	IR	VR = 3 V, TA = 25°C			10	μА
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		30		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 3.3 V, V <sub>F</sub> = 0.8 V		1	100	μА
	Low Level Output Voltage*2	Vol	Vcc = 3.3 V, I <sub>F</sub> = 5 mA, I <sub>OL</sub> = 10 mA		0.2	0.6	V
	High Level Supply Current	Іссн	Vcc = 3.3 V, I <sub>F</sub> = 0 mA, Vo = Open			2	mA
	Low Level Supply Current	Iccl	Vcc = 3.3 V, I <sub>F</sub> = 10 mA, Vo = Open			3	
Coupled	Threshold Input Current $(H \rightarrow L)$	IFHL	$Vcc = 3.3 \text{ V}, Vo = 0.8 \text{ V}, R_L = 350 \Omega$		2	5	mA
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1 kV <sub>DC</sub> , RH = 40 to 60%, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		0.6		pF
	Propagation Delay Time $(H \rightarrow L)^{*3}$	t <sub>PHL</sub>	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.3 \mbox{ V, RL} = 350 \ \Omega, \mbox{ Ir} = 7.5 \mbox{ mA}, \\ \mbox{VTHHL} = \mbox{VTHLH} = 1.5 \mbox{ V} \end{array}$			500	ns
	Propagation Delay Time (L → H)*3	tрLН				700	
	Rise Time	tr			60		ns
	Fall Time	tr			70		
	Pulse Width Distortion (PWD)*3	tphl-tplh				200	ns
	Common Mode Transient Immunity at High Level Output*4	СМн	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.3 \ \mbox{V}, \ \mbox{R}_L = 350 \ \Omega, \ \mbox{T}_A = 25^{\circ}\mbox{C}, \\ \mbox{I}_F = 0 \ \mbox{mA}, \ \mbox{Vo} > 2.0 \ \mbox{V}, \ \mbox{Vcm} = 1.0 \ \mbox{kV} \end{array}$	15	20		kV/μs
	Common Mode Transient Immunity at Low Level Output*4	CML	$Vcc = 3.3 \text{ V, } R_L = 350  \Omega,  T_A = 25^{\circ}\text{C},$ $I_F = 7.5 \text{ mA, } Vo < 0.8 \text{ V, } V_{CM} = 1.0 \text{ kV}$	15	20		

### ELECTRICAL CHARACTERISTICS 2: L rank (T<sub>A</sub> = -40 to +100°C, unless otherwise specified)

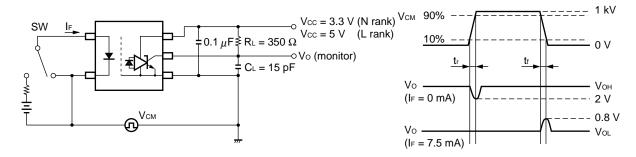
	Parameter	Symbol	Conditions	MIN.	TYP. <sup>*5</sup>	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA, TA = 25°C		1.6	1.8	V
	Reverse Current	IR	VR = 3 V, TA = 25°C			10	μА
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		30		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 5 V, V <sub>F</sub> = 0.8 V		1	100	μA
	Low Level Output Voltage*6	Vol	Vcc = 5 V, I <sub>F</sub> = 5 mA, I <sub>OL</sub> = 13 mA		0.2	0.6	V
	High Level Supply Current	Іссн	Vcc = 5 V, I <sub>F</sub> = 0 mA, Vo = Open			2.5	mA
	Low Level Supply Current	Iccl	Vcc = 5 V, I <sub>F</sub> = 10 mA, Vo = Open			3.5	
Coupled	Threshold Input Current $(H \rightarrow L)$	IFHL	$Vcc = 5 \text{ V}, Vo = 0.8 \text{ V}, R_L = 350 \Omega$		2	5	mA
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1 kV <sub>DC</sub> , RH = 40 to 60%, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		0.6		pF
	Propagation Delay Time $(H \rightarrow L)^{*7}$	<b>t</b> PHL	Vcc = 5 V, RL = 350 $\Omega$ , IF = 7.5 mA, VTHHL = VTHLH = 1.5 V			500	ns
	Propagation Delay Time $(L \rightarrow H)^{+7}$	tрLН				700	
	Rise Time	tr			60		ns
	Fall Time	t <sub>f</sub>			70		
	Pulse Width Distortion (PWD)*7	tphl-tplh				200	ns
	Common Mode Transient Immunity at High Level Output*8	СМн	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 5 \mbox{ V, RL} = 350 \ \Omega, \mbox{ TA} = 25 \mbox{°C}, \\ \mbox{IF} = 0 \mbox{ mA, Vo} > 2.0 \mbox{ V, VcM} = 1.0 \mbox{ kV} \\ \end{array}$	15	20		kV/μ
	Common Mode Transient Immunity at Low Level Output <sup>*8</sup>	CML	$Vcc = 5$ V, $R_L = 350$ Ω, $T_A = 25$ °C, $I_F = 7.5$ mA, $Vo < 0.8$ V, $VcM = 1.0$ kV	15	20		

- \*1, 5. Typical values at  $T_A = 25^{\circ}C$
- \*2, 6. Because VoL of 2 V or more may be output when LED current input and when output supply of Vcc = 2 V more or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- \*3, 7. Test circuit for propagation delay time



Remark C<sub>L</sub> includes probe and stray wiring capacitance.

\*4, 8. Test circuit for common mode transient immunity

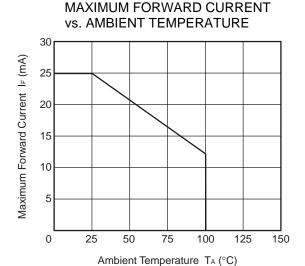


Remark CL includes probe and stray wiring capacitance.

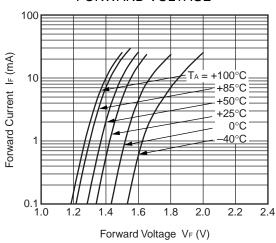
#### **★ USAGE CAUTIONS**

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1  $\mu$ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.

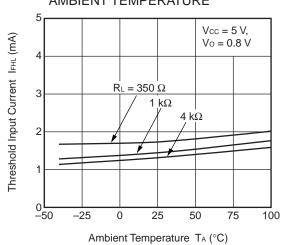
### **★** TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise specified)



### FORWARD CURRENT vs. FORWARD VOLTAGE

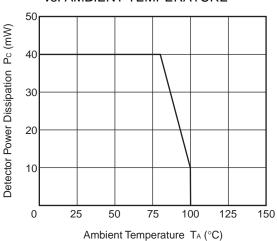


# THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE

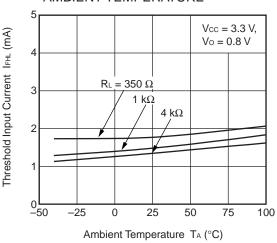


Remark The graphs indicate nominal characteristics.

### DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



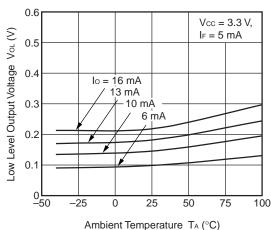
### THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



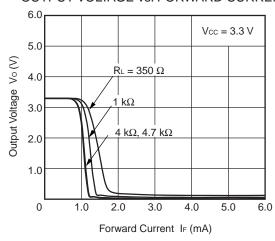
### SUPPLY CURRENT vs. AMBIENT TEMPERATURE 3.0 High Level Supply Current Icch (mA) Low Level Supply Current IccL (mA) Iccl (Vcc = 3.3 V, IF = 10 mA) 2.5 2.0 ICCH ( $Vcc = 3.3 \text{ V}, I_F = 0 \text{ mA}$ ) 1.5 1.0 0.5 0 \_50 25 100 -2575



Ambient Temperature TA (°C)

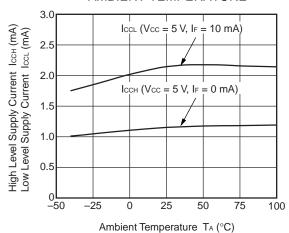


### **OUTPUT VOLTAGE vs. FORWARD CURRENT**

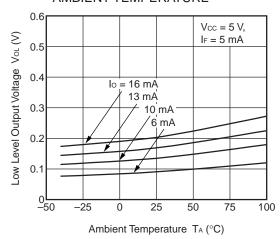


#### Remark The graphs indicate nominal characteristics.

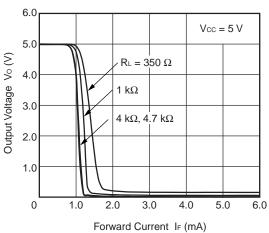
### SUPPLY CURRENT vs. AMBIENT TEMPERATURE



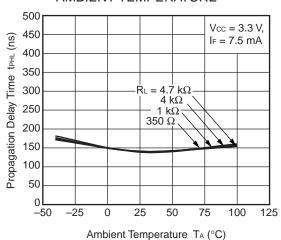
### LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



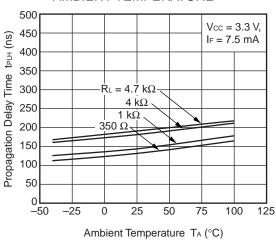
### **OUTPUT VOLTAGE vs. FORWARD CURRENT**



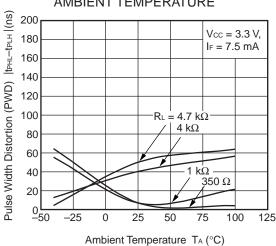
## PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



### PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE

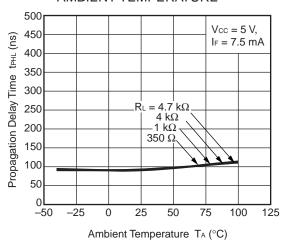


### PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

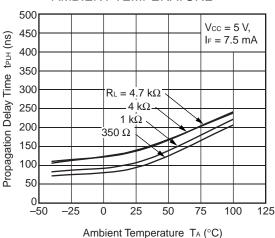


#### **Remark** The graphs indicate nominal characteristics.

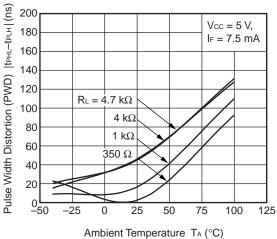
## PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



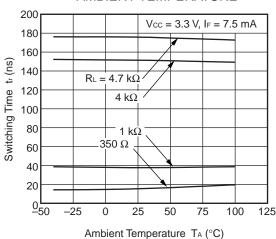
### PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



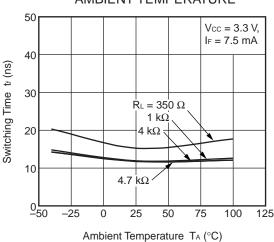
### PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE



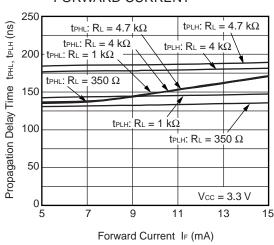




### SWITCHING TIME vs. AMBIENT TEMPERATURE

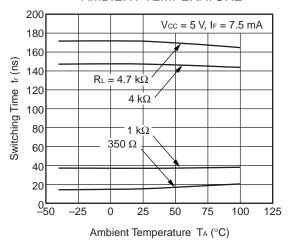


### PROPAGATION DELAY TIME vs. FORWARD CURRENT

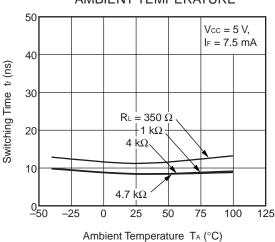


Remark The graphs indicate nominal characteristics.

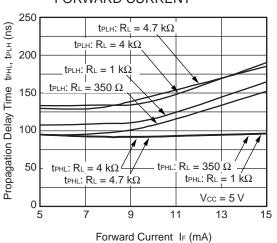
### SWITCHING TIME vs. AMBIENT TEMPERATURE



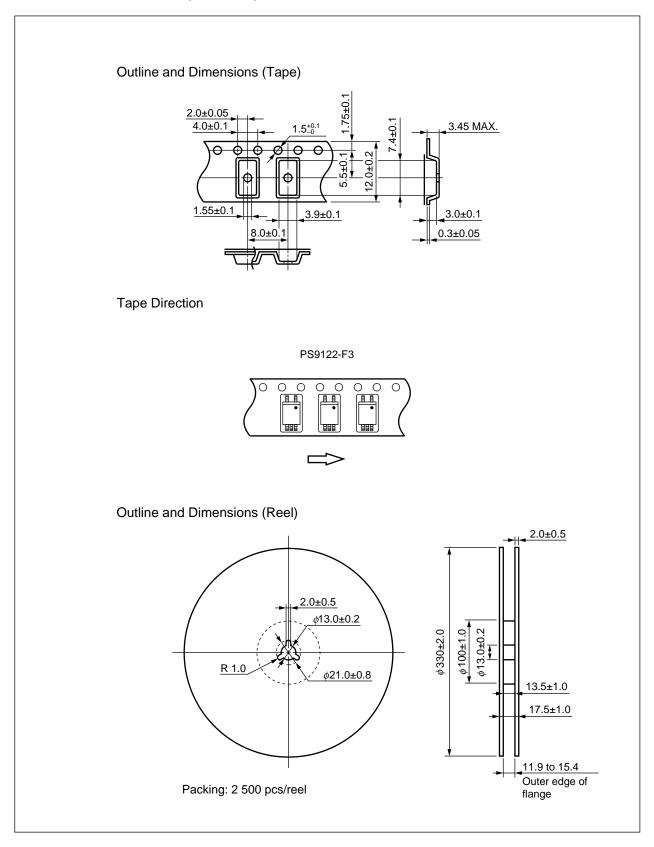
### SWITCHING TIME vs. AMBIENT TEMPERATURE



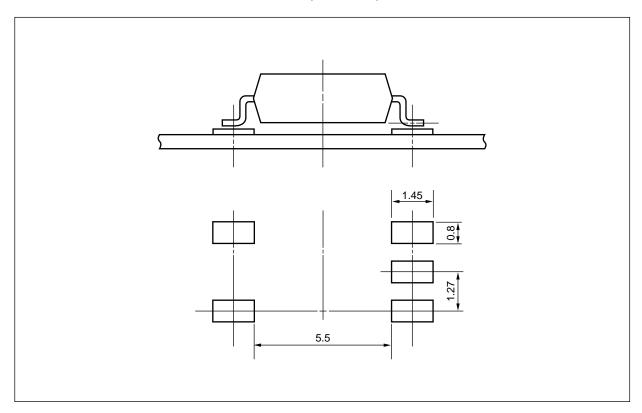
### PROPAGATION DELAY TIME vs. FORWARD CURRENT



### TAPING SPECIFICATIONS (UNIT: mm)



### \* RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



#### **NOTES ON HANDLING**

#### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

Peak reflow temperature
 260°C or below (package surface temperature)

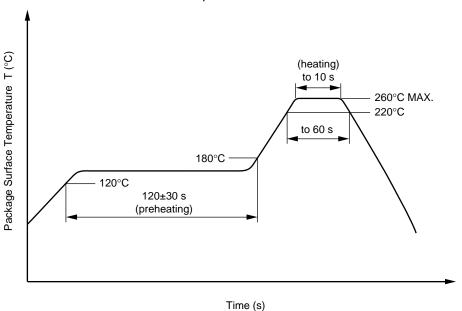
Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

Number of times
 One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

#### (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

Flux
 Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

### (4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

### \* SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM},  P_d < 5  pC$	Uiorm Upr	707 1 061	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM},  P_d < 5 \; pC$	Upr	1 326	V <sub>peak</sub>
Highest permissible overvoltage	Utr	6 000	V <sub>peak</sub>
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +100	°C
Isolation resistance, minimum value  VIO = 500 V dc at TA = 25°C  VIO = 500 V dc at TA MAX. at least 100°C	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)  Package temperature  Current (input current I <sub>F</sub> , Psi = 0)  Power (output or total power dissipation)  Isolation resistance	Tsi Isi Psi	150 200 300	°C mA mW
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

#### Caution

**GaAs Products** 

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

#### **NOTICE**

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
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