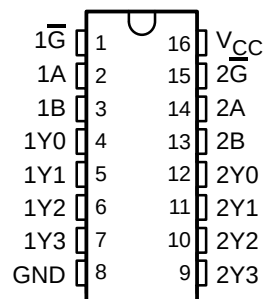


# SN54AHCT139, SN74AHCT139 DUAL 2-LINE TO 4-LINE DECODERS/DEMULTIPLEXERS

SCLS267M – DECEMBER 1995 – REVISED MARCH 2003

- Inputs Are TTL-Voltage Compatible
- Designed Specifically for High-Speed Memory Decoders and Data-Transmission Systems
- Incorporate Two Enable Inputs to Simplify Cascading and/or Data Reception
- Latch-Up Performance Exceeds 250 mA Per JESD 17
- ESD Protection Exceeds JESD 22
  - 2000-V Human-Body Model (A114-A)
  - 200-V Machine Model (A115-A)
  - 1000-V Charged-Device Model (C101)

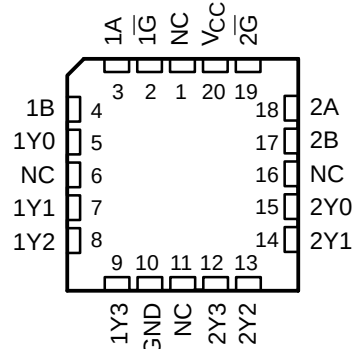
SN54AHCT139 . . . J OR W PACKAGE  
SN74AHCT139 . . . D, DB, DGV, N, NS, OR PW PACKAGE  
(TOP VIEW)



## description/ordering information

The 'AHCT139 devices are dual 2-line to 4-line decoders/demultiplexers designed for 4.5-V to 5.5-V  $V_{CC}$  operation. These devices are designed to be used in high-performance memory-decoding or data-routing applications requiring very short propagation delay times. In high-performance memory systems, these decoders can be used to minimize the effects of system decoding. When used with high-speed memories utilizing a fast enable circuit, the delay times of these decoders and the enable time of the memory usually are less than the typical access time of the memory. This means that the effective system delay introduced by the decoders is negligible.

SN54AHCT139 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

## ORDERING INFORMATION

| $T_A$          | PACKAGE†    |               | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|----------------|-------------|---------------|-----------------------|------------------|
| –40°C to 85°C  | PDIP – N    | Tube          | SN74AHCT139N          | SN74AHCT139N     |
|                | SOIC – D    | Tube          | SN74AHCT139D          | AHCT139          |
|                |             | Tape and reel | SN74AHCT139DR         |                  |
|                | SOP – NS    | Tape and reel | SN74AHCT139NSR        | AHCT139          |
|                | SSOP – DB   | Tape and reel | SN74AHCT139DBR        | HB139            |
|                | TSSOP – PW  | Tube          | SN74AHCT139PW         | HB139            |
|                |             | Tape and reel | SN74AHCT139PWR        |                  |
|                | TVSOP – DGV | Tape and reel | SN74AHCT139DGVR       | HB139            |
| –55°C to 125°C | CDIP – J    | Tube          | SNJ54AHCT139J         | SNJ54AHCT139J    |
|                | CFP – W     | Tube          | SNJ54AHCT139W         | SNJ54AHCT139W    |
|                | LCCC – FK   | Tube          | SNJ54AHCT139FK        | SNJ54AHCT139FK   |

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



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UNLESS OTHERWISE NOTED this document contains PRODUCTION DATA information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS  
INSTRUMENTS**

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SN54AHCT139, SN74AHCT139  
DUAL 2-LINE TO 4-LINE DECODERS/DEMULTIPLEXERS

SCLS267M – DECEMBER 1995 – REVISED MARCH 2003

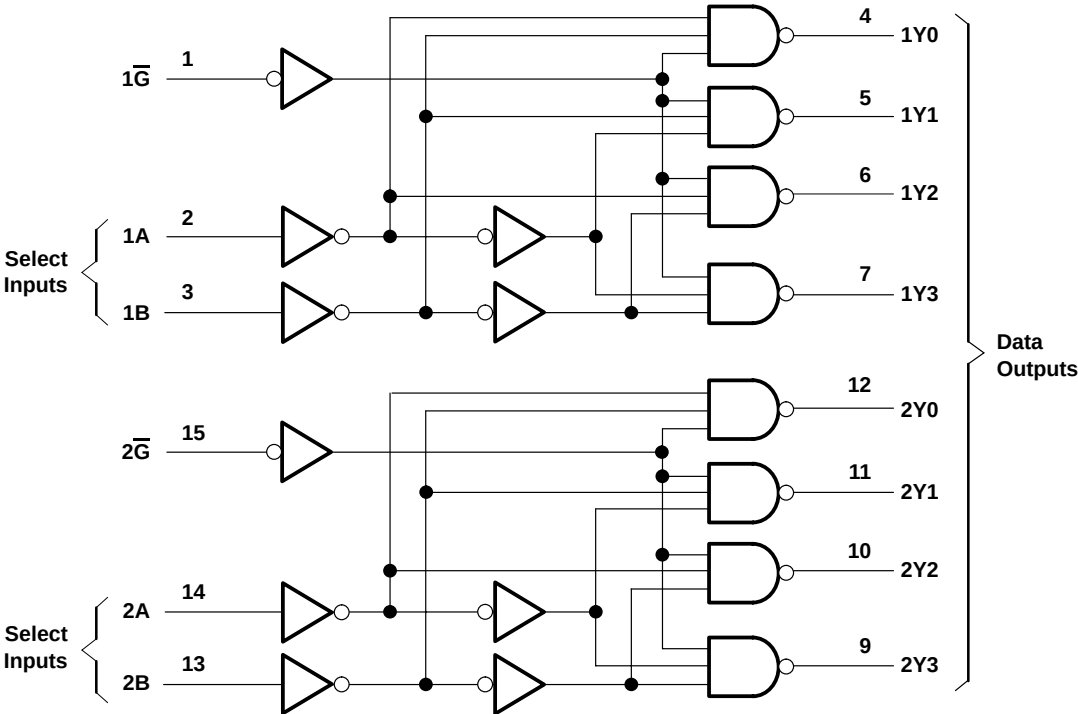
description/ordering information (continued)

The active-low enable ( $\overline{G}$ ) input can be used as a data line in demultiplexing applications. These decoders/demultiplexers feature fully buffered inputs, each of which represents only one normalized load to its driving circuit.

FUNCTION TABLE  
(each decoder/demultiplexer)

| $\overline{G}$ | INPUTS |   | OUTPUTS |    |    |    |
|----------------|--------|---|---------|----|----|----|
|                | SELECT |   | Y0      | Y1 | Y2 | Y3 |
|                | B      | A |         |    |    |    |
| H              | X      | X | H       | H  | H  | H  |
| L              | L      | L | L       | H  | H  | H  |
| L              | L      | H | H       | L  | H  | H  |
| L              | H      | L | H       | H  | L  | H  |
| L              | H      | H | H       | H  | H  | L  |

logic diagram (positive logic)



Pin numbers shown are for the D, DB, DGV, J, N, NS, PW, and W packages.

# SN54AHCT139, SN74AHCT139 DUAL 2-LINE TO 4-LINE DECODERS/DEMULTIPLEXERS

SCLS267M – DECEMBER 1995 – REVISED MARCH 2003

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                                  |                            |
|------------------------------------------------------------------|----------------------------|
| Supply voltage range, $V_{CC}$                                   | –0.5 V to 7 V              |
| Input voltage range, $V_I$ (see Note 1)                          | –0.5 V to 7 V              |
| Output voltage range, $V_O$ (see Note 1)                         | –0.5 V to $V_{CC} + 0.5$ V |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ )                      | –20 mA                     |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ )   | ±20 mA                     |
| Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )       | ±25 mA                     |
| Continuous current through $V_{CC}$ or GND                       | ±75 mA                     |
| Package thermal impedance, $\theta_{JA}$ (see Note 2): D package | 73°C/W                     |
| DB package                                                       | 82°C/W                     |
| DGV package                                                      | 120°C/W                    |
| N package                                                        | 67°C/W                     |
| NS package                                                       | 64°C/W                     |
| PW package                                                       | 108°C/W                    |
| Storage temperature range, $T_{stg}$                             | –65°C to 150°C             |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
2. The package thermal impedance is calculated in accordance with JESD 51-7.

## recommended operating conditions (see Note 3)

|                                                        | SN54AHCT139 |          | SN74AHCT139 |          | UNIT |
|--------------------------------------------------------|-------------|----------|-------------|----------|------|
|                                                        | MIN         | MAX      | MIN         | MAX      |      |
| $V_{CC}$ Supply voltage                                | 4.5         | 5.5      | 4.5         | 5.5      | V    |
| $V_{IH}$ High-level input voltage                      | 2           |          | 2           |          | V    |
| $V_{IL}$ Low-level input voltage                       |             | 0.8      |             | 0.8      | V    |
| $V_I$ Input voltage                                    | 0           | 5.5      | 0           | 5.5      | V    |
| $V_O$ Output voltage                                   | 0           | $V_{CC}$ | 0           | $V_{CC}$ | V    |
| $I_{OH}$ High-level output current                     |             | –8       |             | –8       | mA   |
| $I_{OL}$ Low-level output current                      |             | 8        |             | 8        | mA   |
| $\Delta t/\Delta v$ Input transition rise or fall rate |             | 20       |             | 20       | ns/V |
| $T_A$ Operating free-air temperature                   | –55         | 125      | –40         | 85       | °C   |

NOTE 3: All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

# SN54AHCT139, SN74AHCT139

## DUAL 2-LINE TO 4-LINE DECODERS/DEMULTIPLEXERS

SCLS267M – DECEMBER 1995 – REVISED MARCH 2003

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

| PARAMETER                     | TEST CONDITIONS                                               | V <sub>CC</sub> | T <sub>A</sub> = 25°C |     |      | SN54AHCT139 |      | SN74AHCT139 |      | UNIT |
|-------------------------------|---------------------------------------------------------------|-----------------|-----------------------|-----|------|-------------|------|-------------|------|------|
|                               |                                                               |                 | MIN                   | TYP | MAX  | MIN         | MAX  | MIN         | MAX  |      |
| V <sub>OH</sub>               | I <sub>OH</sub> = -50 µA                                      | 4.5 V           | 4.4                   | 4.5 |      | 4.4         |      | 4.4         |      | V    |
|                               | I <sub>OH</sub> = -8 mA                                       |                 | 3.94                  |     |      | 3.8         |      | 3.8         |      |      |
| V <sub>OL</sub>               | I <sub>OL</sub> = 50 µA                                       | 4.5 V           |                       |     | 0.1  |             | 0.1  |             | 0.1  | V    |
|                               | I <sub>OL</sub> = 8 mA                                        |                 |                       |     | 0.36 |             | 0.44 |             | 0.44 |      |
| I <sub>I</sub>                | V <sub>I</sub> = 5.5 V or GND                                 | 0 V to 5.5 V    |                       |     | ±0.1 |             | ±1*  |             | ±1   | µA   |
| I <sub>CC</sub>               | V <sub>I</sub> = V <sub>CC</sub> or GND, I <sub>O</sub> = 0   | 5.5 V           |                       |     | 2    |             | 20   |             | 20   | µA   |
| ΔI <sub>CC</sub> <sup>†</sup> | One input at 3.4 V,<br>Other inputs at V <sub>CC</sub> or GND | 5.5 V           |                       |     | 1.35 |             | 1.5  |             | 1.5  | mA   |
| C <sub>i</sub>                | V <sub>I</sub> = V <sub>CC</sub> or GND                       | 5 V             |                       | 2   | 10   |             |      |             | 10   | pF   |

\* On products compliant to MIL-PRF-38535, this parameter is not production tested at V<sub>CC</sub> = 0 V.

† This is the increase in supply current for each input at one of the specified TTL voltage levels rather than 0 V or V<sub>CC</sub>.

**switching characteristics over recommended operating free-air temperature range,  
V<sub>CC</sub> = 5 V ± 0.5 V (unless otherwise noted) (see Figure 1)**

| PARAMETER        | FROM<br>(INPUT) | TO<br>(OUTPUT) | LOAD<br>CAPACITANCE    | T <sub>A</sub> = 25°C |       |     | SN54AHCT139 |       | SN74AHCT139 |      | UNIT |
|------------------|-----------------|----------------|------------------------|-----------------------|-------|-----|-------------|-------|-------------|------|------|
|                  |                 |                |                        | MIN                   | TYP   | MAX | MIN         | MAX   | MIN         | MAX  |      |
| t <sub>PLH</sub> | A or B          | Y              | C <sub>L</sub> = 15 pF | 5**                   | 7.2** |     | 1**         | 8.5** | 1           | 8.5  | ns   |
| t <sub>PHL</sub> |                 |                |                        | 5**                   | 7.2** |     | 1**         | 8.5** | 1           | 8.5  |      |
| t <sub>PLH</sub> | $\overline{G}$  | Y              | C <sub>L</sub> = 15 pF | 4.4**                 | 6.3** |     | 1**         | 7.5** | 1           | 7.5  | ns   |
| t <sub>PHL</sub> |                 |                |                        | 4.4**                 | 6.3** |     | 1**         | 7.5** | 1           | 7.5  |      |
| t <sub>PLH</sub> | A or B          | Y              | C <sub>L</sub> = 50 pF | 6.5                   | 9.2   |     | 1           | 10.5  | 1           | 10.5 | ns   |
| t <sub>PHL</sub> |                 |                |                        | 6.5                   | 9.2   |     | 1           | 10.5  | 1           | 10.5 |      |
| t <sub>PLH</sub> | $\overline{G}$  | Y              | C <sub>L</sub> = 50 pF | 5.9                   | 8.3   |     | 1           | 9.5   | 1           | 9.5  | ns   |
| t <sub>PHL</sub> |                 |                |                        | 5.9                   | 8.3   |     | 1           | 9.5   | 1           | 9.5  |      |

\*\* On products compliant to MIL-PRF-38535, this parameter is not production tested.

**operating characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C**

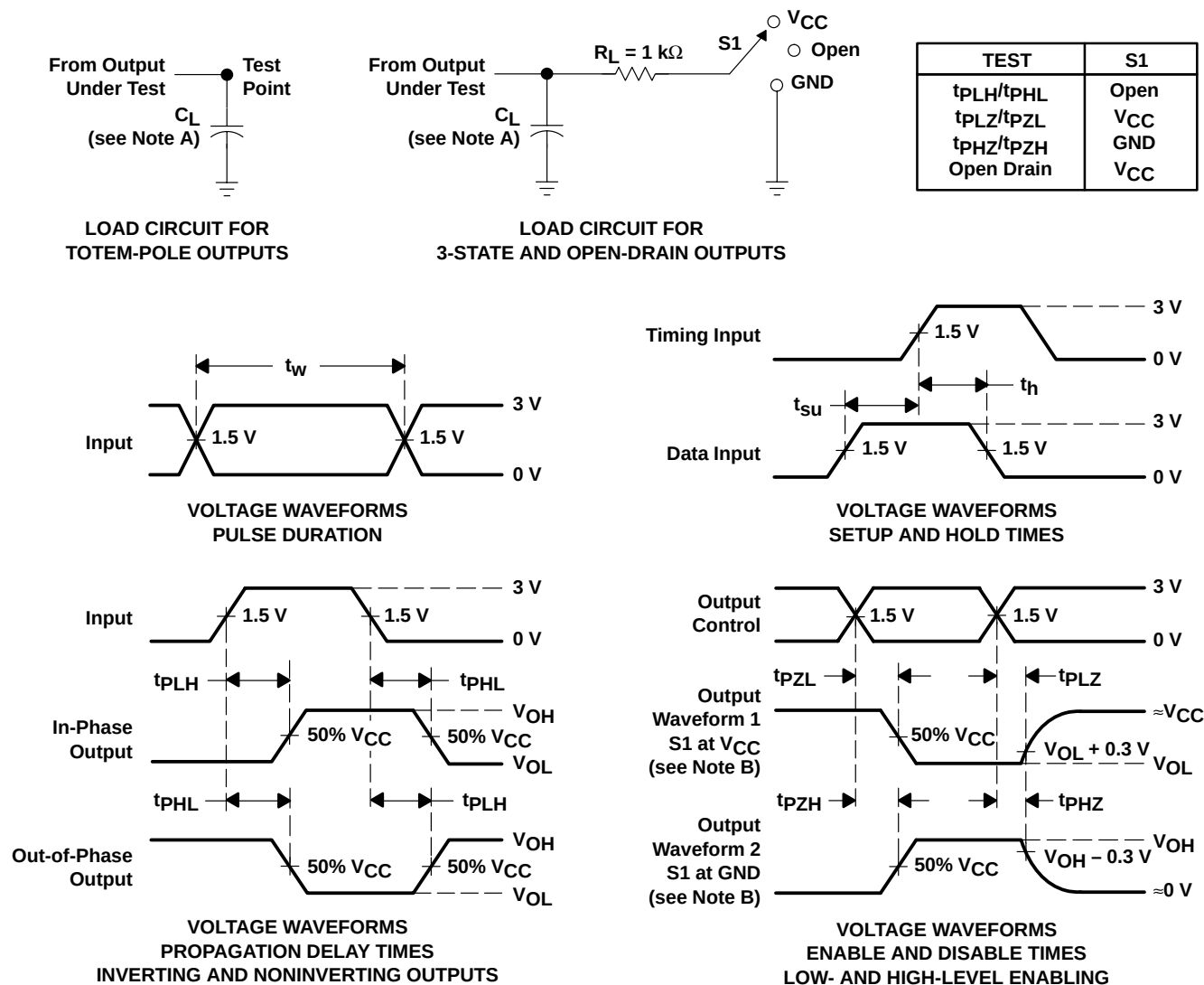
| PARAMETER                                     | TEST CONDITIONS    | TYP | UNIT |
|-----------------------------------------------|--------------------|-----|------|
| C <sub>pd</sub> Power dissipation capacitance | No load, f = 1 MHz | 13  | pF   |

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.



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### PARAMETER MEASUREMENT INFORMATION



- NOTES: A.  $C_L$  includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq 1\text{ MHz}$ ,  $Z_O = 50\ \Omega$ ,  $t_r \leq 3\text{ ns}$ ,  $t_f \leq 3\text{ ns}$ .
- D. The outputs are measured one at a time with one input transition per measurement.

**Figure 1. Load Circuit and Voltage Waveforms**

## PACKAGING INFORMATION

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| SN74AHCT139D     | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | AHCT139                 | <a href="#">Samples</a> |
| SN74AHCT139DBLE  | OBSOLETE      | SSOP         | DB                 | 16   |                | TBD                        | Call TI                 | Call TI              | -40 to 85    |                         |                         |
| SN74AHCT139DBR   | ACTIVE        | SSOP         | DB                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | HB139                   | <a href="#">Samples</a> |
| SN74AHCT139DG4   | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | AHCT139                 | <a href="#">Samples</a> |
| SN74AHCT139DGVR  | ACTIVE        | TVSOP        | DGV                | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | HB139                   | <a href="#">Samples</a> |
| SN74AHCT139DR    | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | AHCT139                 | <a href="#">Samples</a> |
| SN74AHCT139N     | ACTIVE        | PDIP         | N                  | 16   | 25             | Pb-Free<br>(RoHS)          | CU NIPDAU               | N / A for Pkg Type   | -40 to 85    | SN74AHCT139N            | <a href="#">Samples</a> |
| SN74AHCT139NE4   | ACTIVE        | PDIP         | N                  | 16   | 25             | Pb-Free<br>(RoHS)          | CU NIPDAU               | N / A for Pkg Type   | -40 to 85    | SN74AHCT139N            | <a href="#">Samples</a> |
| SN74AHCT139PW    | ACTIVE        | TSSOP        | PW                 | 16   | 90             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | HB139                   | <a href="#">Samples</a> |
| SN74AHCT139PWLE  | OBSOLETE      | TSSOP        | PW                 | 16   |                | TBD                        | Call TI                 | Call TI              | -40 to 85    |                         |                         |
| SN74AHCT139PWR   | ACTIVE        | TSSOP        | PW                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | HB139                   | <a href="#">Samples</a> |
| SN74AHCT139PWRE4 | ACTIVE        | TSSOP        | PW                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | HB139                   | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

---

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

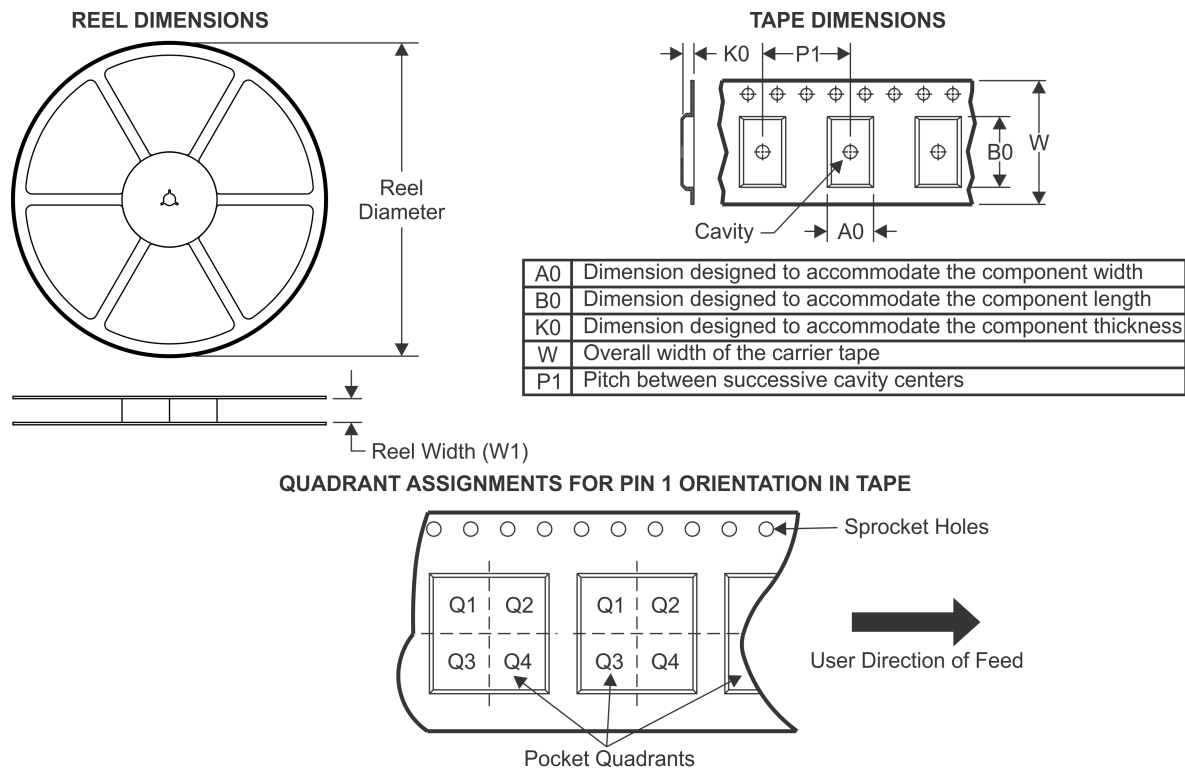
<sup>(4)</sup> There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

<sup>(5)</sup> Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

<sup>(6)</sup> Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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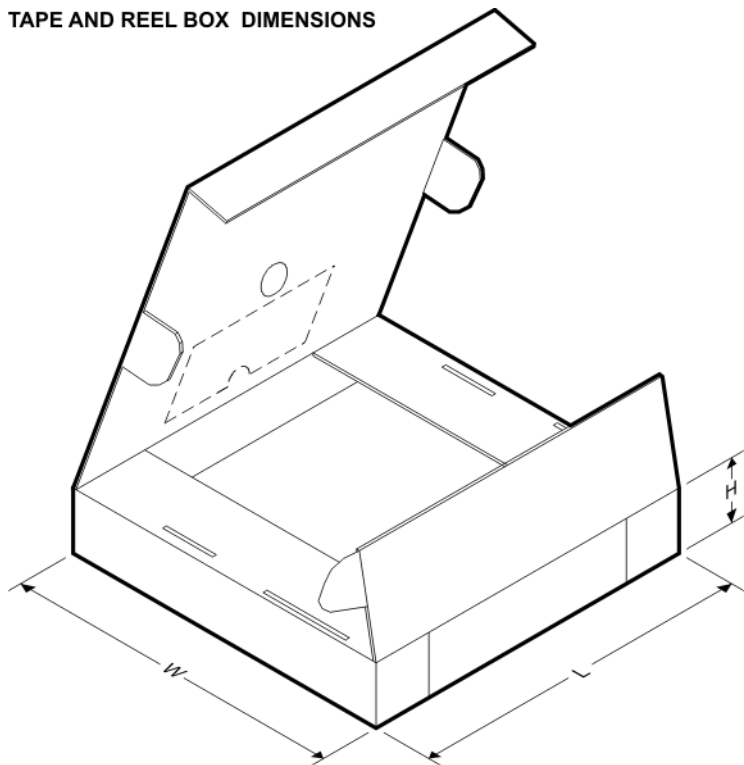
**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device          | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74AHCT139DBR  | SSOP         | DB              | 16   | 2000 | 330.0              | 16.4               | 8.2     | 6.6     | 2.5     | 12.0    | 16.0   | Q1            |
| SN74AHCT139DGVR | TVSOP        | DGV             | 16   | 2000 | 330.0              | 12.4               | 6.8     | 4.0     | 1.6     | 8.0     | 12.0   | Q1            |
| SN74AHCT139DR   | SOIC         | D               | 16   | 2500 | 330.0              | 16.4               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |
| SN74AHCT139PWR  | TSSOP        | PW              | 16   | 2000 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |



## TAPE AND REEL BOX DIMENSIONS



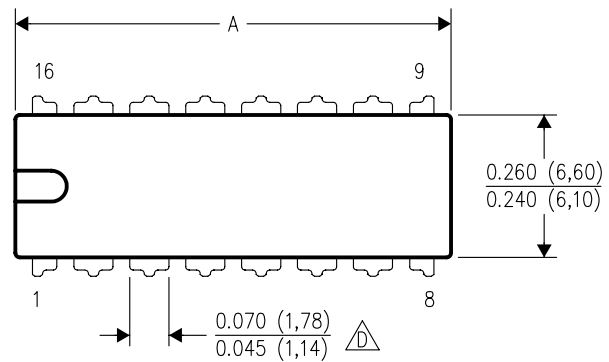
\*All dimensions are nominal

| Device          | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74AHCT139DBR  | SSOP         | DB              | 16   | 2000 | 367.0       | 367.0      | 38.0        |
| SN74AHCT139DGVR | TVSOP        | DGV             | 16   | 2000 | 367.0       | 367.0      | 35.0        |
| SN74AHCT139DR   | SOIC         | D               | 16   | 2500 | 333.2       | 345.9      | 28.6        |
| SN74AHCT139PWR  | TSSOP        | PW              | 16   | 2000 | 367.0       | 367.0      | 35.0        |

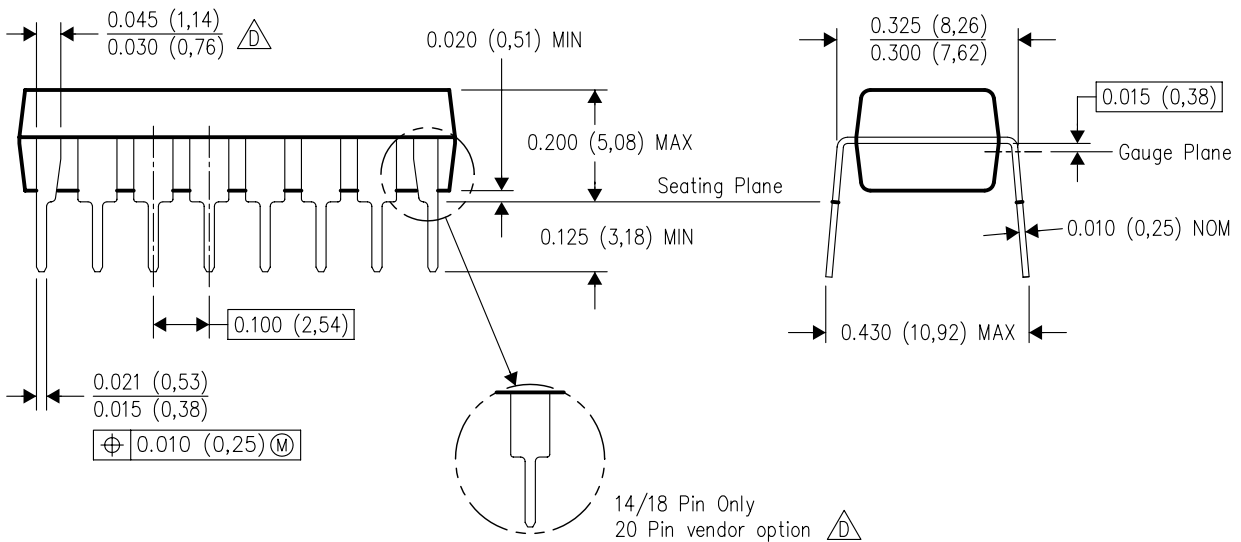
## N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **             | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| DIM                 |                  |                  |                  |                  |
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



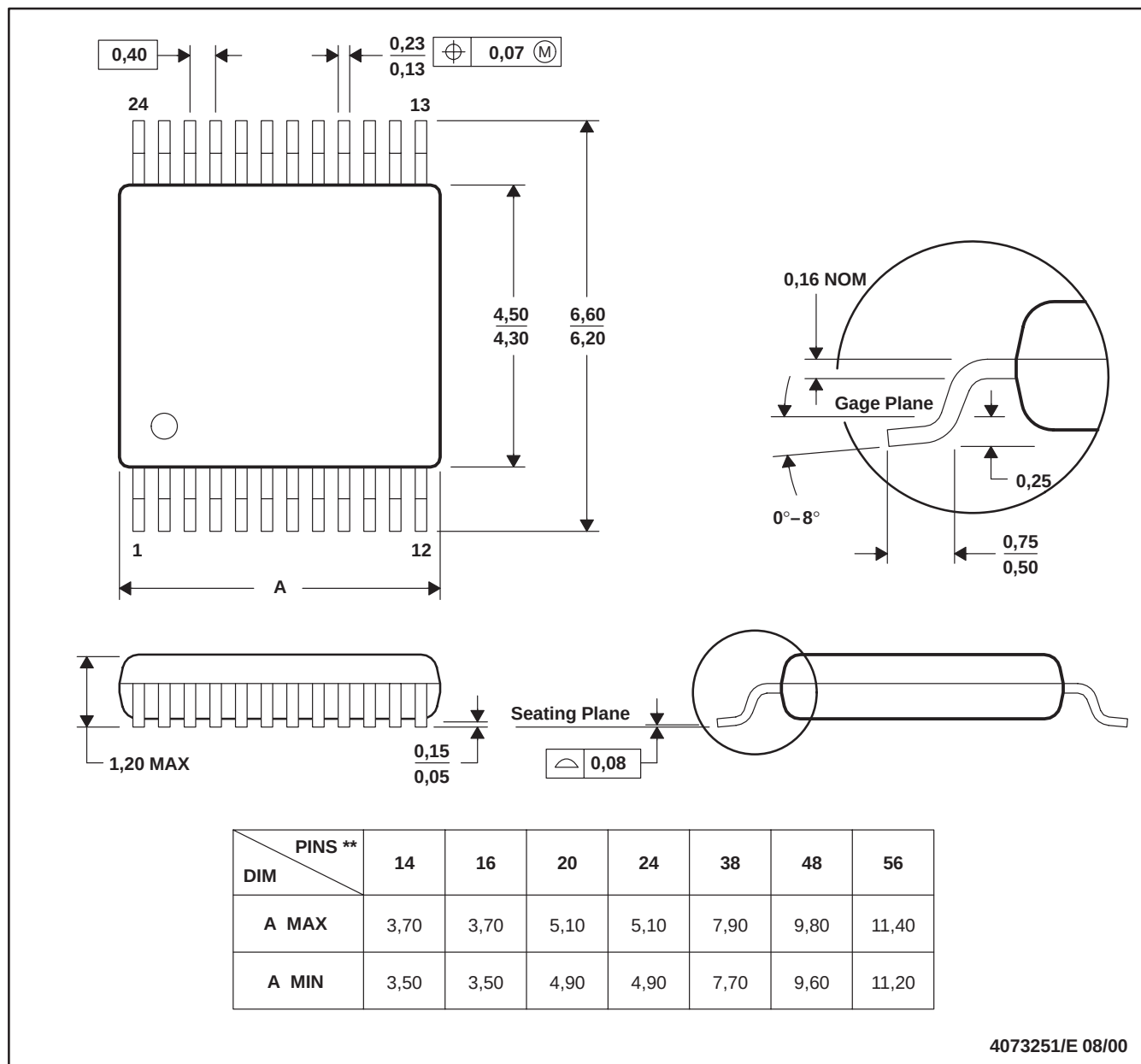
4040049/E 12/2002

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.

## DGV (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

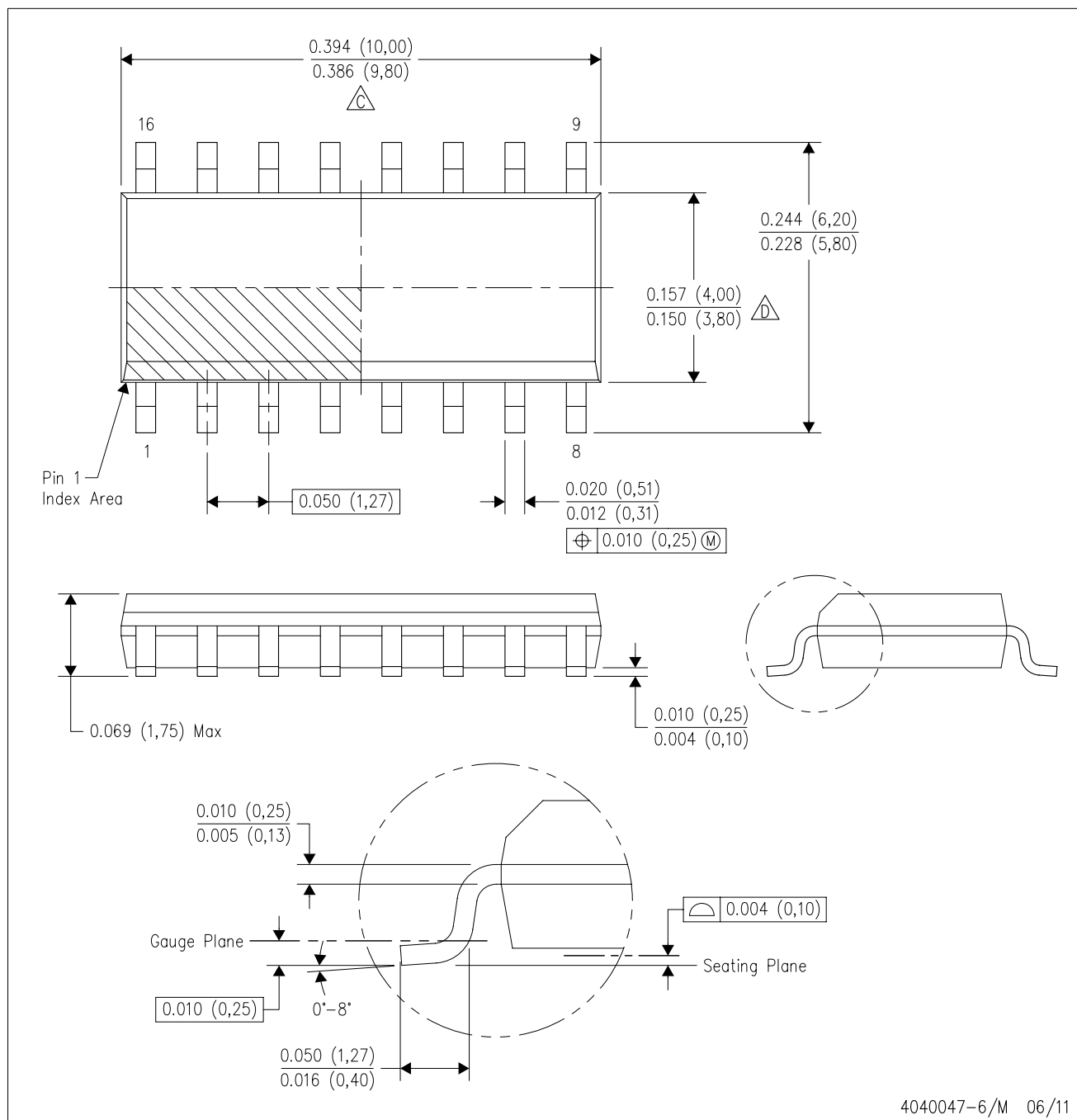
24 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.  
 D. Falls within JEDEC: 24/48 Pins – MO-153  
 14/16/20/56 Pins – MO-194

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE

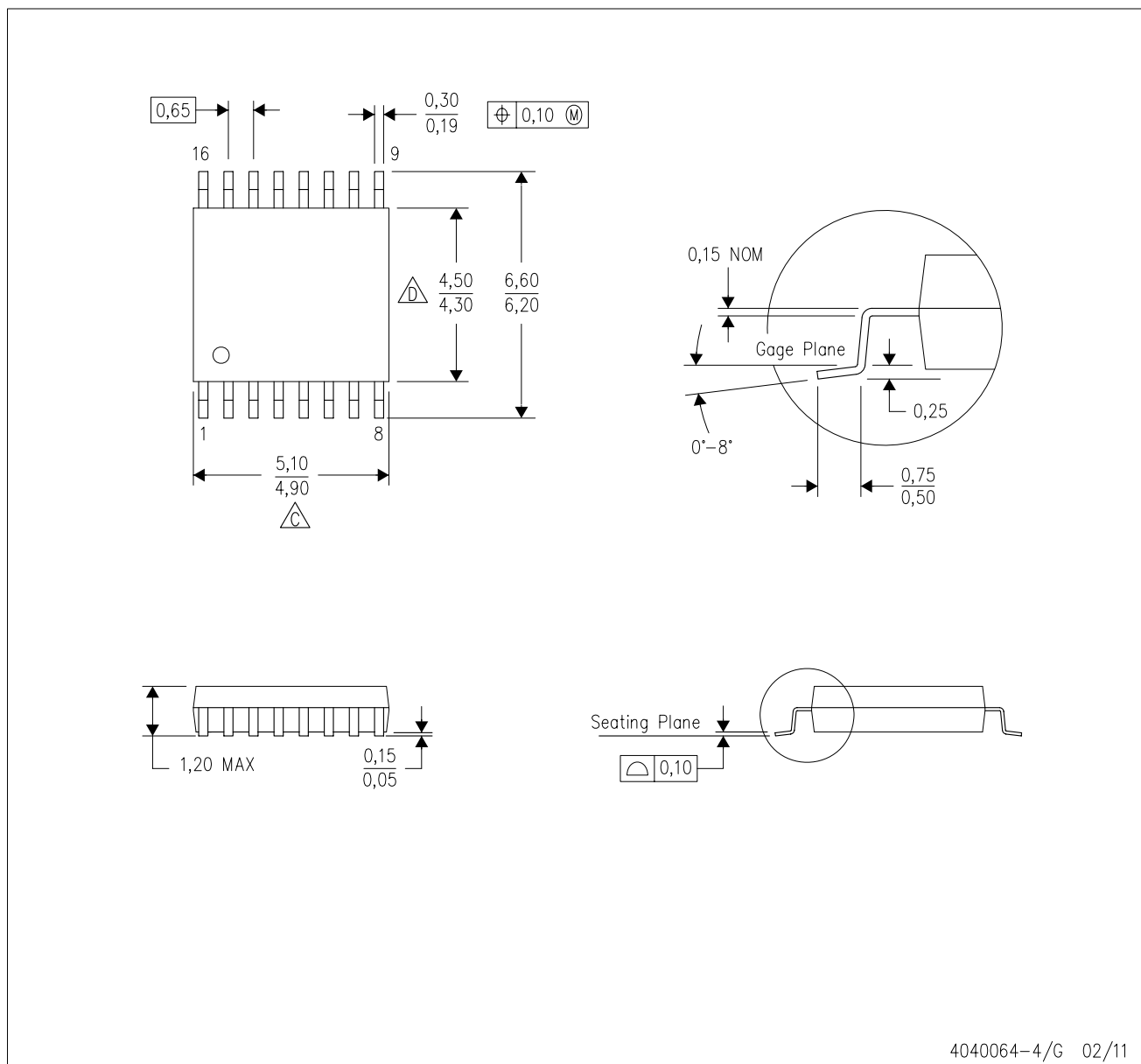


## NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.

PW (R-PDSO-G16)

PLASTIC SMALL OUTLINE

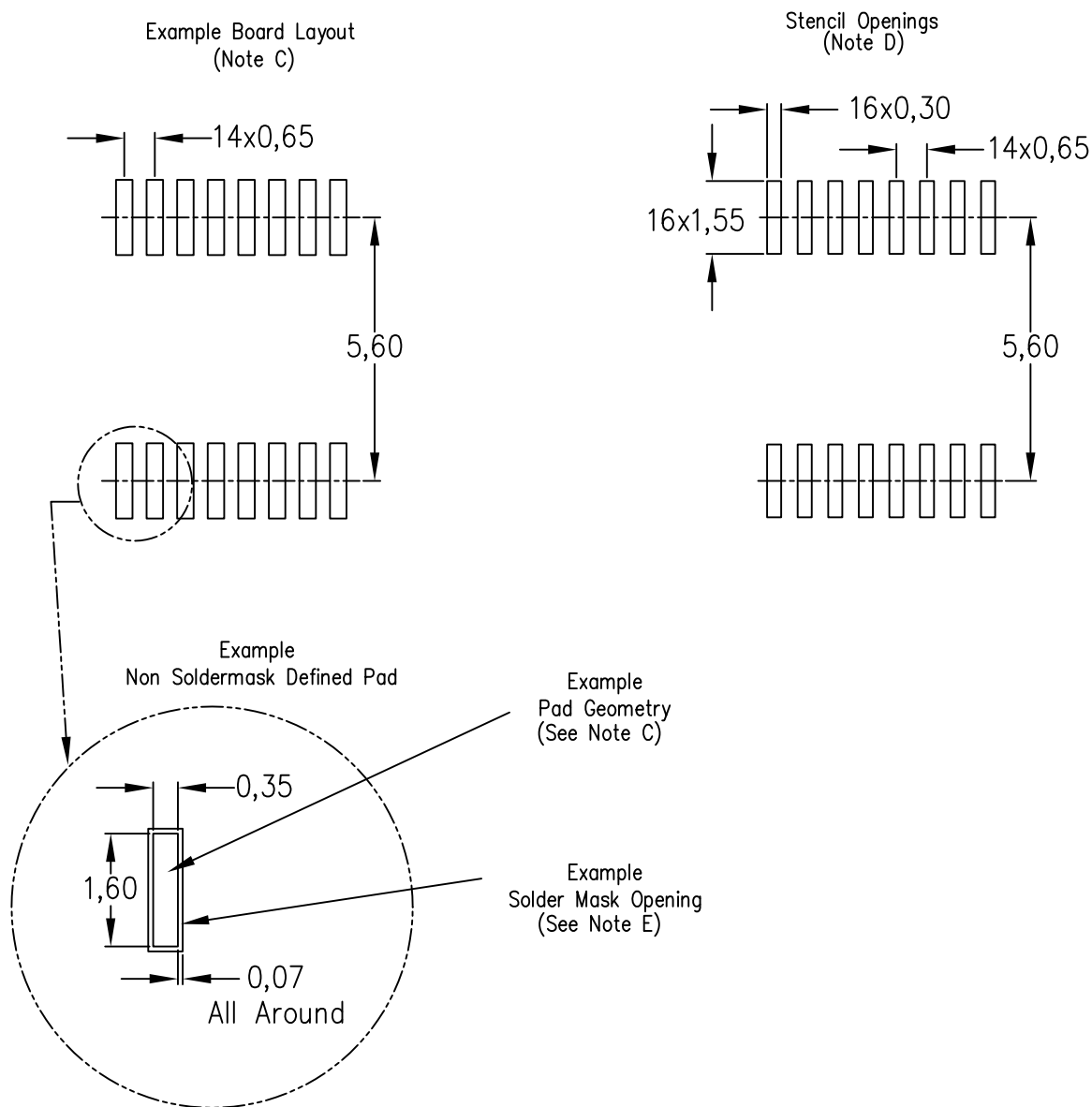


4040064-4/G 02/11

- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - B. This drawing is subject to change without notice.
  - C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
  - D. Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
  - E. Falls within JEDEC MO-153

PW (R-PDSO-G16)

PLASTIC SMALL OUTLINE



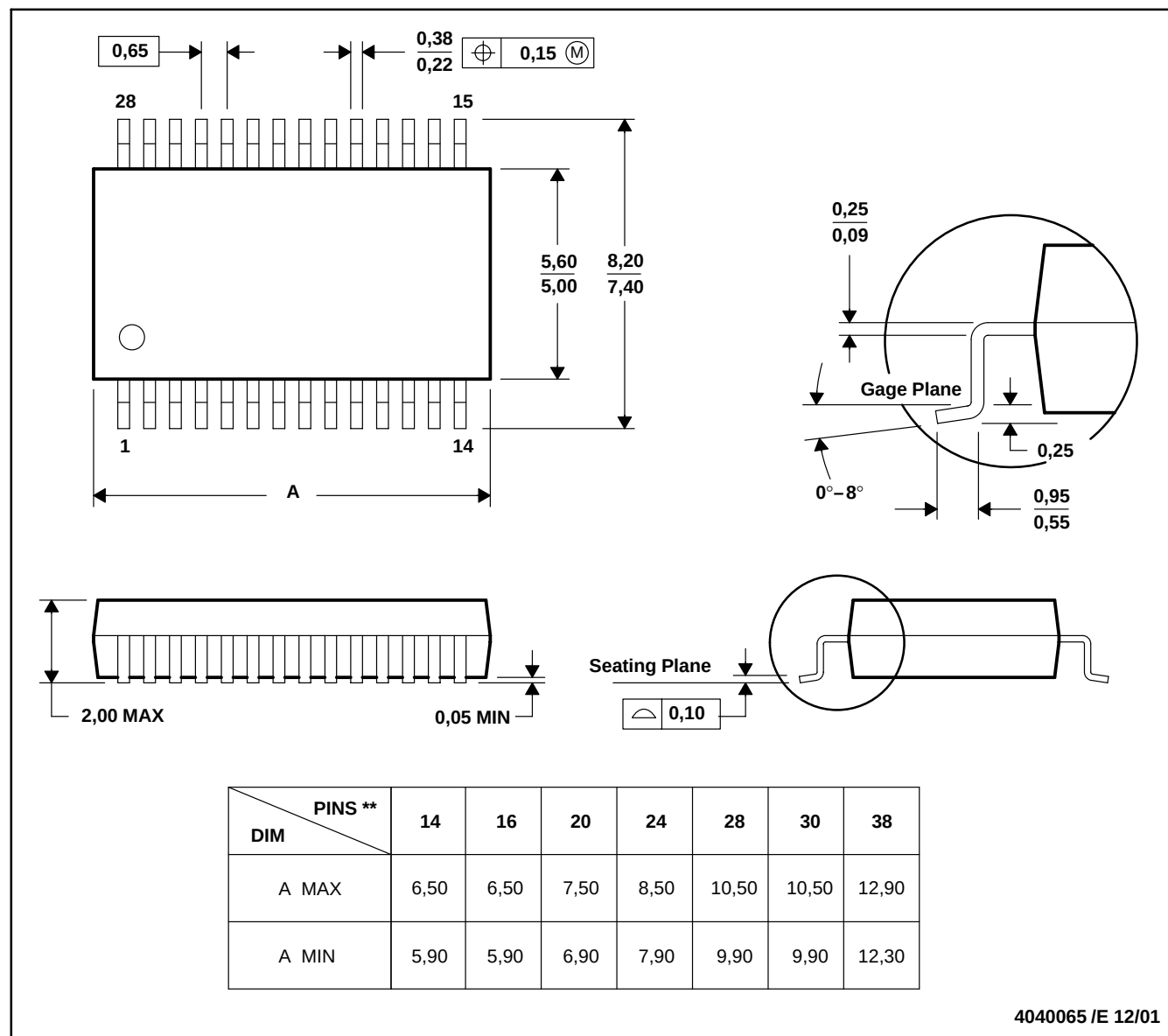
4211284-3/F 12/12

- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

## DB (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

28 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-150

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|                               |                                                                                          |
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