# 74HC1GU04

## Single unbuffered inverter

Rev. 6 — 25 July 2018

**Product data sheet** 

## 1. General description

The 74HC1GU04 is a single unbuffered inverter. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{\rm CC}$ .

### 2. Features and benefits

- Symmetrical output impedance
- Wide operating voltage range from 2.0 V to 6.0 V
- · Low power dissipation
- Balanced propagation delays
- SOT353-1 and SOT753 package options

### 3. Ordering information

#### **Table 1. Ordering information**

Type number	Package							
	Temperature range	Name	Description	Version				
74HC1GU04GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
74HC1GU04GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753				

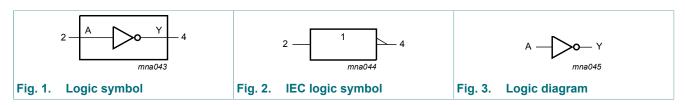
### 4. Marking

### Table 2. Marking codes

Type number	Marking[1]
74HC1GU04GW	HD
74HC1GU04GV	HU4

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 5. Functional diagram

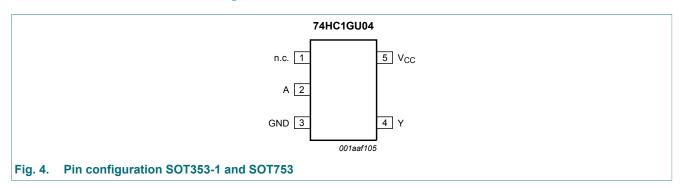




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## 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

**Table 3. Pin description** 

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Υ	4	data output
V <sub>CC</sub>	5	supply voltage

## 7. Functional description

### **Table 4. Function table**

H = HIGH voltage level; L = LOW voltage level

Input	Output
A	Υ
L	Н
Н	L

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CC}$	supply voltage			-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{CC} + 0.5 \text{ V}$	[1]	-	±20	mA
I <sub>OK</sub>	output clamping current	$V_{O}$ < -0.5 V or $V_{O}$ > $V_{CC}$ + 0.5 V	[1]	-	±20	mA
Io	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	[1]	-	±12.5	mA
I <sub>CC</sub>	supply current			-	25	mA
I <sub>GND</sub>	ground current			-25	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	[2]	-	200	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

### 9. Recommended operating conditions

### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		2.0	5.0	6.0	V
VI	input voltage		0	-	$V_{CC}$	V
V <sub>O</sub>	output voltage		0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall	V <sub>CC</sub> = 2.0 V	-	-	625	ns/V
	rate	V <sub>CC</sub> = 4.5 V	-	-	139	ns/V
		V <sub>CC</sub> = 6.0 V	-	-	83	ns/V

### 10. Static characteristics

### **Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V). All typical values are measured at  $T_{amb}$  = 25 °C.

Symbol	ymbol Parameter Conditions		-40 °C to +85 °C			-40 °C t	Unit	
			Min	Тур	Max	Min	Max	
$V_{IH}$	V <sub>IH</sub> HIGH-level input voltage	V <sub>CC</sub> = 2.0 V	1.7	1.4	-	1.7	-	V
		V <sub>CC</sub> = 4.5 V	3.6	2.6	-	3.6	-	V
		V <sub>CC</sub> = 6.0 V	4.8	3.4	-	4.8	-	V
$V_{IL}$	LOW-level input	V <sub>CC</sub> = 2.0 V	-	0.6	0.3	-	0.3	V
	voltage	V <sub>CC</sub> = 4.5 V	-	1.9	0.9	-	0.9	V
		V <sub>CC</sub> = 6.0 V	-	2.6	1.2	-	1.2	V

<sup>[2]</sup> Above 55 °C the value of Ptot derates linearity with 2.5 mW/K.

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Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C		
			Min	Тур	Max	Min	Max		
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$							
	voltage	$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 2.0 $V$	1.8	2.0	-	1.8	-	V	
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.0	4.5	-	4.0	-	V	
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 6.0 $V$	5.5	6.0	-	5.5	-	V	
		$I_{O}$ = -2.0 mA; $V_{CC}$ = 4.5 V	4.13	4.32	-	3.7	-	V	
		$I_{O}$ = -2.6 mA; $V_{CC}$ = 6.0 V	5.63	5.81	-	5.2	-	V	
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$							
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 2.0 $V$	-	0	0.2	-	0.2	V	
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	0	0.5	-	0.5	V	
		$I_O = 20 \mu A; V_{CC} = 6.0 V$	-	0	0.5	-	0.5	V	
		$I_O = 2.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	0.15	0.33	-	0.4	V	
		$I_O = 2.6 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	0.16	0.33	-	0.4	V	
I <sub>I</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	1.0	-	1.0	μΑ	
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0 \text{ V}$	-	-	10	-	20	μΑ	
Cı	input capacitance		-	5	-	-	-	pF	

## 11. Dynamic characteristics

#### **Table 8. Dynamic characteristics**

GND = 0 V;  $t_r = t_f = 6.0$  ns; For test circuit see Fig. 6. All typical values are measured at  $T_{amb} = 25$  °C.

Symbol	Parameter	Conditions		-40 °C to +85 °C			-40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	
t <sub>pd</sub>	propagation delay	A to Y; see Fig. 5	]						
		V <sub>CC</sub> = 2.0 V; C <sub>L</sub> = 50 pF		-	10	90	-	105	ns
		V <sub>CC</sub> = 4.5 V; C <sub>L</sub> = 50 pF		-	7	18	-	21	ns
		$V_{CC} = 6.0 \text{ V}; C_L = 50 \text{ pF}$		-	6	15	-	18	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF		-	5	-	-	-	ns
$C_{PD}$	power dissipation capacitance	$V_1 = GND \text{ to } V_{CC}$ [2]	]	-	14	-	-	-	pF

 $f_i$  = input frequency in MHz;

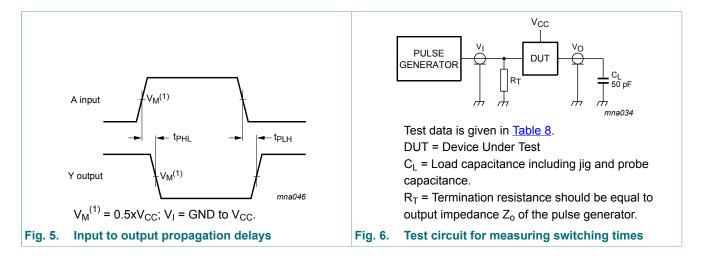
f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

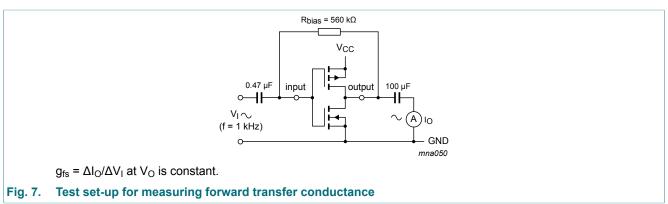
V<sub>CC</sub> = supply voltage in Volts.

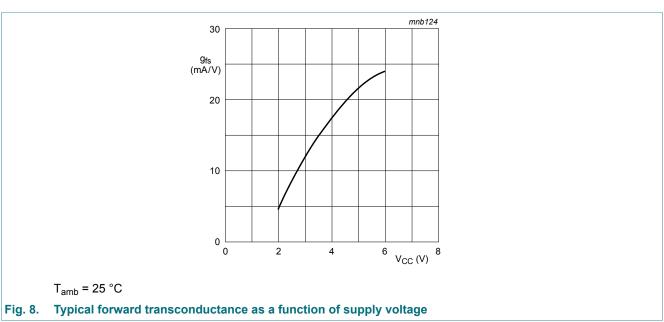
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### 11.1. Waveform and test circuit



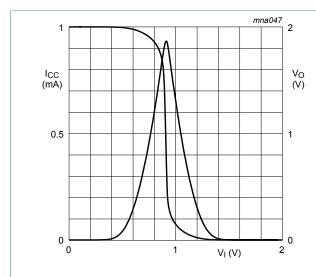
### 11.2. Additional characteristics





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## 11.3. Typical transfer characteristics



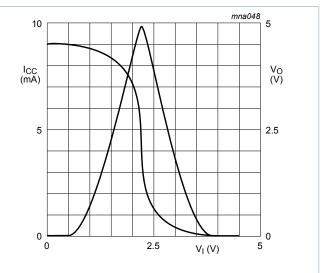


Fig. 9.  $V_{CC} = 2.0 \text{ V}; I_{O} = 0 \text{ A}$ 

Fig. 10.  $V_{CC} = 4.5 \text{ V}; I_{O} = 0 \text{ A}$ 

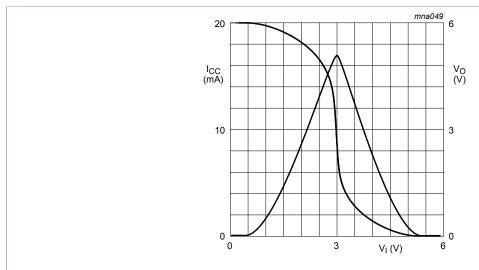


Fig. 11.  $V_{CC} = 6.0 \text{ V}$ ;  $I_{O} = 0 \text{ A}$ 

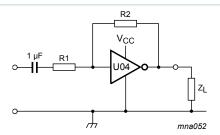
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## 12. Application information

Some applications are:

- Linear amplifier (see <u>Fig. 12</u>)
- In crystal oscillator design (see Fig. 13)

Remark: All values given are typical unless otherwise specified.



Maximum  $V_{o(p-p)} = V_{CC} - 1.5 \text{ V}$  centered at  $0.5 \text{xV}_{CC}$ .

$$G_v = -\frac{G_{\rm ol}}{1 + \frac{\rm R1}{\rm R2} \left(1 + G_{\rm ol}\right)}$$

 $G_{ol}$  = open loop gain

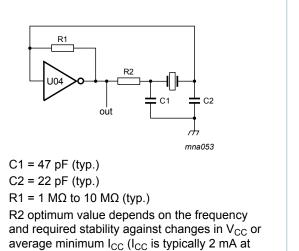
G<sub>v</sub> = voltage gain

 $\mathsf{R}1 \geq 3\;\mathsf{k}\Omega,\;\mathsf{R}2 \leq 1\;\mathsf{M}\Omega$ 

 $Z_L > 10 \text{ k}\Omega$ ;  $G_{ol} = 20 \text{ (typ.)}$ 

Typical unity gain bandwidth product is 5 MHz.

Fig. 12. Used as a linear amplifier



 $V_{CC}$  = 3 V and f = 1 MHz). Fig. 13. Crystal oscillator configuration

### Table 9. External components for resonator (f < 1 MHz)

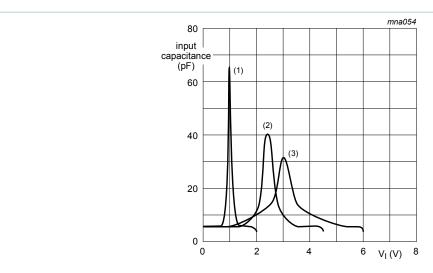
All values given are typical and must be used as an initial set-up

Frequency	R1	R2	C1	C2
10 kHz to 15.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	20 pF
16 kHz to 24.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	10 pF
25 kHz to 54.9 kHz	2.2 ΜΩ	100 kΩ	56 pF	10 pF
55 kHz to 129.9 kHz	2.2 ΜΩ	100 kΩ	47 pF	5 pF
130 kHz to 199.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
200 kHz to 349.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
350 kHz to 600 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF

Table 10. Optimum value for R2

Frequency	R2	Optimum for	
3 kHz	2.0 kΩ	minimum required I <sub>CC</sub>	
	8.0 kΩ	minimum influence due to change in V <sub>CC</sub>	
6 kHz	1.0 kΩ	minimum required I <sub>CC</sub>	
	4.7 kΩ	minimum influence by V <sub>CC</sub>	
10 kHz	0.5 kΩ	minimum required I <sub>CC</sub>	
	2.0 kΩ	minimum influence by V <sub>CC</sub>	
14 kHz	0.5 kΩ	minimum required I <sub>CC</sub>	
	1.0 kΩ	minimum influence by V <sub>CC</sub>	
>14 kHz	-	replace R2 by C3 with a typical value of 35 pF	

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- (1)  $V_{CC} = 2.0 \text{ V}$
- (2)  $V_{CC} = 4.5 \text{ V}$
- (3)  $V_{CC} = 6.0 \text{ V}$

Fig. 14. Typical input capacitance as a function of the input voltage

**Product data sheet** 

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## 13. Package outline

## TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm SOT353-1 = v M A ⊕ w M detail X 3 mm scale **DIMENSIONS (mm are the original dimensions)** ΗE $A_2$ Α3 $D^{(1)}$ $E^{(1)}$ e<sub>1</sub> L $Z^{(1)}$ UNIT θ Lp max 0.30 0.25 1.35 0.60 1.1 mm 0.15 0.65 1.3 0.425 0.3 0.1 0.1

#### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT353-1		MO-203	SC-88A		<del>-00-09-01</del> 03-02-19

Fig. 15. Package outline SOT353-1 (TSSOP5)

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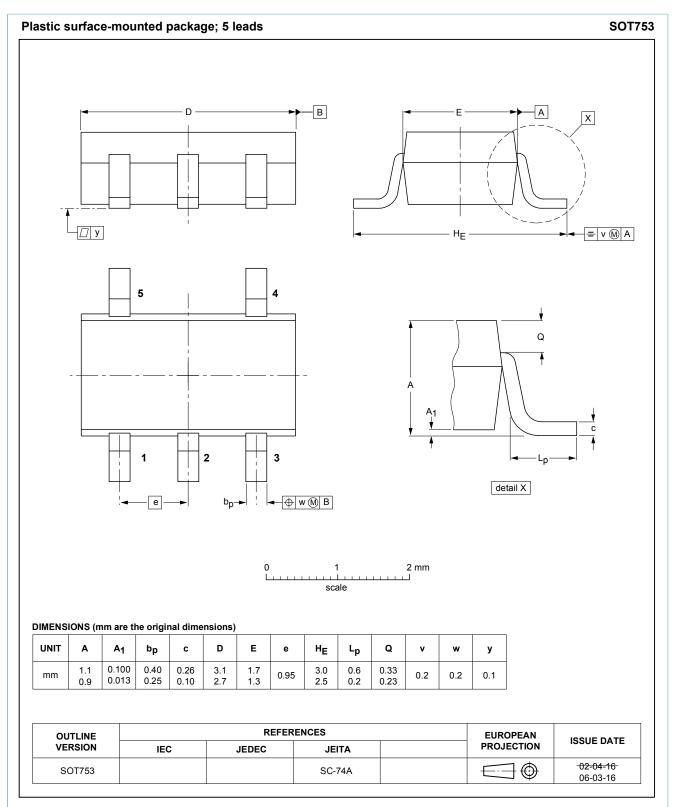


Fig. 16. Package outline SOT753 (SC-74A)

### Single unbuffered inverter

## 14. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test

## 15. Revision history

### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC1GU04 v.6	20180725	Product data sheet	-	74HC1GU04 v.5	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Fig. 8: forward transconductance graph added.</li> </ul>				
74HC1GU04 v.5	20070710	Product data sheet	-	74HC1GU04 v.4	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Package SOT353 changed to SOT353-1 in Table 1 and Fig. 15.</li> <li>Quick Reference Data and Soldering sections removed.</li> <li>Section 2 updated.</li> </ul>				
74HC1GU04 v.4	20020527	Product specification	-	74HC1GU04 v.3	
74HC1GU04 v.3	20020513	Product specification	-	74HC1GU04 v.2	
74HC1GU04 v.2	20010427	Product specification	-	74HC1GU04 v.1	
74HC1GU04 v.1	19981118	Product specification	_	_	

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### 16. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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