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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HAT2140H

Silicon N Channel Power MOS FET Power Switching

REJ03G1192-0400
(Previous: ADE-208-1581B)
Rev.4.00
Sep 07, 2005

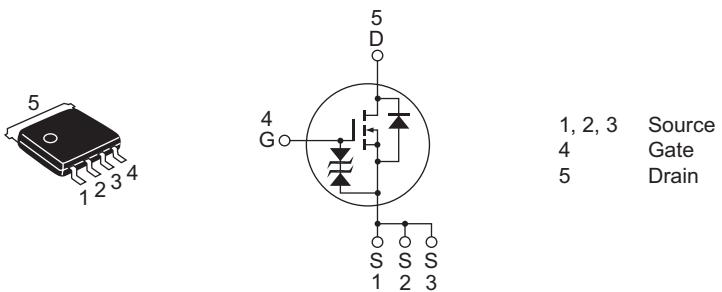
Features

- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

$R_{DS(on)}$ = 12.5 mΩ typ. (at $V_{GS} = 10$ V)

Outline

RENESAS Package code: PTZZ0005DA-A
(Package name: LFPAK)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	25	A
Drain peak current	I _D (pulse) ^{Note 1}	100	A
Body-drain diode reverse drain current	I _{DR}	25	A
Avalanche current	I _{AP} ^{Note 3}	25	A
Avalanche energy	E _{AR} ^{Note 3}	62.5	mJ
Channel dissipation	P _{ch} ^{Note 2}	30	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%

2. T_c = 25 °C3. Value at T_{ch} = 25°C, R_g ≥ 50 Ω**Electrical Characteristics**

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	100	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR) GSS}	±20	—	—	V	I _G = ±100 µA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	µA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	µA	V _{DS} = 100 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	2.0	—	3.5	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state resistance	R _{DS (on)}	—	12.5	16.0	mΩ	I _D = 12.5 A, V _{GS} = 10 V ^{Note 4}
	R _{DS (on)}	—	13.5	18.0	mΩ	I _D = 12.5 A, V _{GS} = 7 V ^{Note 4}
Forward transfer admittance	y _{fs}	27	45	—	S	I _D = 12.5 A, V _{DS} = 10 V ^{Note 4}
Input capacitance	C _{iss}	—	6500	—	pF	V _{DS} = 10 V
Output capacitance	C _{oss}	—	480	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	210	—	pF	f = 1 MHz
Total gate charge	Q _g	—	105	—	nC	V _{DD} = 50 V
Gate to source charge	Q _{gs}	—	20	—	nC	V _{GS} = 10 V
Gate to drain charge	Q _{gd}	—	22	—	nC	I _D = 25 A
Turn-on delay time	t _{d (on)}	—	25	—	ns	V _{GS} = 10 V, I _D = 12.5 A
Rise time	t _r	—	24	—	ns	V _{DD} ≥ 30 V
Turn-off delay time	t _{d (off)}	—	100	—	ns	R _L = 2.4 Ω
Fall time	t _f	—	12	—	ns	R _g = 4.7 Ω
Body-drain diode forward voltage	V _{DF}	—	0.83	1.08	V	I _F = 25 A, V _{GS} = 0 ^{Note 4}
Body-drain diode reverse recovery time	t _{rr}	—	55	—	ns	I _F = 25 A, V _{GS} = 0 di _F /dt = 100 A/µs

Note: 4. Pulse test

Package Dimensions

JEITA Package Code	RENESAS Code	Package Name	MASS(Typ.)	Unit: mm
SC-100	PTZZ0005DA-A	LFPAK	0.080g	

(Ni/Pd/Au plating)

Ordering Information

Part Name	Quantity	Shipping Container
HAT2140H-EL-E	2500 pcs	Taping

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