

DLP-uCF2321
LEAD-FREE

PIC18F2321 TARGET BOARD

1.0 INTRODUCTION

The DLP-uCF2321 Target Board is designed to easily connect to the DLP-FLASH2 Device Programmer/Debugger and provide the design engineer with a low-cost hardware platform for developing and testing applications for the PIC18F2321 microcontroller.

(Refer to the schematic at the end of this datasheet for additional details.)

2.0 SPECIFICATIONS

- Program Memory: 4K x 14
- 2.0-5.5V Operation**
- RAM Size: 512 x 8
- 19 I/O (9x10b Analog)
- EEPROM Size: 256 x 8
- LED Indicator
- Size: 1.31 x 0.89 Inches

****Note:** If this target board is operated at a voltage other than 5.0 volts via a user-supplied power source, then the TPWR jumper must be removed on the DLP-FLASH2 Programmer.

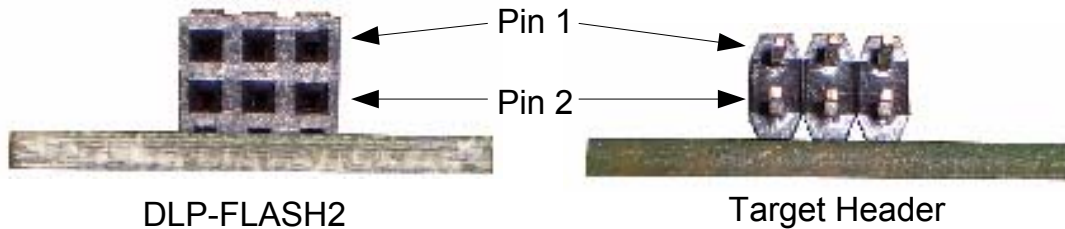
3.0 OSCILLATOR OPTIONS

The PIC18F2321 has an internal oscillator with eight user-selectable frequencies from 31 kHz to 8 MHz. The internal oscillator also provides a range of clock speeds from 31 kHz to 32 MHz when used with the PLL. Refer to the datasheet for the PIC18F2321 for additional details.

A user-supplied crystal and associated capacitors can also be added if a specific operating frequency is required.

4.0 PROGRAMMER/DEBUGGER INTERFACE

The DLP-FLASH2 Programmer/Debugger utilizes a 6-pin, 2mm female header for connection to the target device. The following shows the pinout for the programming interface connectors:



Pin #	Description	Alternate Name
1	PGM	B3
2	PGC	B6
3	PGD	B7
4	Ground	
5	Target Power	TVDD
6	MCLR	Vpp

5.0 DISCLAIMER

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This document provides preliminary information that may be subject to change without notice.

6.0 CONTACT INFORMATION

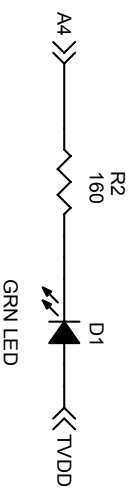
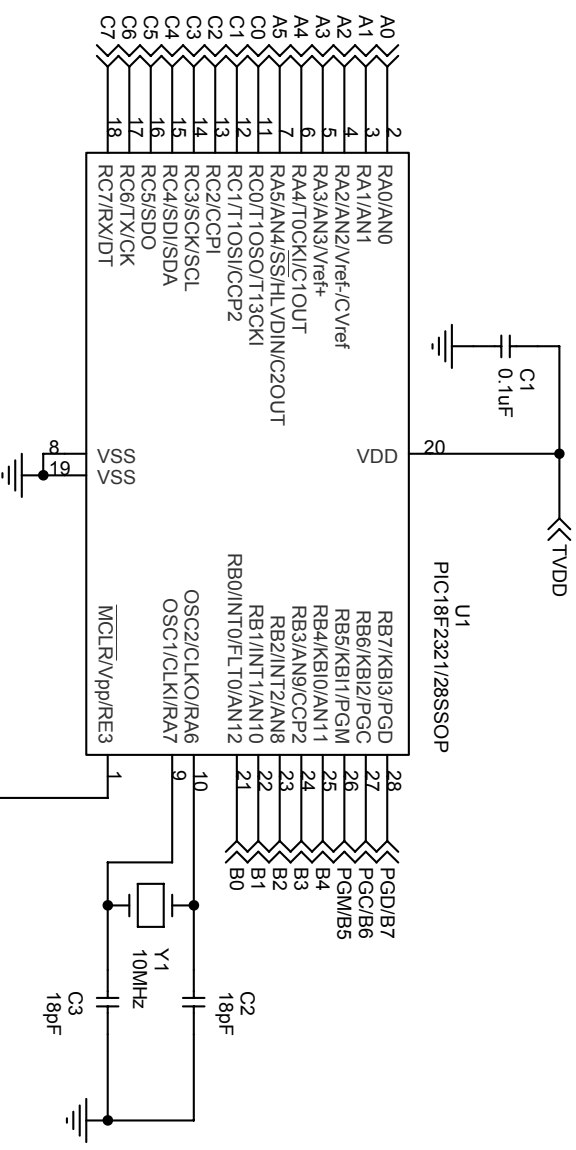
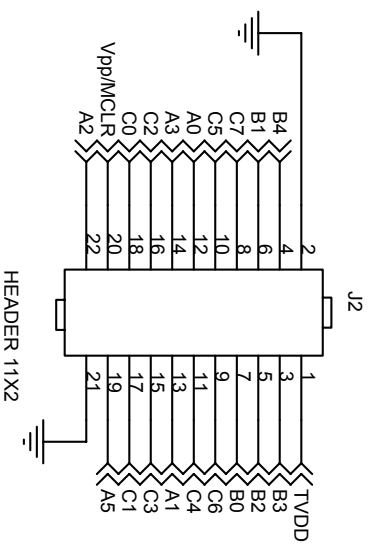
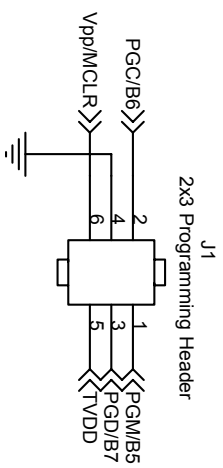
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