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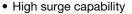
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Thyristor High Voltage, Phase Control SCR, 70 A

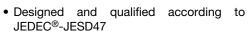


PRODUCT SUMMARY					
Package	Super TO-247				
Diode variation	Single SCR				
I _{T(AV)}	70 A				
V _{DRM} /V _{RRM}	1200 V, 1600 V				
V_{TM}	1.25 V				
I _{GT}	100 mA				
T _J	-40 °C to 125 °C				

FEATURES







 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

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RoHS COMPLIANT

APPLICATIONS

- · AC switches
- High voltage input rectification (soft start)
- High current crow-bar
- · Other phase-control circuits
- Designed to be used with Vishay input diodes, switches, and output rectifiers which are available in identical package outlines

DESCRIPTION

The VS-70TPS..PbF high voltage series of silicon controlled rectifiers are specifically designed for high and medium power switching, and phase control applications.

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	70	A		
I _{RMS}	Lead current limitation	75	A		
V _{RRM} /V _{DRM}	Range	1200/1600	V		
I _{TSM}		1100	А		
V _T	100 A, T _J = 25 °C	1.4	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ		-40 to 125	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
VS-70TPS12PbF	1200	1300	15			
VS-70TPS16PbF	1600	1700	15			



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PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average on-state current	I _{T(AV)}	T _C = 82 °C, 180° cor	nduction half sine wave		70	
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}	Lead current limitation	Lead current limitation		75	Α
Maximum peak, one-cycle		10 ms sine pulse, rat	ted V _{RRM} applied		930	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no	voltage reapplied		1100	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rat	ted V _{RRM} applied	Initial $T_J = T_J$ maximum	4325	A ² s
Maximum i-t for fusing	I-ί	10 ms sine pulse, no voltage reapplied		6115	A-2	
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied		61 150	A²√s	
Low level value of threshold voltage	V _{T(TO)1}			0.916	V	
High level value of threshold voltage	V _{T(TO)2}	T 105 °C	T _J = 125 °C		1.21	v
Low level value of on-state slope resistance	r _{t1}	1J= 125 C		4.138	 0	
High level value of on-state slope resistance	r _{t2}			3.43	mΩ	
Maximum peak on-state voltage	V_{TM}	100 A, T _J = 25 °C			1.4	V
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C		150	A/μs	
Maximum holding current	I _H	Anode supply = 6 V, resistive load, initial I _T = 1 A, T _J = 25 °C		200		
Maximum latching current	ΙL	Anode supply = 6 V, resistive load, T _J = 25 °C		400		
Maximum reverse and direct leakage current	I _{RRM} /I _{DRM}	T _J = 25 °C	V _R = Rated V _{RRM} /V _{DI}	RM	1.0	mA
		T _J = 125 °C	T _J = 125 °C (T _J = T _J max., linear to 80 %		15	1
Maximum rate of rise of off-state voltage	dV/dt	T _J = 125 °C			500	V/µs

TRIGGERING					
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P _{GM}	T = 30 µs		10	w
Maximum average gate power	P _{G(AV)}	1 = 30 μs		2.5	VV
Maximum peak gate current	I _{GM}			2.5	Α
Maximum peak negative gate voltage	- V _{GM}			10	
		T _J = - 40 °C	Anode supply = 6 V resistive load	1.8	V
Maximum required DC gate voltage to trigger	V _{GT}	T _J = 25 °C		1.5	l v
		T _J = 125 °C		1.1	
		T _J = - 40 °C		150	
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	100	mA
		T _J = 125 °C		80	
Maximum DC gate voltage not to trigger	V_{GD}	T 405 °C V Pated value		0.25	V
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value		6	mA

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		TJ		-40 to 125	°C
Maximum storage temperature range		T _{Stg}		-40 to 150	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.27	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2	
				6	g
Approximate weight				0.21	OZ.
Maunting torque	minimum			6 (5)	kgf · cm
Mounting torque m	maximum			12 (10)	(lbf · in)
Marking device			Coop atula Super TO 247	70TPS	12
			Case style Super TO-247	70TPS	70TPS16

△R _{thJ-hs} CONDUCTION PER JUNCTION											
DEVICE	s	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION				UNITS
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VS-70TPSPbF	0.078	0.092	0.117	0.172	0.302	0.053	0.092	0.125	0.180	0.306	°C/W

Note

The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC

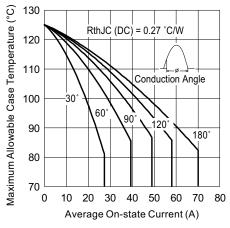


Fig. 1 - Current Rating Characteristics

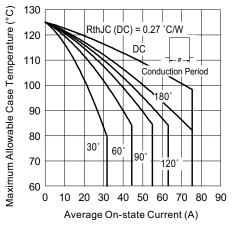


Fig. 2 - Current Rating Characteristics

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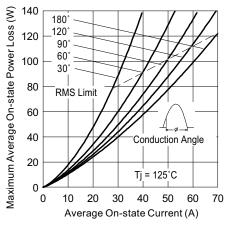


Fig. 3 - On-State Power Loss Characteristics

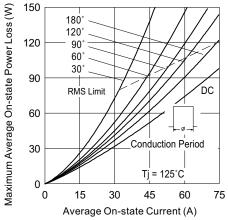


Fig. 4 - On-State Power Loss Characteristics

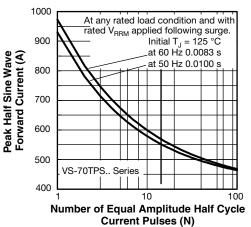


Fig. 5 - Maximum Non-Repetitive Surge Current

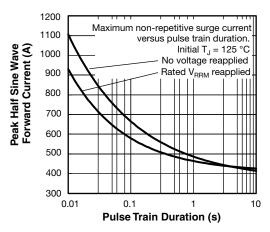


Fig. 6 - Maximum Non-Repetitive Surge Current

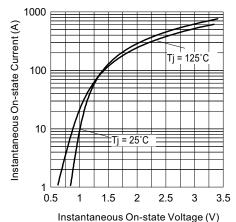
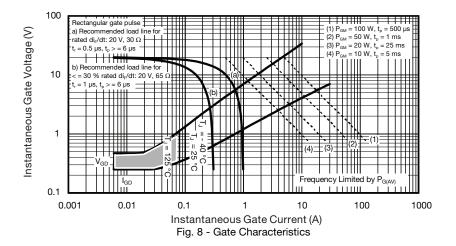


Fig. 7 - On-State Voltage Drop Characteristics

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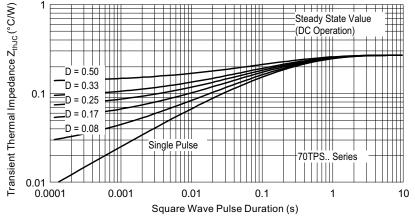


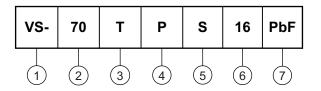
Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE





1 - Vishay Semiconductors product

2 - Current rating (70 = 70 A)

3 - Circuit configuration:

T = Thyristor

4 - Package:

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P = Super TO-247

5 - Type of silicon:

S = Standard recovery rectifier

Voltage code x 100 = V_{RRM} 12 = 1200 V 16 = 1600 V

7 - PbF = Lead (Pb)-free

ORDERING INFORMATION (example)					
PREFERED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-70TPS12PbF	25	500	Antistatic plastic tube		
VS-70TPS16PbF	25	500	Antistatic plastic tube		

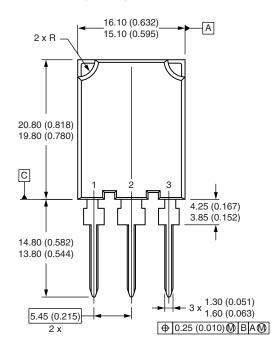
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95073				
Part marking information	www.vishay.com/doc?95070				

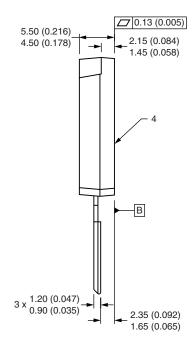


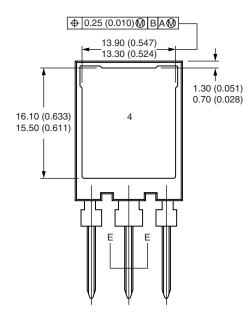
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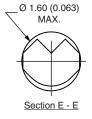
Super TO-247

DIMENSIONS in millimeters (inches)









Lead assignments

MOSFET	<u>IGBT</u>
1 - Gate	1 - Gate
2 - Drain	2 - Collector
3 - Source	3 - Emitter
4 - Drain	4 - Collector

Notes

- (1) Dimension and tolerancing per ASME Y14.5M-1994
- (2) Controlling dimension: millimeter
- (3) Outline conforms to JEDEC® outline TO-274AA



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