

1. Scope

The present specifications shall apply to an SJPL-D2.

2. Outline

Type	Silicon Diode
Structure	Resin Molded
Applications	High Frequency Rectification

3. Flammability

UL94V-0(Equivalent)

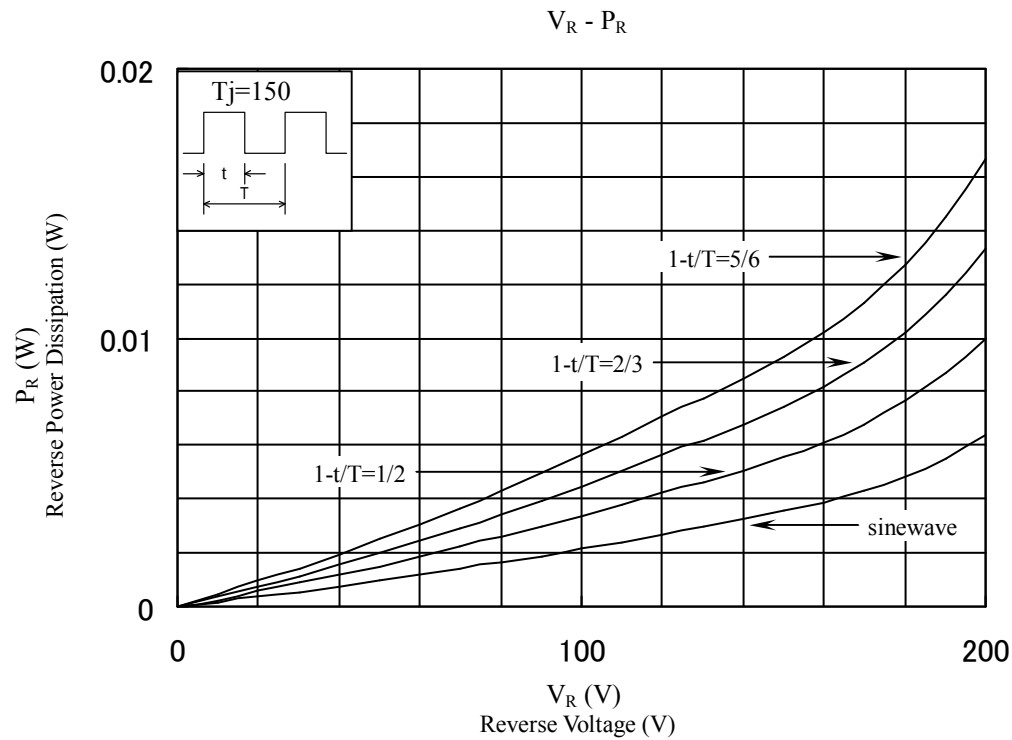
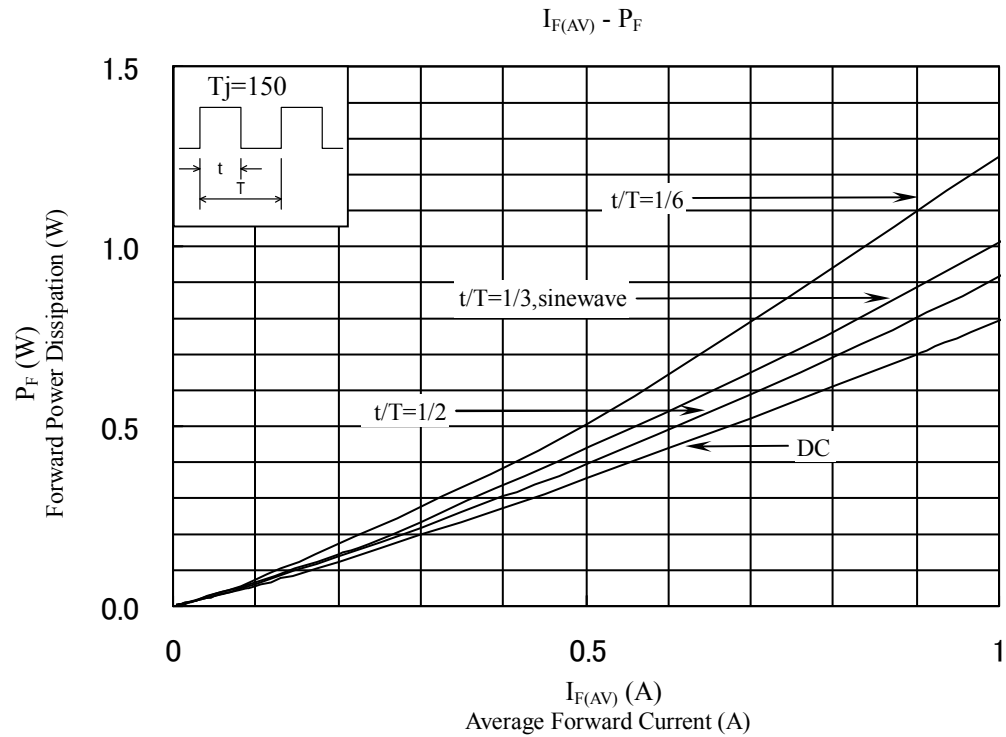
4. Absolute maximum ratings

No.	Item	Symbol	Unit	Rating	Conditions
1	Transient Peak Reverse Voltage	V_{RSM}	V	200	
2	Peak Reverse Voltage	V_{RM}	V	200	
3	Average Forward Current	$I_{F(AV)}$	A	1.0	Refer to Derating of 7
4	Peak Surge Forward Current	I_{FSM}	A	25	10msec. Half sinewave, one shot
5	I^2t Limiting Value	I^2t	A^2s	3.125	$1msec \leq t \leq 10msec$
6	Junction Temperature	T_j	°C	-40~+150	
7	Storage Temperature	T_{stg}	°C	-40~+150	

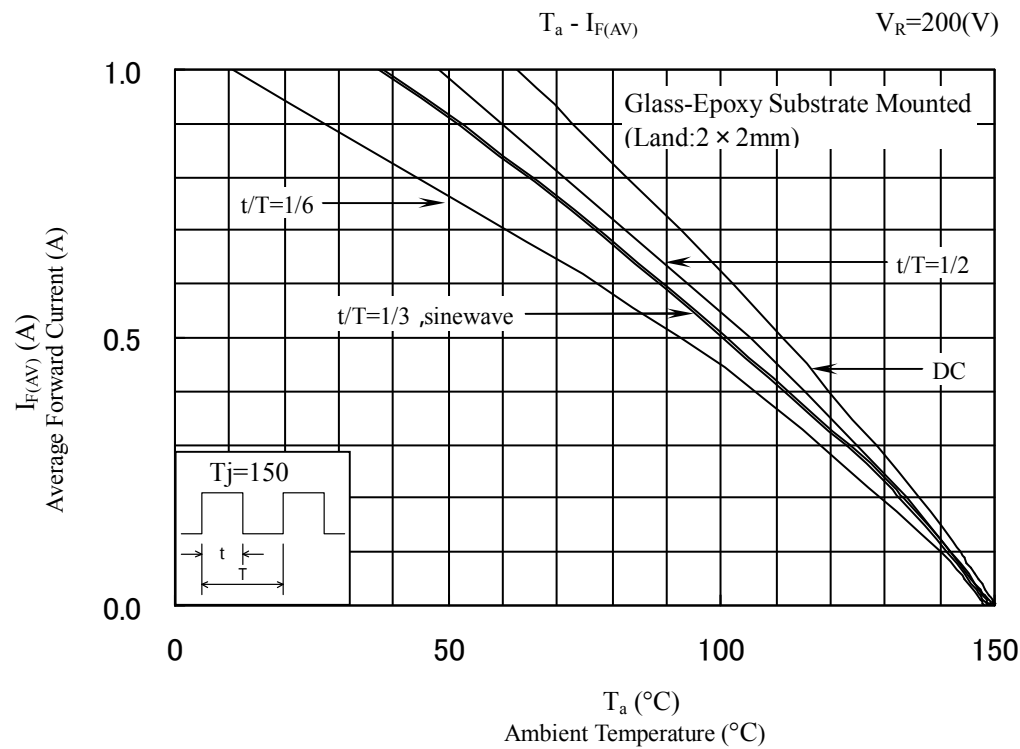
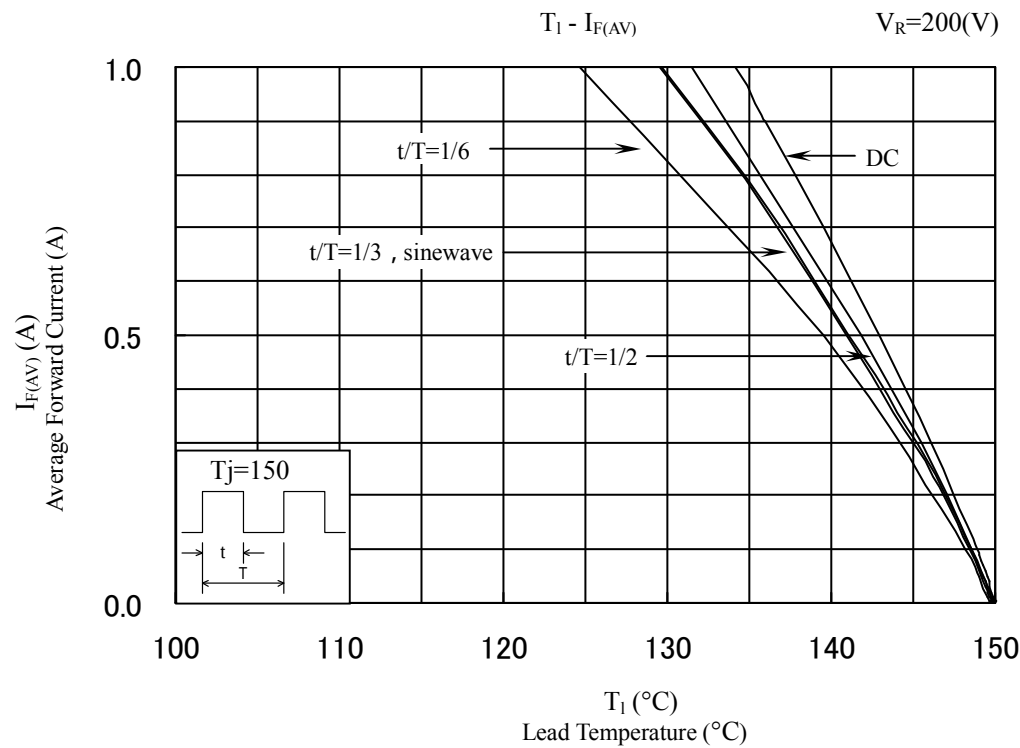
5. Electrical characteristics (Ta=25°C , unless otherwise specified)

No.	Item	Symbol	Unit	Value	Conditions
1	Forward Voltage Drop	V_F	V	0.98 max.	$I_F=1.0A$
2	Reverse Leakage Current	I_R	uA	25 max.	$V_R=V_{RM}$
3	Reverse Leakage Current Under High Temperature	$H \cdot I_R$	uA	100 max.	$V_R=V_{RM}, T_j=150^\circ C$
4	Reverse Recovery Time	trr-1	ns	50 max.	$I_F=I_{RP}=100mA$ 90% Recovery point, $T_j=25^\circ C$
		trr-2	ns	35 max.	$I_F=100mA, I_{RP}=200mA$ 75% Recovery point, $T_j=25^\circ C$
5	Thermal Resistance	$R_{th(j-l)}$	°C /W	20 max.	Between Junction and lead

6. Characteristics

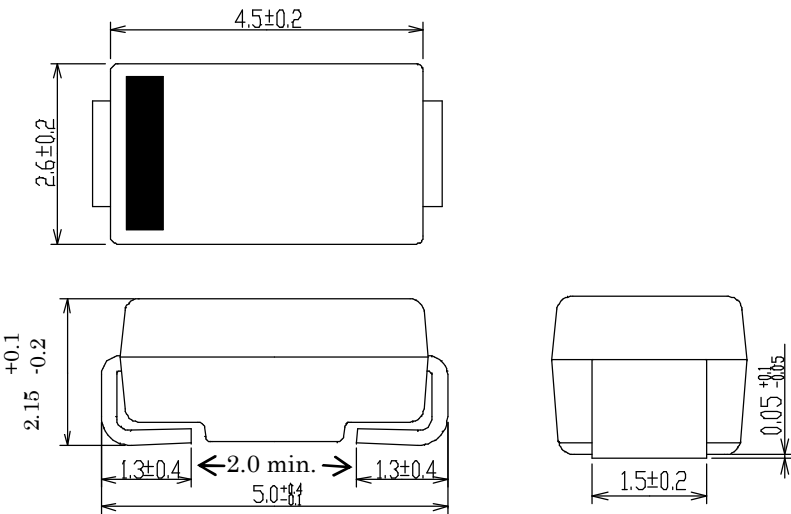


7. Derating



8. Package information

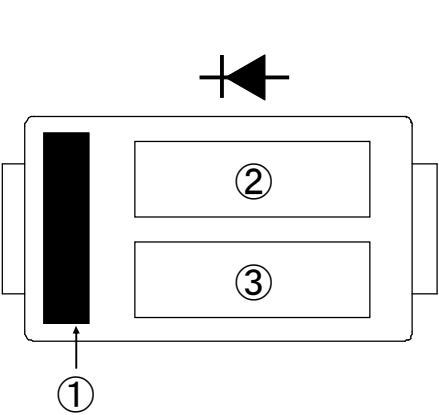
8-1Package type, physical dimensions and material



8-2Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3Marking



Polarity marking (Cathode band)

Type number
SJPL-D2 as abbreviated of LD2

Lot number
Example) 7227
7: Last number of Year
2: Month from 1 to 9 for Jan. to Sep.
O for Oct. , N for Nov. , D for Dec.
27: Day