

Off-line Power Supply Controller

FEATURES

- Transformerless Off-line Power Supply
- Wide 100VDC to 400VDC Allowable Input Range
- Fixed 5VDC or Adjustable Low Voltage Output
- Output Sinks 200mA, Sources 150mA Into a MOSFET Gate
- Uses Low Cost SMD Inductors
- Short Circuit Protected
- Optional Isolation Capability

DESCRIPTION

The UCC3888 controller is optimized for use as an off-line, low power, low voltage, regulated bias supply. The unique circuit topology utilized in this device can be visualized as two cascaded flyback converters, each operating in the discontinuous mode, both driven from a single external power switch. The significant benefit of this approach is the ability to achieve voltage conversion ratios as high as 400V to 2.7V with no transformer and low internal losses.

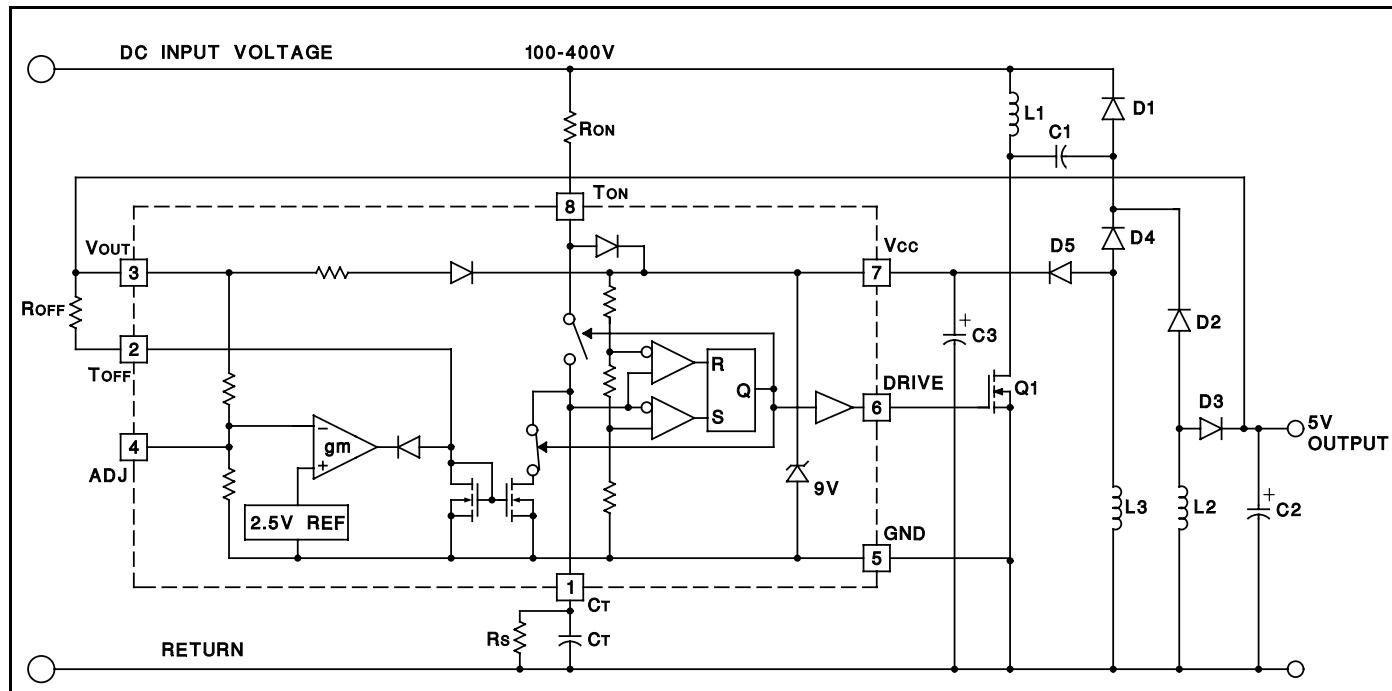
The control algorithm utilized by the UCC3888 sets the switch on time inversely proportional to the input line voltage and sets the switch off time inversely proportional to the output voltage. This action is automatically controlled by an internal feedback loop and reference. The cascaded configuration allows a voltage conversion from 400V to 2.7V to be achieved with a switch duty cycle of 7.6%. This topology also offers inherent short circuit protection since as the output voltage falls to zero, the switch off time approaches infinity.

The output voltage is set internally to 5V. It can be programmed for other output voltages with two external resistors. An isolated version can be achieved with this topology as described further in Unitrode Application Note U-149.

OPERATION

With reference to the application diagram below, when input voltage is first applied, the current through R_{ON} into T_{ON} is directed to V_{CC} where it charges the external capacitor, C_3 , connected to V_{CC} . As voltage builds on V_{CC} , an internal undervoltage lockout holds the circuit off and the output at $DRIVE$ low until V_{CC} reaches 8.4V. At this time, $DRIVE$ goes high turning on the power switch, Q_1 , and redirecting the current into T_{ON} to the timing capacitor, C_T . C_T charges to a fixed threshold with a current $I_{CHG}=0.8 \cdot (V_{IN} - 4.5V)/R_{ON}$. Since $DRIVE$ will only be high for as long as C_T charges, the power switch on time will be inversely proportional to line voltage. This provides a constant $(line\ voltage) \cdot (switch\ on\ time)$ product.

TYPICAL APPLICATION



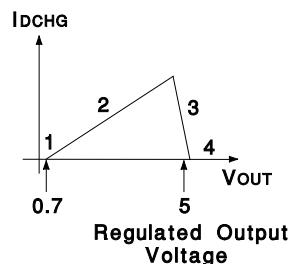
Note: This device incorporates patented technology used under license from Lambda Electronics, Inc.

UDG-96013

OPERATION (cont.)

At the end of the on time, Q1 is turned off and the current through R_{ON} is again diverted to V_{CC}. Thus the current through R_{ON}, which charges C_T during the on time, contributes to supplying power to the chip during the off time.

The power switch off time is controlled by the discharge of C_T which, in turn, is programmed by the regulated output voltage. The relationship between C_T discharge current, I_{DCHG}, and output voltage is illustrated as follows:

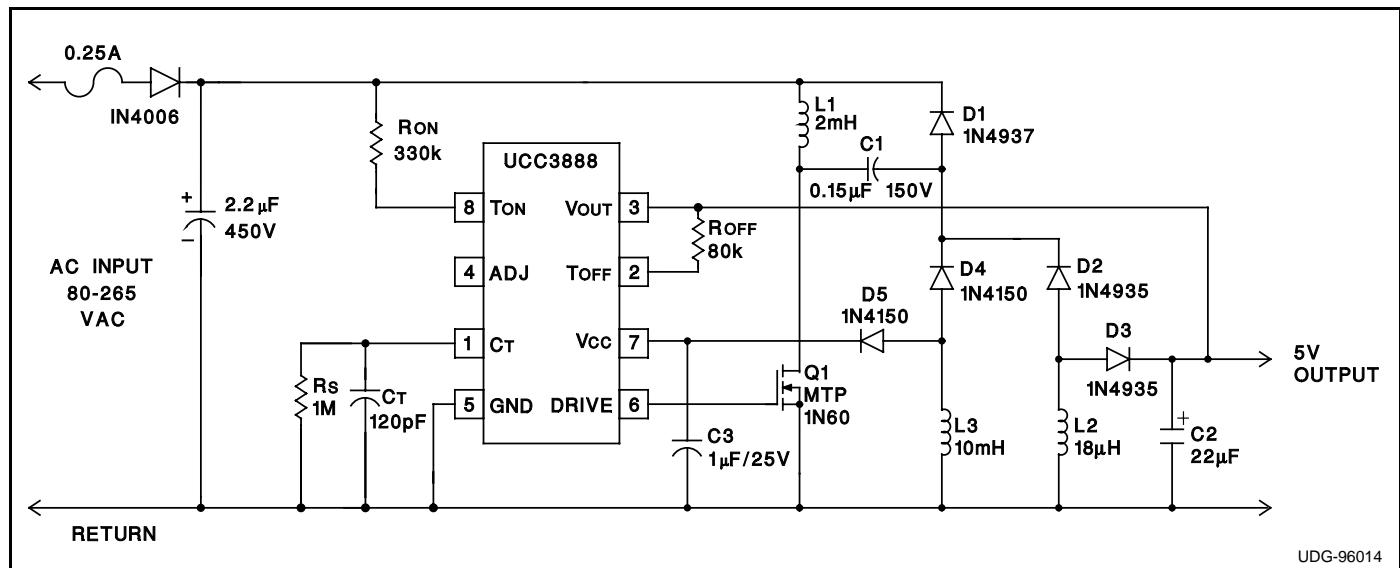


Region 1. When V_{OUT} = 0, the off time is infinite. This feature provides inherent short circuit protection. However, to ensure output voltage startup when the output is not a short, a high value resistor, R_S, is placed in parallel with C_T to establish a minimum switching frequency.

Region 2. As V_{OUT} rises above approximately 0.7V to its regulated value, I_{DCHG} is defined by R_{OFF}, and is equal to:

DESIGN EXAMPLE

The UCC3888 regulates a 5 volt, 1 Watt nonisolated DC output from AC inputs between 80 and 265 volts. In this example, the IC is programmed to deliver a maximum on time gate drive pulse width of 2.2 microseconds which occurs at 80 VAC. The corresponding switching frequency is approximately 100kHz at low line, and overall efficiency is approximately 50%. Additional design information is available in Unitrode Application Note U-149.



UDG-96014

$$I_{DCHG} = (V_{OUT} - 0.7V) / R_{OFF}$$

As V_{OUT} increases, I_{DCHG} increases reducing off time. The operating frequency increases and V_{OUT} rises quickly to its regulated value.

Region 3. In this region, a transconductance amplifier reduces I_{DCHG} in order to maintain a regulated V_{OUT}.

Region 4. If V_{OUT} should rise above its regulation range, I_{DCHG} falls to zero and the circuit returns to the minimum frequency established by R_S and C_T.

The range of switching frequencies is established by R_{ON}, R_{OFF}, R_S, and C_T as follows:

$$\text{Frequency} = 1/(T_{ON} + T_{OFF})$$

$$T_{ON} = R_{ON} \cdot C_T \cdot 4.6 \text{ V} / (V_{IN} - 4.5 \text{ V})$$

$$T_{OFF} (\text{max}) = 1.4 \cdot R_S \cdot C_T$$

Regions 1 and 4

$$T_{OFF} = R_{OFF} \cdot C_T \cdot 3.7 \text{ V} / (V_{OUT} - 0.7 \text{ V})$$

Region 2, excluding the effects of R_S which have a minimal impact on T_{OFF}.

The above equations assume that V_{CC} equals 9V. The voltage at T_{ON} increases from approximately 2.5V to 6.5V while C_T is charging. To take this into account, V_{IN} is adjusted by 4.5V in the calculation of T_{ON}. The voltage at T_{OFF} is approximately 0.7V.

ABSOLUTE MAXIMUM RATINGS

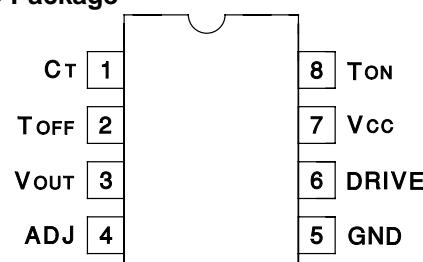
I _{CC}	8mA
Current into T _{ON} Pin.....	1.5mA
Voltage on V _{OUT} Pin.....	20V
Current into T _{OFF} Pin.....	250µA
Storage Temperature.....	-65°C to +150°C

Note: Unless otherwise indicated, voltages are referenced to ground and currents are positive into, negative out of, the specified terminals.

CONNECTION DIAGRAM

DIL-8, SOIC-8 (Top View)

N or J, D Package



ELECTRICAL CHARACTERISTICS Unless otherwise stated, these specifications hold for $T_A = 0^\circ\text{C}$ to 70°C for the UCC3888, -40°C to $+85^\circ\text{C}$ for the UCC2888, and -55°C to $+125^\circ\text{C}$ for the UCC1888. No load at DRIVE pin ($C_{LOAD}=0$).

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
General					
V _{CC} Zener Voltage	I _{CC} < 1.5mA	8.6	9.0	9.3	V
Startup Current	V _{OUT} = 0		150	250	µA
Operating Current I(V _{CC})	V _{CC} = V _{CC} (zener) – 100mV, F = 150kHz		1.2	2.5	mA
Under-Voltage-Lockout					
Start Threshold	V _{OUT} = 0	8.0	8.4	8.8	V
Minimum Operating Voltage after Start	V _{OUT} = 0	6.0	6.3	6.6	V
Hysteresis	V _{OUT} = 0	1.8			V
Oscillator					
Amplitude	V _{CC} = 9V	3.5	3.7	3.9	V
C _T to DRIVE high Propagation Delay	Overdrive = 0.2V		100	200	ns
C _T to DRIVE low Propagation Delay	Overdrive = 0.2V		50	100	ns
Driver					
V _O L	I = 20mA, V _{CC} = 9V		0.15	0.4	V
	I = 100mA, V _{CC} = 9V		0.7	1.8	V
V _O H	I = -20mA, V _{CC} = 9V	8.5	8.8		V
	I = -100mA, V _{CC} = 9V	6.1	7.8		V
Rise Time	C _{LOAD} = 1nF		35	70	ns
Fall Time	C _{LOAD} = 1nF		30	60	ns
Line Voltage Detection					
Charge Coefficient: I _{CHG} / I(T _{ON})	V _{CT} = 3V, DRIVE = High, I(T _{ON}) = 1mA	0.73	0.79	0.85	
Minimum Line Voltage for Fault	R _{ON} = 330k	60	80	100	V
Minimum Current I(T _{ON}) for Fault	R _{ON} = 330k		220		µA
On Time During Fault	C _T = 150pF, V _L INE = Min – 1V		2		µs
Oscillator Restart Delay after Fault			0.5		ms
V_{OUT} Error Amp					
V _{OUT} Regulated 5V (ADJ Open)	V _{CC} = 9V, I _{DCHG} = I(T _{OFF})/2	4.5	5.0	5.5	V
Discharge Ratio: I _{DCHG} / I(T _{OFF})	I(T _{OFF}) = 50µA	0.93	1.00	1.07	
Voltage at T _{OFF}	I(T _{OFF}) = 50µA	0.6	0.95	1.3	V
Regulation gm (Note 1)	Max I _{DCHG} = 50µA		2.4		mA/V
	Max I _{DCHG} = 125µA	1.9	4.1	7.0	mA/V

Note 1: gm is defined as $\frac{\Delta I_{DCHG}}{\Delta V_{OUT}}$ for the values of V_{OUT} when V_{OUT} is in regulation. The two points used to calculate gm are for I_{DCHG} at 65% and 35% of its maximum value.

PIN DESCRIPTIONS

ADJ: The ADJ pin is used to provide a 5V regulated supply without additional external components. Other output voltages can be obtained by connecting a resistor divider between V_{OUT}, ADJ and GND. Use the formula

$$V_{\text{OUT}} = 2.5V \cdot \frac{R1 + R2}{R2}$$

where $R1$ is connected between $VOUT$ and ADJ , and $R2$ is connected between ADJ and GND . $R1 \parallel R2$ should be less than $1k\Omega$ to minimize the effect of the temperature coefficient of the internal $30k$ resistors which also connect to $VOUT$, ADJ , and GND . See Block Diagram.

CT (timing capacitor): The signal voltage at CT has a peak-to-peak swing of 3.7V for 9V VCC. As the voltage at CT crosses the oscillator upper threshold, DRIVE goes low. As the voltage on CT crosses the oscillator lower threshold, DRIVE goes high.

DRIVE: This output is a CMOS stage capable of sinking 200mA peak and sourcing 150mA peak. The output voltage swing is 0 to Vcc.

GND (chip ground): All voltages are measured with respect to GND.

TOFF (regulated output control): TOFF sets the discharge current of the timing capacitor through an external resistor connected between VOUT and TOFF.

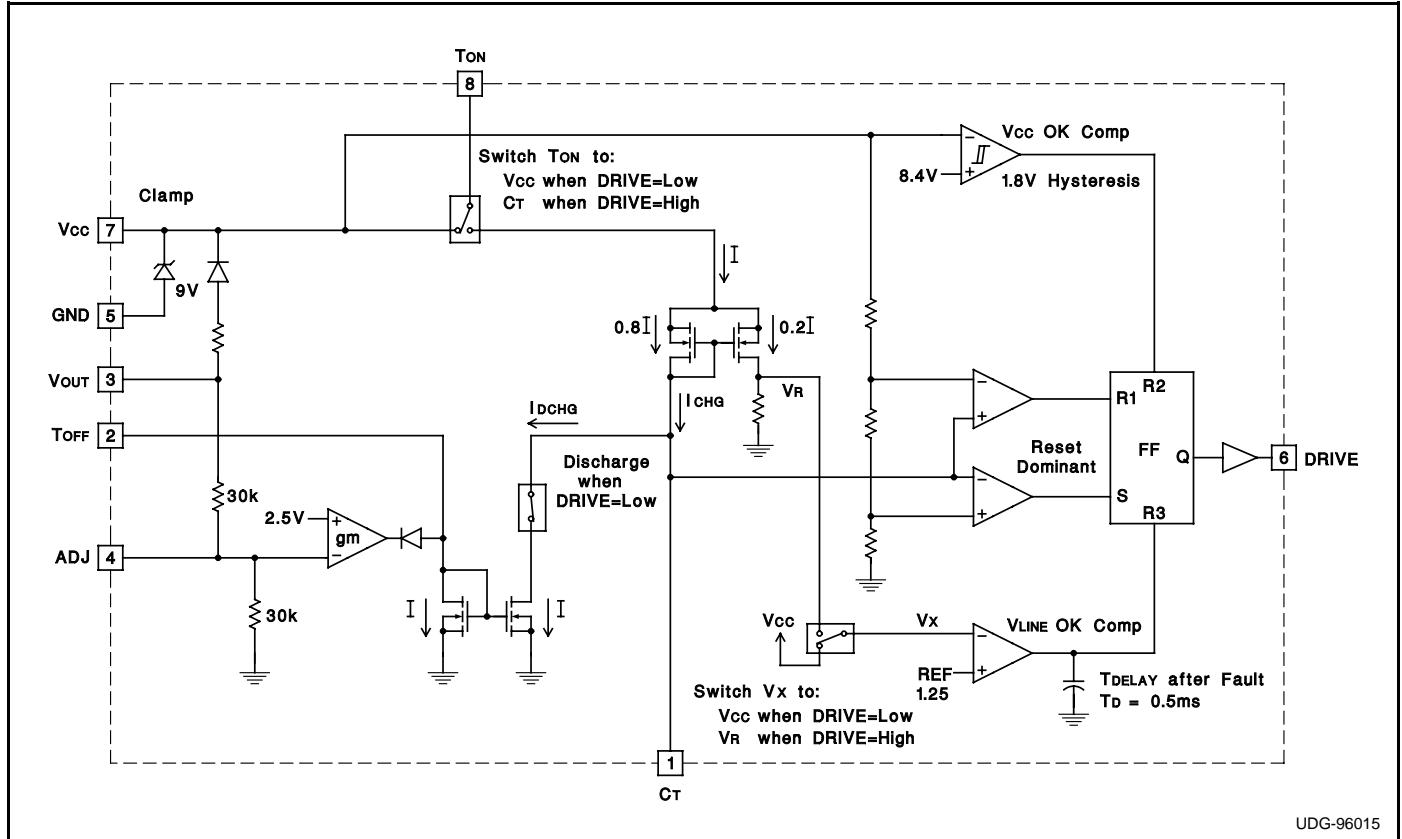
TON (line voltage control): TON serves three functions. When CT is discharging (off time), the current through TON is routed to Vcc. When CT is charging (on time), the current through TON is split 80% to set the CT charge time and 20% to sense minimum line voltage which occurs for a TON current of $220\mu\text{A}$. For a minimum line voltage of 80V , RON is $330\text{k}\Omega$.

The CT voltage slightly affects the value of the charge current during the on time. During this time, the voltage at the TON pin increases from 2.5V to 6.5V.

Vcc (chip supply voltage): The supply voltage of the device at pin Vcc is internally clamped at 9V. The device needs an external supply, from a source such as the rectified AC line or derived from the switching circuit. Precautions must be taken to ensure that total I_{CC} does not exceed 8mA.

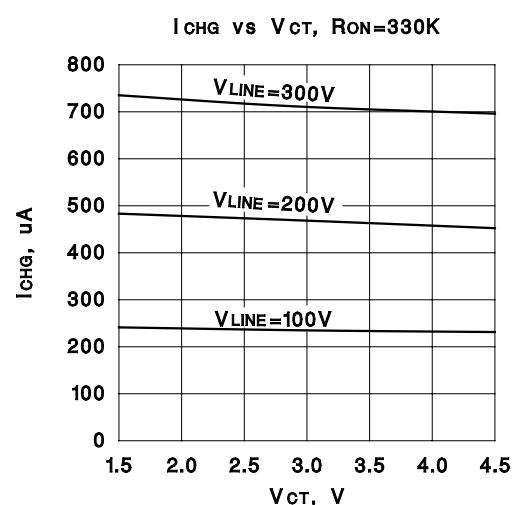
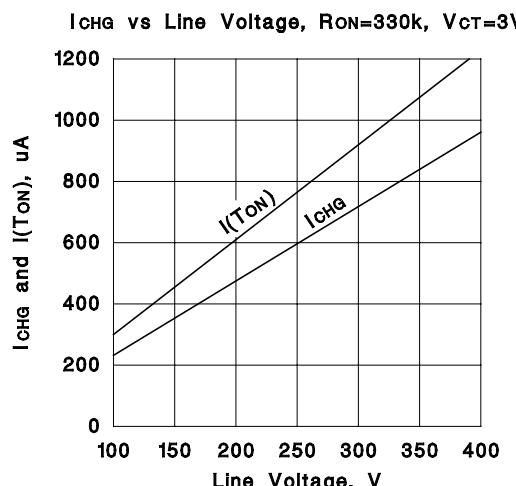
V_{OUT} (regulated output): The V_{OUT} pin is directly connected to the power supply output voltage. When V_{OUT} is greater than V_{CC}, V_{OUT} bootstraps V_{CC}.

BLOCK DIAGRAM



UCC1888
UCC2888
UCC3888

TYPICAL CHARACTERISTICS CURVES



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
UCC2888D	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC2888DG4	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC3888D	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC3888DG4	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC3888DTR	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC3888DTRG4	ACTIVE	SOIC	D	8		Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
UCC3888N	ACTIVE	PDIP	P	8		Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type
UCC3888NG4	ACTIVE	PDIP	P	8		Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

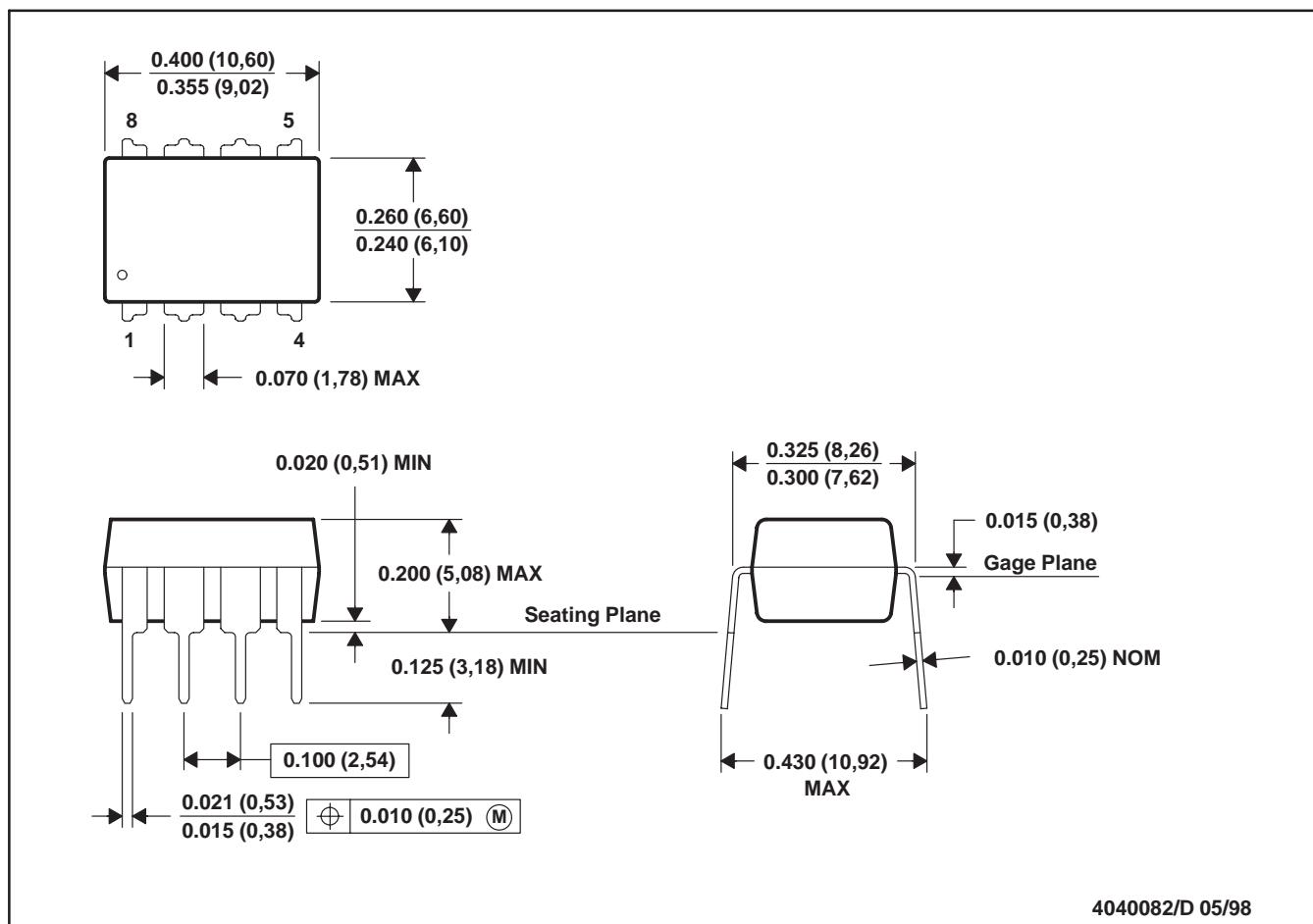
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



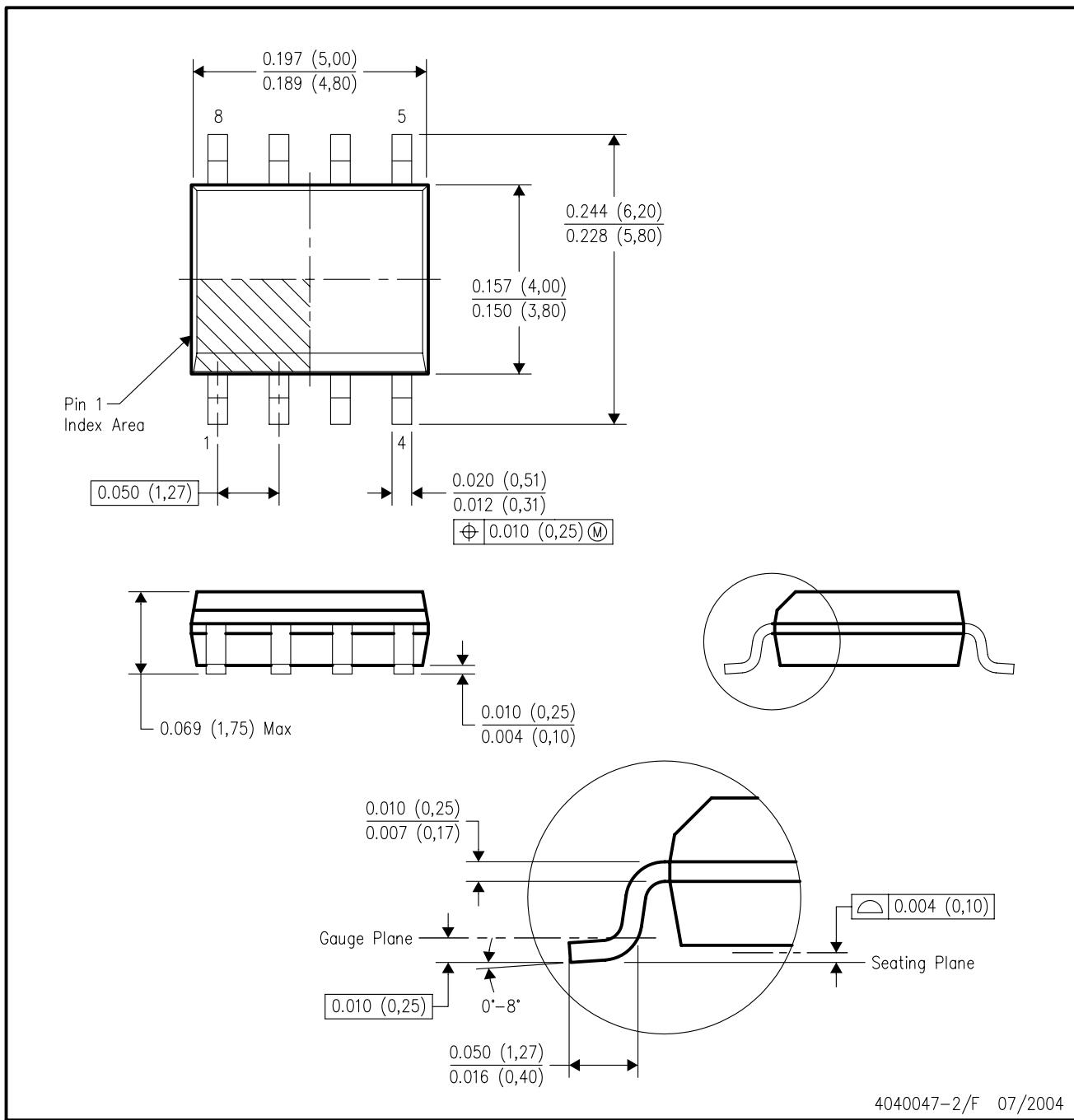
4040082/D 05/98

NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001

For the latest package information, go to http://www.ti.com/sc/docs/package/pkg_info.htm

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



4040047-2/F 07/2004

NOTES:

- All linear dimensions are in inches (millimeters).
- This drawing is subject to change without notice.
- Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0.15).
- Falls within JEDEC MS-012 variation AA.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DSP	dsp.ti.com	Broadband	www.ti.com/broadband
Interface	interface.ti.com	Digital Control	www.ti.com/digitalcontrol
Logic	logic.ti.com	Military	www.ti.com/military
Power Mgmt	power.ti.com	Optical Networking	www.ti.com/opticalnetwork
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
		Telephony	www.ti.com/telephony
		Video & Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless

Mailing Address: Texas Instruments
Post Office Box 655303 Dallas, Texas 75265

Copyright © 2006, Texas Instruments Incorporated