

MMBZ5V6ALT1 Series

Preferred Devices

24 and 40 Watt Peak Power Zener Transient Voltage Suppressors

SOT-23 Dual Common Anode Zeners for ESD Protection

These dual monolithic silicon zener diodes are designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment and other applications. Their dual junction common anode design protects two separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Specification Features:

- SOT-23 Package Allows Either Two Separate Unidirectional Configurations or a Single Bidirectional Configuration
- Working Peak Reverse Voltage Range – 3 V to 26 V
- Standard Zener Breakdown Voltage Range – 5.6 V to 33 V
- Peak Power – 24 or 40 Watts @ 1.0 ms (Unidirectional), per Figure 5. Waveform
- ESD Rating of Class N (exceeding 16 kV) per the Human Body Model
- Maximum Clamping Voltage @ Peak Pulse Current
- Low Leakage < 5.0 μ A
- Flammability Rating UL 94V-O

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic case

FINISH: Corrosion resistant finish, easily solderable

MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:
260°C for 10 Seconds

Package designed for optimal automated board assembly
Small package size for high density applications
Available in 8 mm Tape and Reel

Use the Device Number to order the 7 inch/3,000 unit reel.

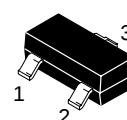
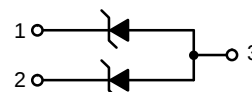
Replace the “T1” with “T3” in the Device Number to order the 13 inch/10,000 unit reel.



ON Semiconductor™

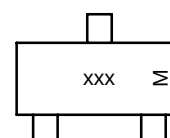
<http://onsemi.com>

PIN 1. CATHODE
2. CATHODE
3. ANODE



SOT-23
CASE 318
STYLE 12

MARKING DIAGRAM



xxx = Device Code
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping
MMBZ5V6ALT1	SOT-23	3000/Tape & Reel
MMBZ6V2ALT1	SOT-23	3000/Tape & Reel
MMBZ6V8ALT1	SOT-23	3000/Tape & Reel
MMBZ9V1ALT1	SOT-23	3000/Tape & Reel
MMBZ10VALT1	SOT-23	3000/Tape & Reel
MMBZ12VALT1	SOT-23	3000/Tape & Reel
MMBZ15VALT1	SOT-23	3000/Tape & Reel
MMBZ18VALT1	SOT-23	3000/Tape & Reel
MMBZ20VALT1	SOT-23	3000/Tape & Reel
MMBZ27VALT1	SOT-23	3000/Tape & Reel
MMBZ33VALT1	SOT-23	3000/Tape & Reel

Preferred devices are recommended choices for future use and best overall value.

DEVICE MARKING INFORMATION

See specific marking information in the device marking column of the table on page 3 of this data sheet.

MMBZ5V6ALT1 Series

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation @ 1.0 ms (Note 1.) MMBZ5V6ALT1 thru MMBZ10VALT1 @ $T_L \leq 25^\circ\text{C}$ MMBZ12VALT1 thru MMBZ33VALT1	P_{pk}	24 40	Watts
Total Power Dissipation on FR-5 Board (Note 2.) @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Total Power Dissipation on Alumina Substrate (Note 3.) @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C/W}$
Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to +150	$^\circ\text{C}$
Lead Solder Temperature – Maximum (10 Second Duration)	T_L	260	$^\circ\text{C}$

1. Non-repetitive current pulse per Figure 5. and derate above $T_A = 25^\circ\text{C}$ per Figure 6.
2. FR-5 = 1.0 x 0.75 x 0.62 in.
3. Alumina = 0.4 x 0.3 x 0.024 in., 99.5% alumina

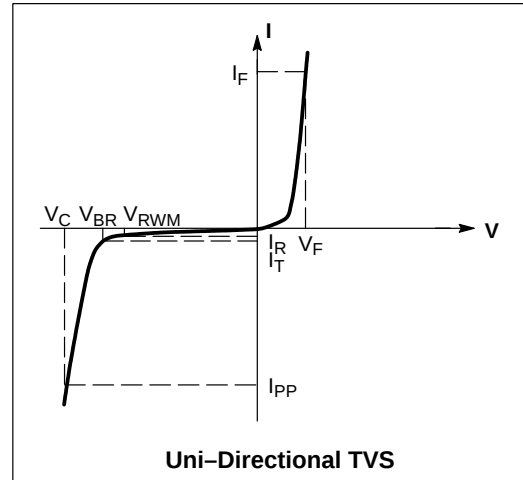
*Other voltages may be available upon request

ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

UNIDIRECTIONAL (Circuit tied to Pins 1 and 3 or 2 and 3)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
ΘV_{BR}	Maximum Temperature Coefficient of V_{BR}
I_F	Forward Current
V_F	Forward Voltage @ I_F
Z_{ZT}	Maximum Zener Impedance @ I_{ZT}
I_{ZK}	Reverse Current
Z_{ZK}	Maximum Zener Impedance @ I_{ZK}



MMBZ5V6ALT1 Series

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

UNIDIRECTIONAL (Circuit tied to Pins 1 and 3 or Pins 2 and 3)

($V_F = 0.9\text{ V Max @ } I_F = 10\text{ mA}$)

24 WATTS

Device	Device Marking	V _{RWM}	I _R @ V _{RWM}	Breakdown Voltage				Max Zener Impedance (Note 5.)			V _C @ I _{PP} (Note 6.)		ΘV _{BR}
				V _{BR} (Note 4.) (V)			@ I _T	Z _{ZT} @ I _{ZT}	Z _{ZK} @ I _{ZK}	V _C	I _{PP}		
		Volts	μA	Min	Nom	Max	mA	Ω	Ω	mA	V	A	mV/°C
MMBZ5V6ALT1	5A6	3.0	5.0	5.32	5.6	5.88	20	11	1600	0.25	8.0	3.0	1.26
MMBZ6V2ALT1	6A2	3.0	0.5	5.89	6.2	6.51	1.0	—	—	—	8.7	2.76	2.80

($V_F = 1.1\text{ V Max @ } I_F = 200\text{ mA}$)

Device	Device Marking	V _{RWM}	I _R @ V _{RWM}	Breakdown Voltage				V _C @ I _{PP} (Note 6.)		θV _{BR}
				V _{BR} (Note 4.) (V)			@ I _T	V _C	I _{PP}	
		Volts	μA	Min	Nom	Max	mA	V	A	mV/°C
MMBZ6V8ALT1	6A8	4.5	0.5	6.46	6.8	7.14	1.0	9.6	2.5	3.4
MMBZ9V1ALT1	9A1	6.0	0.3	8.65	9.1	9.56	1.0	14	1.7	7.5
MMBZ10VALT1	10A	6.5	0.3	9.50	10	10.5	1.0	14.2	1.7	7.5

($V_F = 1.1\text{ V Max @ } I_F = 200\text{ mA}$)

40 WATTS

Device	Device Marking	V _{RWM}	I _R @ V _{RWM}	Breakdown Voltage				V _C @ I _{PP} (Note 6.)		θV _{BR}
				V _{BR} (Note 4.) (V)			@ I _T	V _C	I _{PP}	
		Volts	nA	Min	Nom	Max	mA	V	A	mV/°C
MMBZ12VALT1	12A	8.5	200	11.40	12	12.60	1.0	17	2.35	7.5
MMBZ15VALT1	15A	12	50	14.25	15	15.75	1.0	21	1.9	12.3
MMBZ18VALT1	18A	14.5	50	17.10	18	18.90	1.0	25	1.6	15.3
MMBZ20VALT1	20A	17	50	19.00	20	21.00	1.0	28	1.4	17.2
MMBZ27VALT1	27A	22	50	25.65	27	28.35	1.0	40	1.0	24.3
MMBZ33VALT1	33A	26	50	31.35	33	34.65	1.0	46	0.87	30.4

4. V_{BR} measured at pulse test current I_T at an ambient temperature of 25°C .

5. Z_{ZT} and Z_{ZK} are measured by dividing the AC voltage drop across the device by the AC current applied. The specified limits are for $I_{Z(AC)} = 0.1 I_{Z(DC)}$, with the AC frequency = 1.0 kHz.

6. Surge current waveform per Figure 5. and derate per Figure 6.

MMBZ5V6ALT1 Series

TYPICAL CHARACTERISTICS

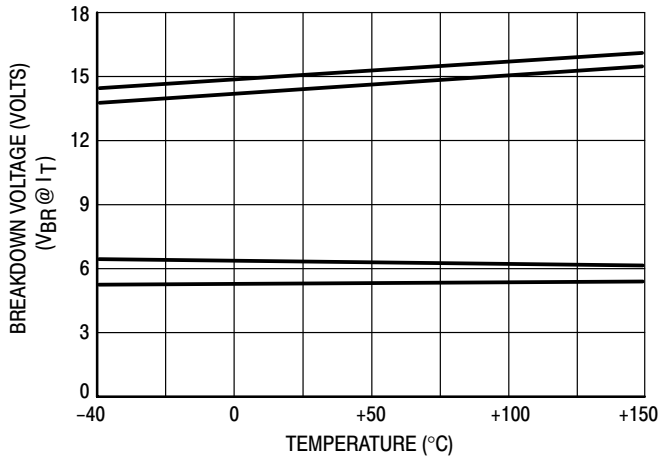


Figure 1. Typical Breakdown Voltage versus Temperature

(Upper curve for each voltage is bidirectional mode, lower curve is unidirectional mode)

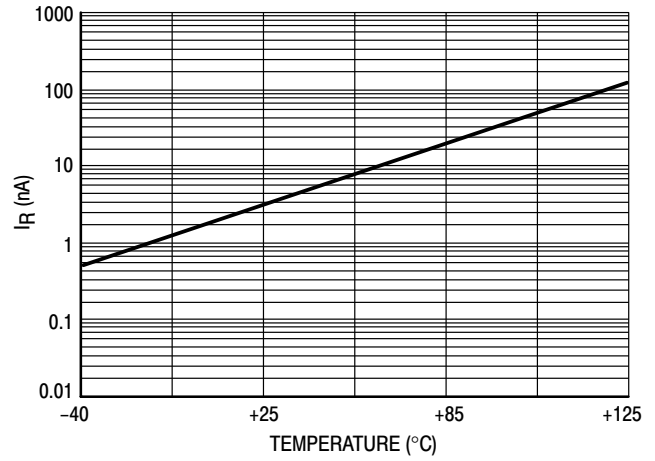


Figure 2. Typical Leakage Current versus Temperature

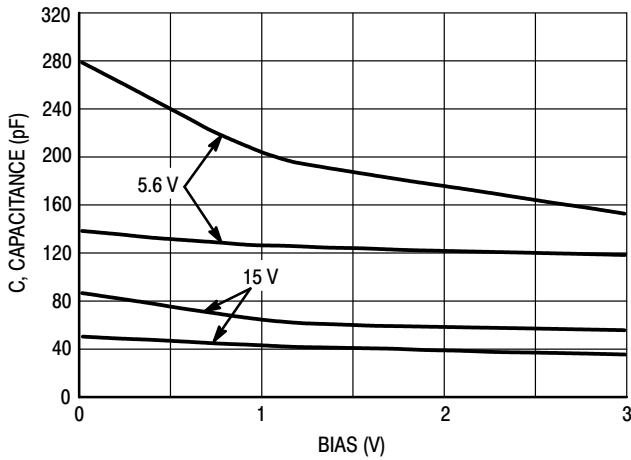


Figure 3. Typical Capacitance versus Bias Voltage

(Upper curve for each voltage is unidirectional mode, lower curve is bidirectional mode)

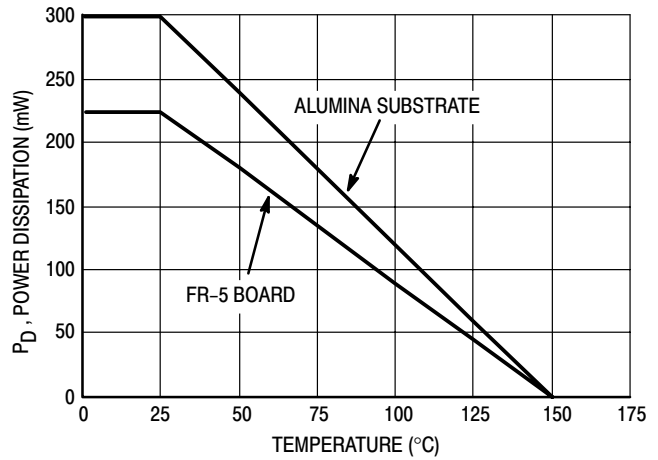


Figure 4. Steady State Power Derating Curve

MMBZ5V6ALT1 Series

TYPICAL CHARACTERISTICS

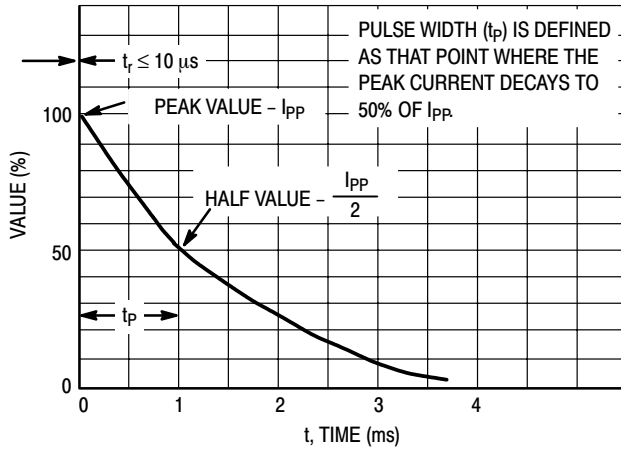


Figure 5. Pulse Waveform

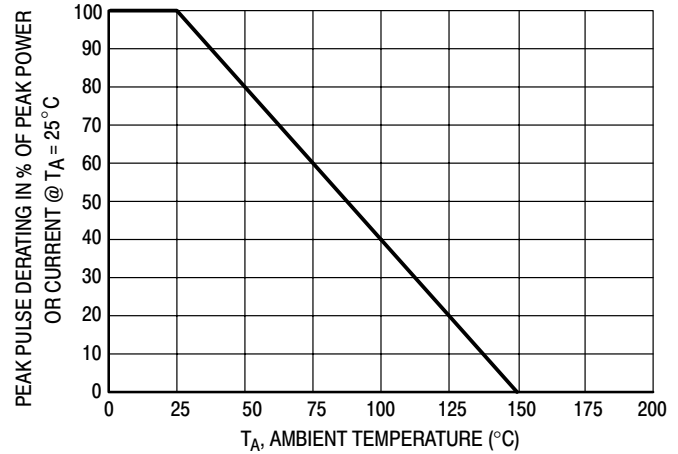


Figure 6. Pulse Derating Curve

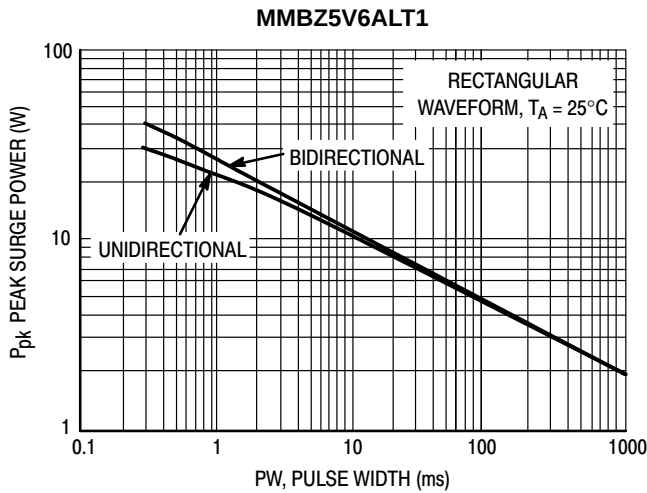


Figure 7. Maximum Non-repetitive Surge Power, P_{pk} versus PW

Power is defined as $V_{RSM} \times I_Z(pk)$ where V_{RSM} is the clamping voltage at $I_Z(pk)$.

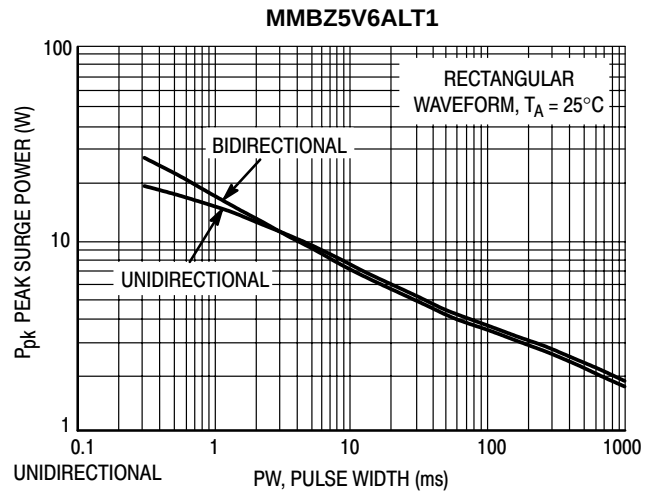


Figure 8. Maximum Non-repetitive Surge Power, $P_{pk(NOM)}$ versus PW

Power is defined as $V_Z(NOM) \times I_Z(pk)$ where $V_Z(NOM)$ is the nominal zener voltage measured at the low test current used for voltage classification.

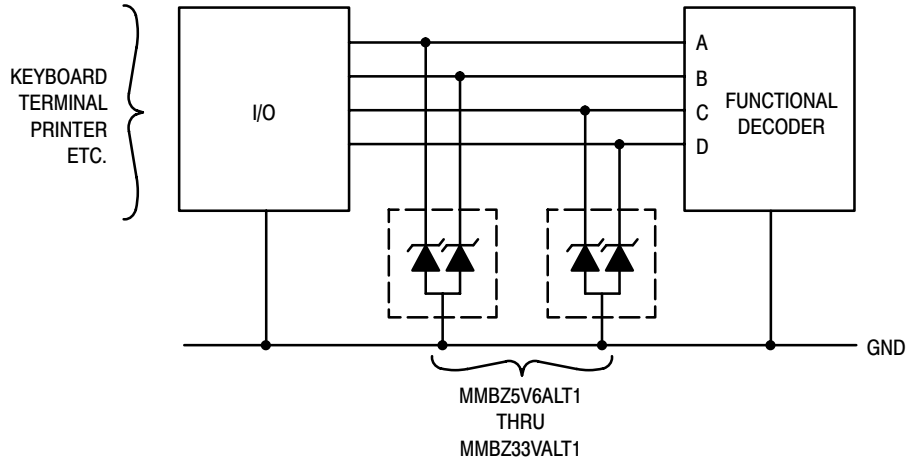
MMBZ5V6ALT1 Series

TYPICAL COMMON ANODE APPLICATIONS

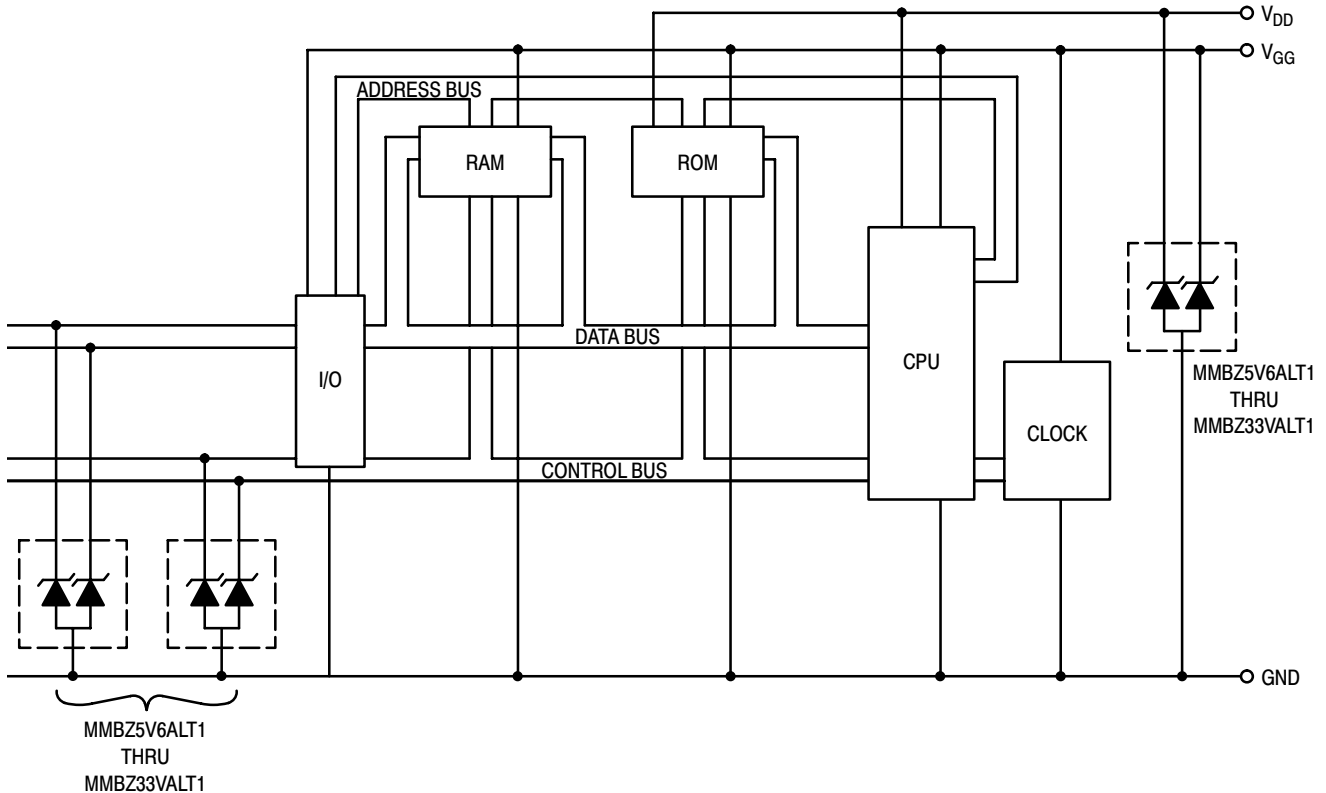
A quad junction common anode design in a SOT-23 package protects four separate lines using only one package. This adds flexibility and creativity to PCB design especially

when board space is at a premium. Two simplified examples of TVS applications are illustrated below.

Computer Interface Protection



Microprocessor Protection

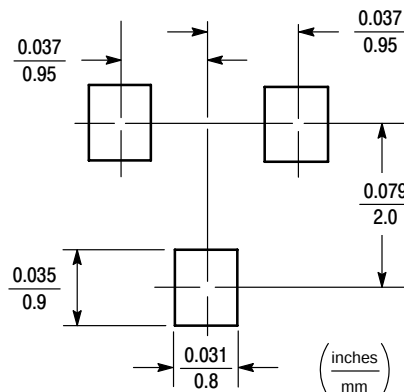


INFORMATION FOR USING THE SOT-23 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection

interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



SOT-23

SOT-23 POWER DISSIPATION

The power dissipation of the SOT-23 is a function of the drain pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_{J(max)}$, the maximum rated junction temperature of the die, $R_{\theta JA}$, the thermal resistance from the device junction to ambient, and the operating temperature, T_A . Using the values provided on the data sheet for the SOT-23 package, P_D can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature T_A of 25°C, one can calculate the power dissipation of the device which in this case is 225 milliwatts.

$$P_D = \frac{150^\circ\text{C} - 25^\circ\text{C}}{556^\circ\text{C/W}} = 225 \text{ milliwatts}$$

The 556°C/W for the SOT-23 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 225 milliwatts. There are other alternatives to achieving higher power dissipation from the SOT-23 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad™. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

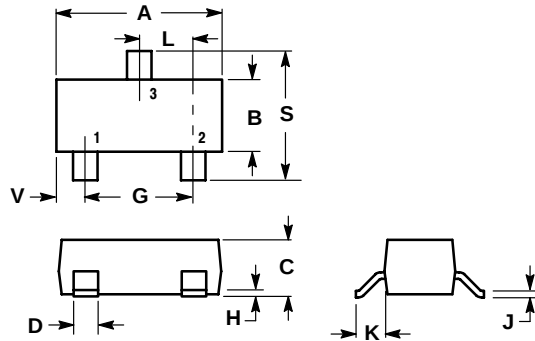
- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.

* Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

Transient Voltage Suppressors – Surface Mount

24 & 40 Watts Peak Power

SOT-23
TO-236AB
CASE 318-08
ISSUE AF



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

STYLE 12:

- PIN 1. CATHODE
2. CATHODE
3. ANODE

Thermal Clad is a trademark of the Bergquist Company

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

NORTH AMERICA Literature Fulfillment:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: ONlit@hibbertco.com
Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

EUROPE: LDC for ON Semiconductor – European Support

German Phone: (+1) 303-308-7140 (Mon-Fri 2:30pm to 7:00pm CET)
Email: ONlit-german@hibbertco.com
French Phone: (+1) 303-308-7141 (Mon-Fri 2:00pm to 7:00pm CET)
Email: ONlit-french@hibbertco.com
English Phone: (+1) 303-308-7142 (Mon-Fri 12:00pm to 5:00pm GMT)
Email: ONlit@hibbertco.com

EUROPEAN TOLL-FREE ACCESS*: 00-800-4422-3781

*Available from Germany, France, Italy, UK, Ireland

CENTRAL/SOUTH AMERICA:

Spanish Phone: 303-308-7143 (Mon-Fri 8:00am to 5:00pm MST)
Email: ONlit-spanish@hibbertco.com
Toll-Free from Mexico: Dial 01-800-288-2872 for Access –
then Dial 866-297-9322

ASIA/PACIFIC: LDC for ON Semiconductor – Asia Support

Phone: 1-303-675-2121 (Tue-Fri 9:00am to 1:00pm, Hong Kong Time)
Toll Free from Hong Kong & Singapore:
001-800-4422-3781

Email: ONlit-asia@hibbertco.com

JAPAN: ON Semiconductor, Japan Customer Focus Center

4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031
Phone: 81-3-5740-2700
Email: r14525@onsemi.com

ON Semiconductor Website: <http://onsemi.com>

For additional information, please contact your local Sales Representative.