

PROTECTION PRODUCTS

Description

The SL series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD, lightning, and other voltage-induced transient events. They are available with operating voltages of 5V, 12V, 15V and 24V.

TVS diodes are solid-state devices designed specifically for transient suppression. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage and no device degradation. The SL series devices feature a low capacitance, fast switching compensation diode in series with a standard TVS diode. This effectively reduces the overall capacitance of the device to less than 5pF making it an integrated, low capacitance solution for use on high-speed interfaces.

The SL series devices may be used to meet the immunity requirements of IEC 61000-4-2, level 4.

Features

- ◆ Transient protection for high-speed data lines to **IEC 61000-4-2 (ESD) 15kV (air), 8kV (contact)**
IEC 61000-4-4 (EFT) 40A (5/50ns)
IEC 61000-4-5 (Lightning) 12A (8/20μs)
- ◆ Small package for use in portable electronics
- ◆ Two devices will protect one line
- ◆ Low capacitance for high-speed data lines
- ◆ Working voltages: 5V, 12V, 15V and 24V
- ◆ Solid-state silicon avalanche technology

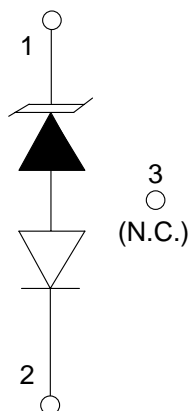
Mechanical Characteristics

- ◆ JEDEC SOT23 package
- ◆ Molding compound flammability rating: UL 94V-0
- ◆ Marking : Marking Code
- ◆ Packaging : Tape and Reel per EIA 481

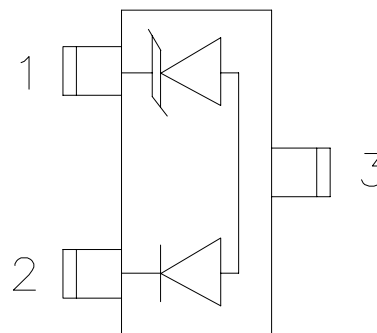
Applications

- ◆ High-Speed data lines
- ◆ Cellular handsets & accessories
- ◆ Universal Serial Bus (USB) port protection
- ◆ Portable instrumentation
- ◆ LAN/WAN equipment
- ◆ Peripherals

Circuit Diagram



Schematic & PIN Configuration



SOT23 Top View

PROTECTION PRODUCTS
Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{pk}	300	Watts
ESD Voltage (HBM per IEC 61000-4-2)	I_{pp}	>25	kV
Lead Soldering Temperature	T_L	260 (10 sec.)	°C
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics
SL05

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$	6			V
Reverse Leakage Current	I_R	$V_{RWM} = 5V, T=25^\circ C$			20	μA
Clamping Voltage	V_C	$I_{pp} = 1A,$ $t_p = 8/20\mu s$			9.8	V
Clamping Voltage	V_C	$I_{pp} = 5A,$ $t_p = 8/20\mu s$			11	V
Maximum Peak Pulse Current	I_{pp}	$t_p = 8/20\mu s$			17	A
Junction Capacitance	C_J	Pin 1 to 2 $V_R = 0V, f = 1MHz$			5	pF

SL12

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				12	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$	13.3			V
Reverse Leakage Current	I_R	$V_{RWM} = 12V, T=25^\circ C$			1	μA
Clamping Voltage	V_C	$I_{pp} = 1A,$ $t_p = 8/20\mu s$			19	V
Clamping Voltage	V_C	$I_{pp} = 5A,$ $t_p = 8/20\mu s$			24	V
Maximum Peak Pulse Current	I_{pp}	$t_p = 8/20\mu s$			12	A
Junction Capacitance	C_J	Pin 1 to 2 $V_R = 0V, f = 1MHz$			5	pF

PROTECTION PRODUCTS
Electrical Characteristics (Continued)
SL15

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				15	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1\text{mA}$	16.7			V
Reverse Leakage Current	I_R	$V_{RWM} = 15\text{V}$, $T=25^\circ\text{C}$			1	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$, $t_p = 8/20\mu\text{s}$			24	V
Clamping Voltage	V_C	$I_{PP} = 5\text{A}$, $t_p = 8/20\mu\text{s}$			30	V
Maximum Peak Pulse Current	I_{PP}	$t_p = 8/20\mu\text{s}$			10	A
Junction Capacitance	C_j	Pin 1 to 2 $V_R = 0\text{V}$, $f = 1\text{MHz}$			5	pF

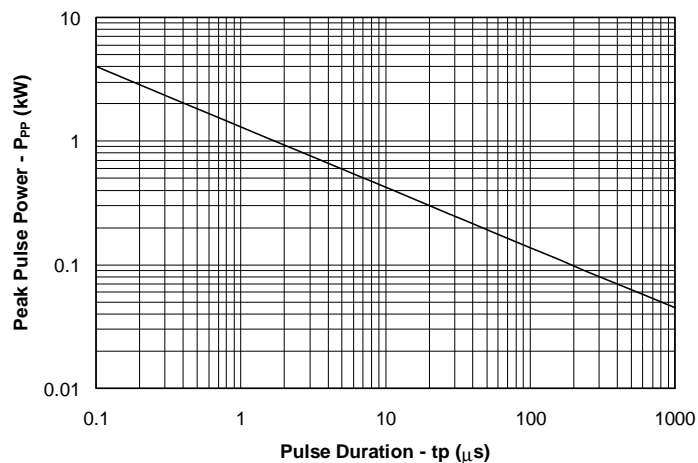
SL24

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				24	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1\text{mA}$	26.7			V
Reverse Leakage Current	I_R	$V_{RWM} = 24\text{V}$, $T=25^\circ\text{C}$			1	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$, $t_p = 8/20\mu\text{s}$			43	V
Clamping Voltage	V_C	$I_{PP} = 5\text{A}$, $t_p = 8/20\mu\text{s}$			55	V
Maximum Peak Pulse Current	I_{PP}	$t_p = 8/20\mu\text{s}$			5	A
Junction Capacitance	C_j	Pin 1 to 2 $V_R = 0\text{V}$, $f = 1\text{MHz}$			5	pF

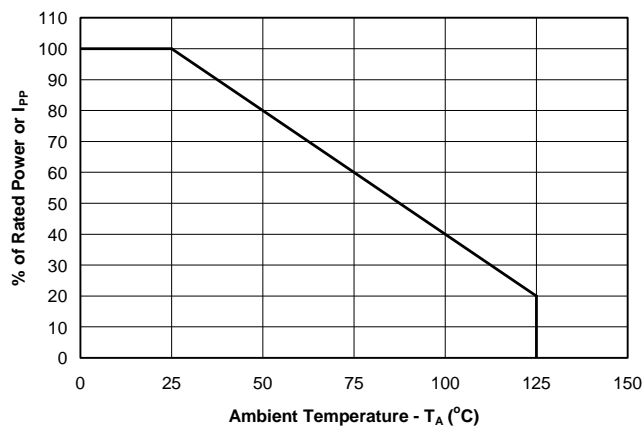
PROTECTION PRODUCTS

Typical Characteristics

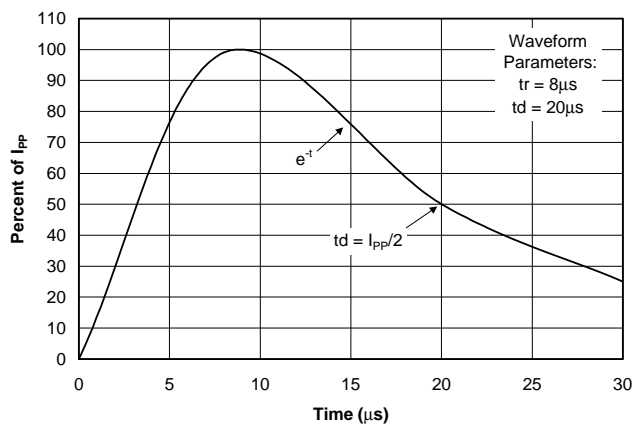
Non-Repetitive Peak Pulse Power vs. Pulse Time



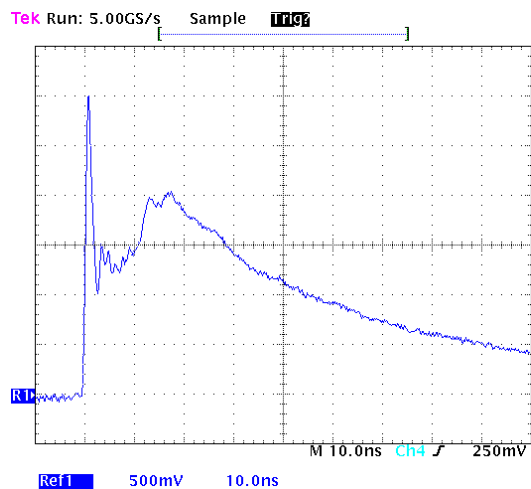
Power Derating Curve



Pulse Waveform



ESD Pulse Waveform (Per IEC 61000-4-2)



IEC 61000-4-2 Discharge Parameters

Level	First Peak Current (A)	Peak Current at 30 ns (A)	Peak Current at 60 ns (A)	Test Voltage (Contact Discharge) (kV)	Test Voltage (Air Discharge) (kV)
1	7.5	4	8	2	2
2	15	8	4	4	4
3	22.5	12	6	6	8
4	30	16	8	8	15

PROTECTION PRODUCTS

Applications Information

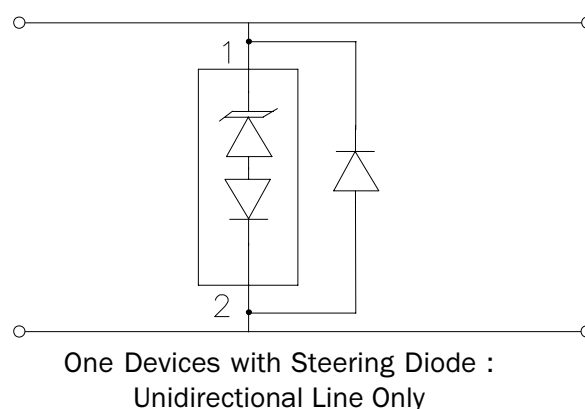
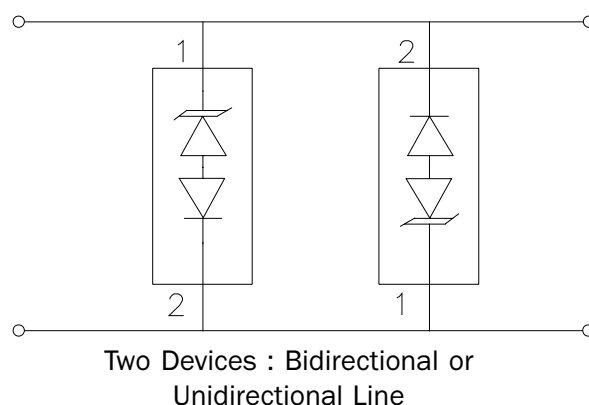
Device Connection for Protection of One High-Speed Data Line

The SL series devices are designed to protect high-speed data lines. The SLxx utilizes a low capacitance compensation diode in series with, but in opposite polarity to a TVS diode in each line to achieve an effective capacitance of less than 5pF per device. During a transient event, the internal rectifier must be forward biased (TVS is reversed biased). Therefore, each device will only suppress transient events in one polarity. To achieve protection in both positive and negative polarity, a second device is connected in anti-parallel to the first. On unidirectional lines, a fast switching steering diode may be used as an alternative to using two SL devices.

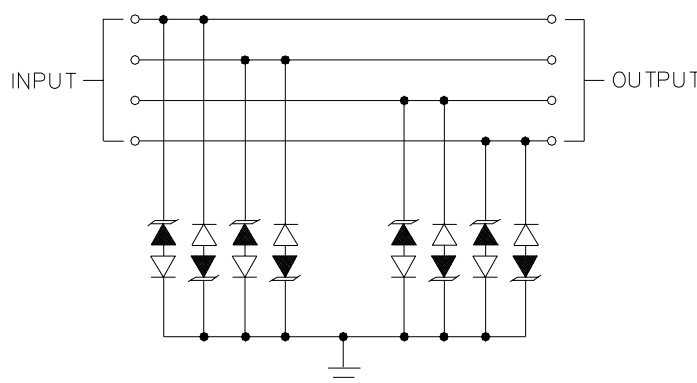
The options for connecting the devices are as follows:

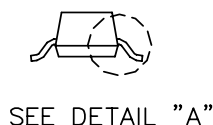
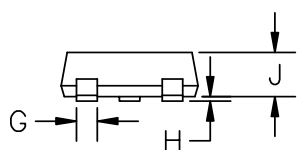
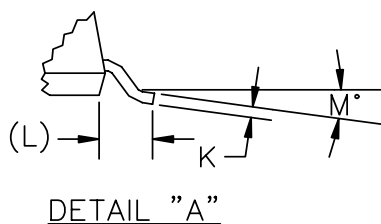
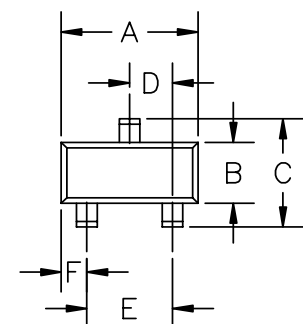
- Low capacitance protection of one high-speed line:** Protection of one unidirectional or bidirectional high-speed line is achieved by connecting two devices in anti-parallel. Pin 1 of the first device is connected to the line and pin 2 is connected to ground (or to a second line for differential protection). Pin 2 of the second device is connected to line 1 and pin 1 is connected to ground (or line 2) as shown. Pin 3 is not connected. During positive duration transients, the first device will conduct from pin 1 to 2. The steering diode conducts in the forward direction while the TVS will avalanche and conduct in the reverse direction. During negative transients, the second device will conduct in the same manner. For optimum protection, the ground connections should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces. The path length between the TVS and the protected line should also be minimized.
- Connection Option for Unidirectional Lines Only:** An optional method for protecting unidirectional (normal signal polarities above ground) lines is to add a fast switching steering diode in parallel to the SL TVS. Input/outputs are connected to pin 1 of the SL device and the cathode of the rectifier. The anode of the diode and pin 2 of the SL TVS are connected to ground.

SL Connection Options

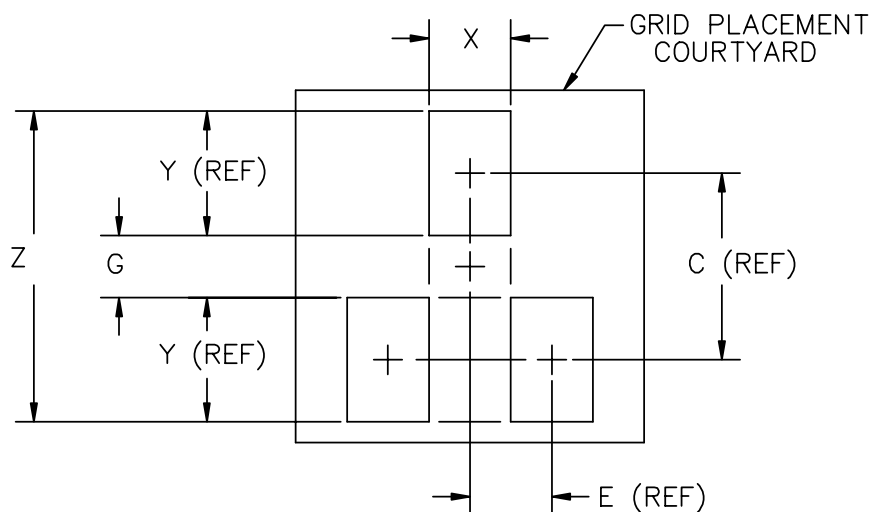


I/O Line Protection



PROTECTION PRODUCTS
Outline Drawing - SOT23


DIMENSIONS [1]					
DIM ^N	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.119	2.8	3.04	—
B	.047	.055	1.20	1.40	—
C	.083	.104	2.10	2.64	—
D	.035	.040	0.88	1.02	—
E	.070	.081	1.78	2.05	—
F	.017	.024	.44	.60	—
G	.014	.020	.37	.51	—
H	.0005	.004	.013	0.10	—
J	.034	.040	.87	1.02	—
K	.003	.007	.085	.180	—
L	—	.022	—	0.55	REF
M	0	8°	0	8°	—

Land Pattern - SOT23


DIMENSIONS					
DIM ^N	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
C	—	.09	—	2.20	—
E	—	.04	—	.95	—
G	.03	.04	.80	1.00	—
X	.03	.04	.80	1.00	—
Y	—	.06	—	1.40	—
Z	.14	.15	3.40	3.60	—

Note 1 : Grid placement courtyard is 8 x 8 elements (4mm x 4mm) in accordance with the international grid detailed in IEC Publication 97.

PROTECTION PRODUCTS**Marking Codes**

Part Number	Marking Code
SL05	L05
SL12	L12
SL15	L15
SL24	L24

Ordering Information

Part Number	Working Voltage	Qty per Reel	Reel Size
SL05.TC	5V	3,000	7 Inch
SL05.TG	5V	10,000	13 Inch
SL12.TC	12V	3,000	7 Inch
SL12.TG	12V	10,000	13 Inch
SL15.TC	15V	3,000	7 Inch
SL15.TG	15V	10,000	13 Inch
SL24.TC	24V	3,000	7 Inch
SL24.TG	24V	10,000	13 Inch

Contact Information

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