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ADC1415S065F1

ADC1415S065F1 demo board; CMOS outputs

IDT's ADC1415S065F1 demo board is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output. A data acquisition board can be used to easily analyze the ADC performance in the design and prototyping phase.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)



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Documents

	Title	Type 	Format	File Size	Date
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

Product ID	Title
ADC1415S065HN	Single 14-bit ADC 65 Msps with Input Buffer CMOS or LVDS DDR digital outputs

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