Silicon NPN Power Transistor





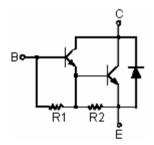
Feature:

• Darlington

Application:

For medium power linear and switching applications

Fig. 1 Simplified Outline (TO-126) and Symbol



Pinning

Pin	Description
1	Emitter
2	Collector; connected to mounting base
3	Base

Absolute Maximum Ratings ($T_a = 25$ °C)

Symbol	Parameter	Conditions	Value	Unit
V_{CBO}	Collector - base voltage	Open emitter	100	V
V _{CEO}	Collector - emitter voltage	Open base	100	V
V _{EBO}	Emitter - base voltage	Open collector	5	V
I _C	Collector current	-	4	А
I _{CM}	Collector current - peak	-	6	А
Ι _Β	Base current	-	0.1	А
P _C	Collector power dissipation	T _C = 25°C	40	W
Tj	Junction temperature	-	150	°C
T _{stg}	Storage temperature	-	-65 to 150	°C

Characteristics ($T_j = 25^{\circ}C$ Unless Otherwise Specified)

Symbol	Parameter	Conditions	Minimum	Typical	Maximum	Unit
V _{CEO (SUS)}	Collector - emitter sustaining voltage	I _C = 50 mA; I _B = 0	100	-	-	V
V _{CEsat}	Collector - emitter saturation voltage	I _C = 1.5 A; I _B = 30 mA	-	-	2.5	V
V _{BE (ON)}	Base - emitter voltage	I _C = 1.5 A; V _{CE} = 3 V	-	-	2.5	V
I _{CBO}	Collector cut-off current	V _{CB} = 100 V; I _E = 0	-	-	0.2	mA
I _{CEO}	Collector cut-off current	V _{CE} = 100 V; V _{BE} = 0	-	-	0.5	mA
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V; I _C = 0	-	-	2	mA
h _{FE}	DC current gain	I _C = 1.5 A; V _{CE} = 3 V	750	-	-	-

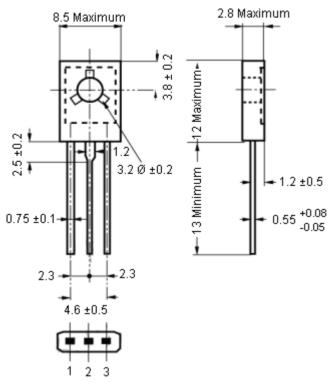
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Package Outline



Dimensions : Millimetres

Fig. 2 Outline Dimensions

Part Number Table

Description	Part Number		
Silicon NPN Darlington Power Transistor	BD681		

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